SAMSUNG ELECTRONICS

EMC TEST REPORT

Project No. : LBE031963

Product : PROJECTOR

Model No. : SP-H700AE

Date of test : Sep 5 ~ Sep 6, 2003

Issued Date : Sep 8, 2003

Tested by:
Jay Yong, PARK / Test Engineer

Reviewed by:
No Cheon, PARK / Manager of EMC Lab.

Authorized by:
Kyu Baek, CHUNG / Chief of EMC Lab.

SAMSUNG ELECTRONICS Co., Ltd.
EMC Test Laboratory
416 Maetan-3 Dong, Paldal-Ku, Suwon City, Kyungki-Do, Korea,442-742
Tel. : 81-31-200-2185 Fax. : 81-31-200-2189

Table of Contents

1. Introduction & Summary

1.1 Description of the EUT

Applicant	SAMSUNG ELECTRONICS Co., Ltd.
Project Number	LBE031963
Equipment Under Test	PROJECTOR
Trade Name	SAMSUNG
Model Number	SP-H700AE
Variant Model	-
FCC ID Number	A3LSPH700

1.2 Test facility

The EMI/EMS measurement facilities used to collect the tested data are located at 416 Maetan 3 Dong, Paldal-Ku, Suwon City, Kyungki Do, Korea.

The sites are constructed in conformance with the requirements of ANSI C63.4 and CISPR 16-1 & 16-2.
SAMSUNG Electronics Co.,Ltd is accredited by Korea Laboratory Accreditation Scheme(KOLAS) which signed the International Laboratory Accreditation Cooperation (ILAC) Mutual Recognition Arrangement (MRA) for the above test item(s) and test method(s).

Measured in Semi-anechoic chamber #1 that is FCC Registration Number 98856.

1.3 Test equipment

Equipment	Model No.	Serial No.	Makers	Last calibration and Interval
Spectrum analyzer	E7405A	MY42000109	Agilent	03/04/18, 12Months
	Firmware versions : Rev.29.9.86			
Quasi-peak adapter	85650A	2521A00687	H.P	02/10/09, 12Months
10dB Attenuator	8491B	58226	Agilent	37537
RF Relais Matrix	PSU	861206/024	R & S	N/A
Measurement Software	EP5CE	-	TOYO	N/A
RF Selector	NS4900	0303-015	TOYO	N/A
Measurement Software	EP5RE	-	TOYO	N/A
Field strength meter	ESCS30	839809/022	R & S	02/06/27, 12Months
	Firmware versions : Main 1.08, OTP 02.01, GRA 02.03			
Field strength meter	ESCS30	100104	R & S	03/01/17, 12Months
L.I.S.N	ESH3-Z5	847265/028	R & S	02/10/09, 12Months
Bi-Log Antenna	CBL6112B	2766	SCHAFFNER	03/06/20, 12Months
EMC Analyzer	E7405A	US41110272	Agilent	2003-06-14

2. Test Set-up

2.1 Test mode

The EUT was tested in the following operating modes for the tests mention in this report:

Description of Testing operating mode & Tested Resolution

Operating Mode	Resolutions	Refresh rates	Colors
-	1280x720	Horizontal F.: 70kHz	-
		Vertical F. : 85Hz	

Measured each about 2 input(PC VIDEO INPUT mode & Digital Video Interface)of EUT.
Further details of cabling and configuration are shown in the test system configuration.
The EUT exercise program used during radiated and conducted testing was designed
to exercise the various system components in a manner similar to typical use.

2.2 Justification

The system was configured for testing in typical fashion use. Cable were attached to each of the available I/O Ports.
The mode of operation utilized for testing was selected to best simulate typical EUT use.

2.3 Test equipment setup

The explanation of measuring equipment setup when respective function is used in any frequency band is as following:

Frequency Band [MHz]	Equipment	Detector function	Resolution Bandwidth	Video Bandwidth
0.15 to 30	EMI Test receiver	Quasi-Peak	9kHz	-
30 to 1000	Spectrum analyzer	Peak	100kHz	1MHz
	EMI Test receiver	Quasi-Peak	120kHz	-
Above 1000	EMI Test receiver	Peak	1MHz	1MHz

2.4 Tested System Details

1) Configuration of EUT and peripherals

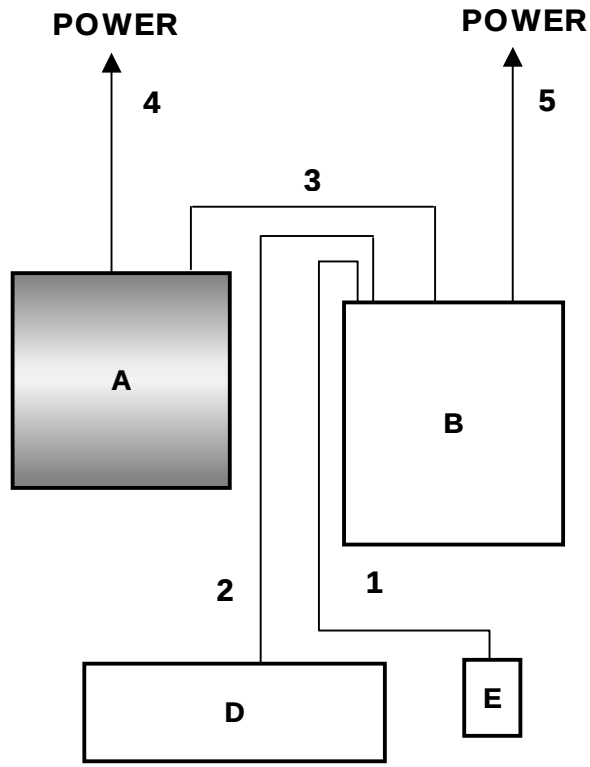
Mark	Item	Model No.	Serial No.	Manufacturer	FCC ID
A	PROJECTOR	SP-H700AE	-	Samsung	A3LSPH700
B	Personal Computer	M6200	450092DN101415	Samsung	
C	PS/2 Keyboard	SEM-A17R	04MB-0000229	Samsung	
D	PS/2 Mouse	M-U48a	LZE12153242	Logitech	

* DOC : FCC Declaration of Conformity

2) Used Cable Description

No.	Item	Length[m]	Shielded(Y/N)	Remark
1	PS/2 Mouse Cable	1.8	N	
2	PS/2 Keyboard Cable	1.6	N	
3	Video Cable	1.5	Y	
4	AC Power Cable(Projector)	1.2	N	
5	AC Power Cable(PC)	1.8	N	

2.5 System Block Diagram of Test Configuration



2.6 Test rule and Procedure

FCC Rule Part 15, Subpart B : Unintentional Radiators
Test Procedure : ANSI C63.4-1992

2.7 Test Summary

Test item	Test Procedure	Result
AC POWERLINE CONDUCTED EMISSION	ANSI C63.4-1992	Pass
RADIATED EMISSION	ANSI C63.4-1992	Pass

* N/A : Test not applicable

3. Test Results

3.1 AC POWERLINE CONDUCTED EMISSION MEASUREMENT

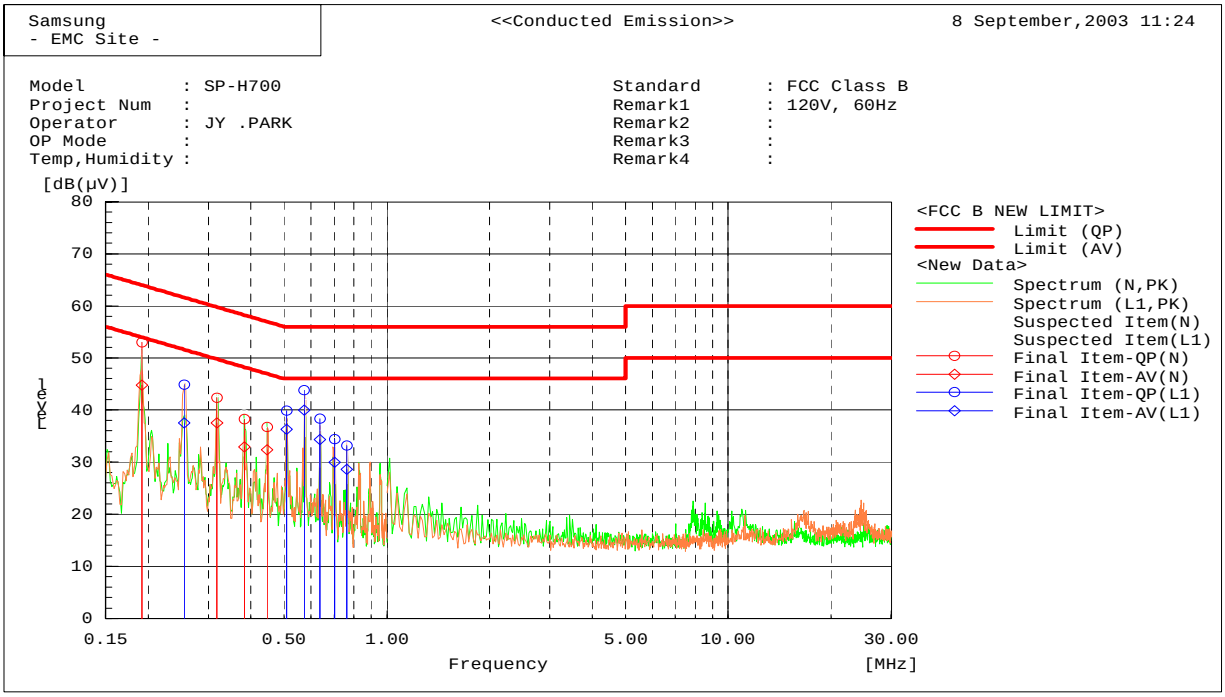
3.1.1 Test Procedure

Configurate the EUT System in accordance with ANSI C63.4-1992 section 7 and 12.2.
Connect the EUT's AC line cord to the EUT port of LISN.
All input terminals are terminated in the proper impedance.
The output ports are connected to the cable provided with the device and the ending port are terminated in the proper impedance.

Using a calibrated coaxial cable, the TEST RECEIVER is connected to the measuring port of the LISN for EUT. To the find out an EUT condition procedures the maximum emission, the position of cables, EUT operations mode are the position of cables, EUT operations mode are checked under normal usage of EUT.
Then, the emission are scanned from 0.15MHz to 30MHz relative to the limit are recorded.

3.1.2 Test Results

O Test Data (LISN Mode : Live & Neutral)



Final Result

--- N Phase ---

No.	Frequency [MHz]	Reading QP [dB(µV)]	Reading AV [dB(µV)]	c.f [dB]	Result QP [dB(µV)]	Result AV [dB(µV)]	Limit QP [dB(µV)]	Limit AV [dB(µV)]	Margin QP [dB]	Margin AV [dB]
1	0.19149	42.8	34.6	10.2	53.0	44.8	64.0	54.0	11.0	9.2
2	0.31832	32.1	27.3	10.3	42.4	37.6	59.8	49.8	17.4	12.2
3	0.38198	27.9	22.6	10.3	38.2	32.9	58.2	48.2	20.0	15.3
4	0.4457	26.5	22.0	10.3	36.8	32.3	57.0	47.0	20.2	14.7

--- L1 Phase ---

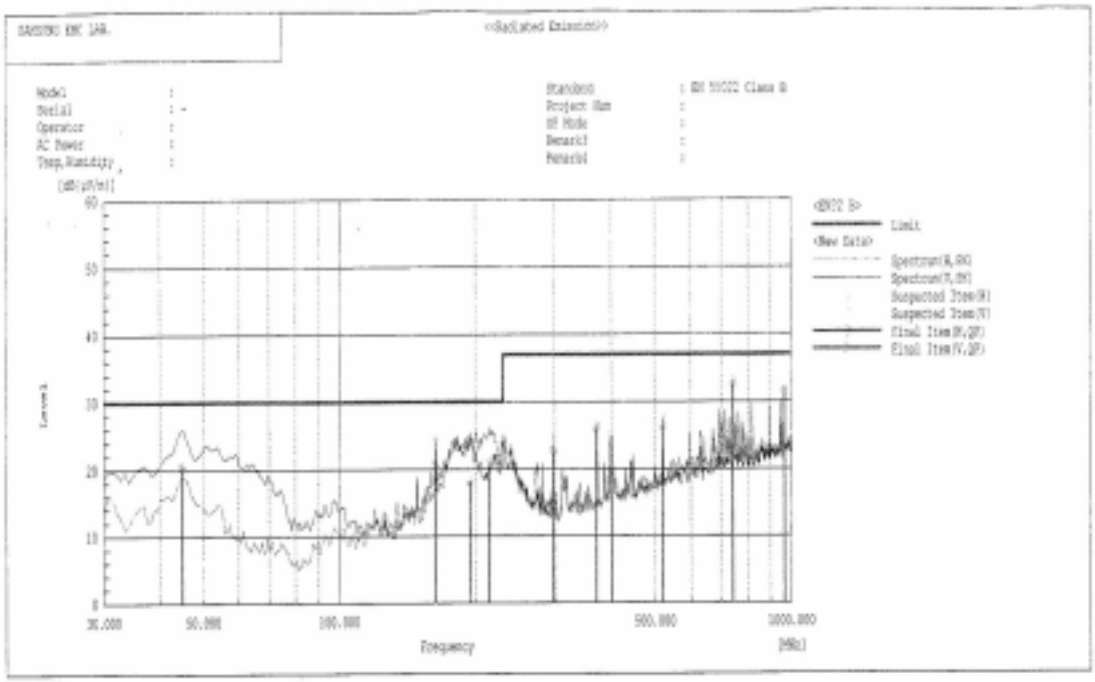
No.	Frequency [MHz]	Reading QP [dB(µV)]	Reading AV [dB(µV)]	c.f [dB]	Result QP [dB(µV)]	Result AV [dB(µV)]	Limit QP [dB(µV)]	Limit AV [dB(µV)]	Margin QP [dB]	Margin AV [dB]
1	0.25475	34.6	27.3	10.3	44.9	37.6	61.6	51.6	16.7	14.0
2	0.50939	29.6	26.0	10.3	39.9	36.3	56.0	46.0	16.1	9.7
3	0.57307	33.5	29.7	10.3	43.8	40.0	56.0	46.0	12.2	6.0
4	0.63688	28.1	24.0	10.3	38.4	34.3	56.0	46.0	17.6	11.7
5	0.70288	24.1	19.7	10.3	34.4	30.0	56.0	46.0	21.6	16.0
6	0.76414	22.9	18.3	10.3	33.2	28.6	56.0	46.0	22.8	17.4

3.2 RADIATED EMISSION MEASUREMENT

3.2.1 Test Procedure

Configurate the EUT System in accordance with ANSI C63.4-1992section 8 and 12.2.
Power cords for the EUT System are connected the receptacle on the ground plane.
The output ports are connected to the cable provided with the device and the ending port of the cable are terminated in the proper impedance.
To find out the maximum emission, change the position of the cable, and the EUT operation mode under normal usage of the EUT.
The spectrum analyzer are scanned from 30MHz to 1,000MHz.
And, detecting waves mode is peak mode, Graph's result in worst arrangement state of EUT. Spectrum analyzer result did horizontal and vertical polarization maxhold.

3.2.2 Radiated Emission Test Data

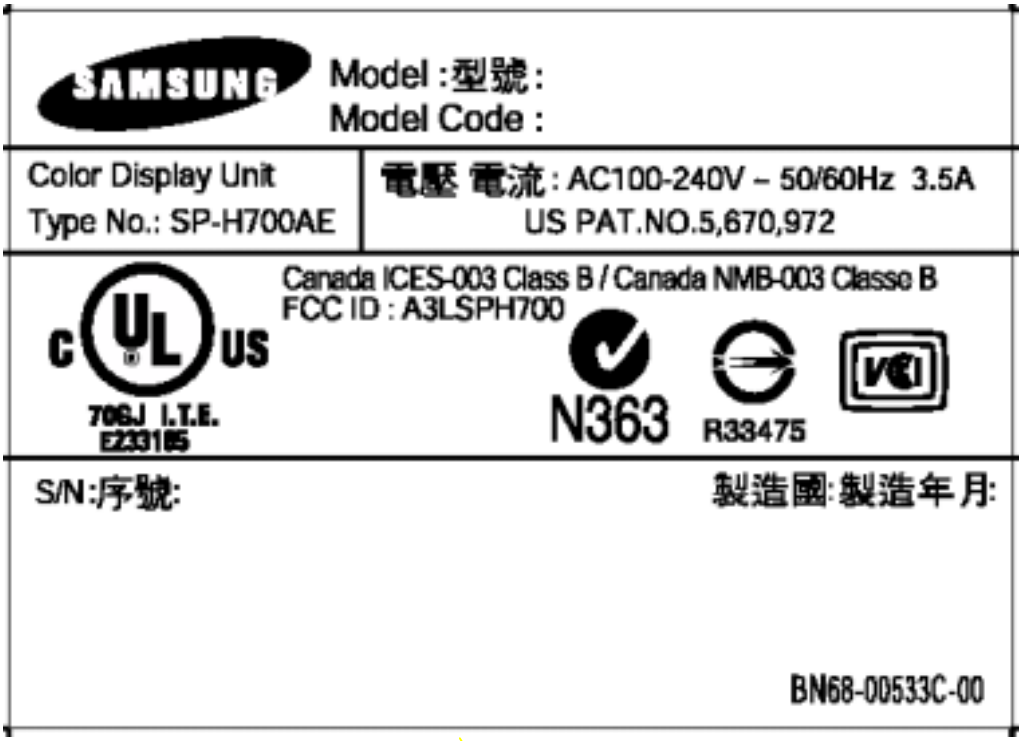


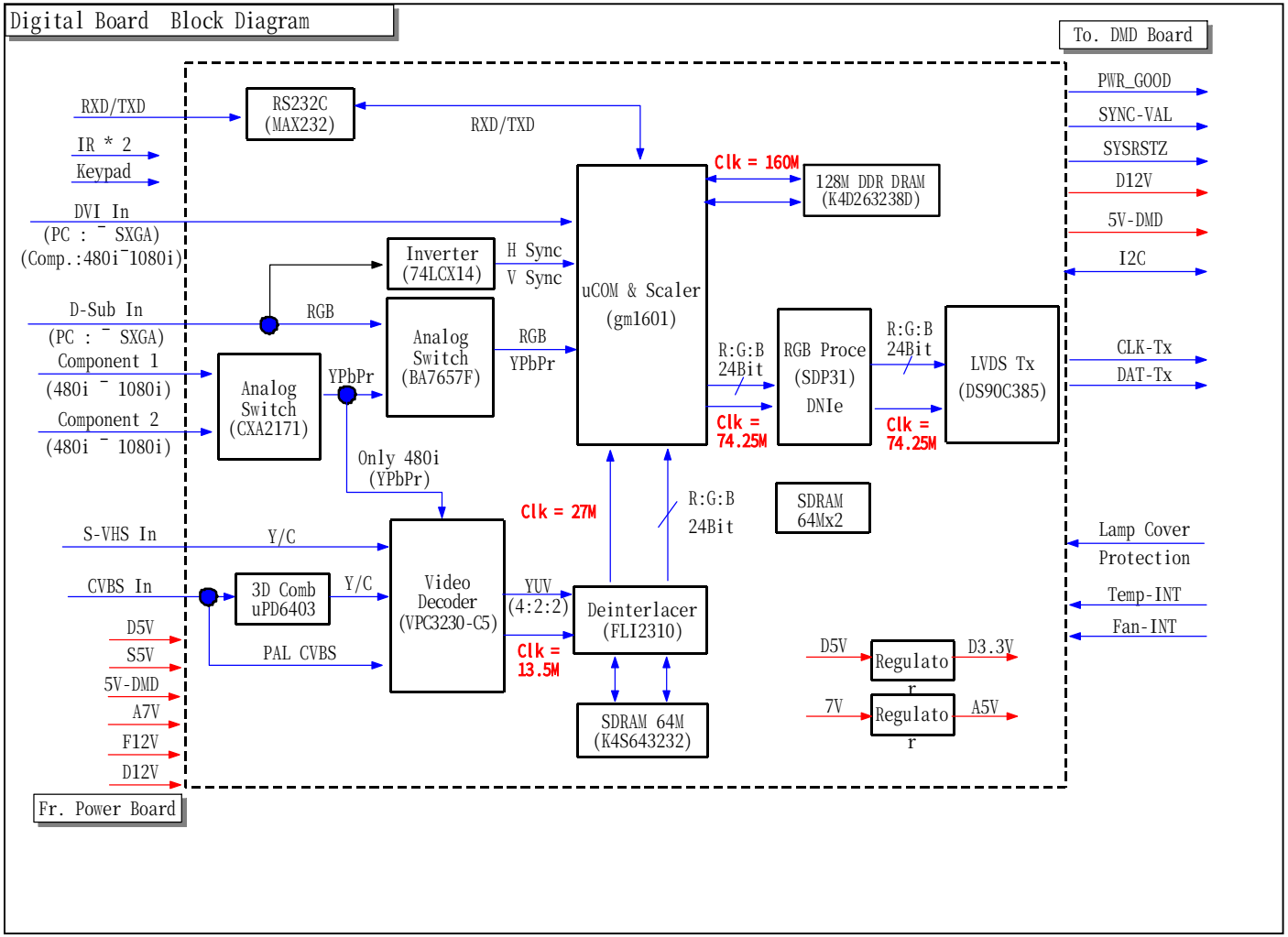
Final Result

No.	Frequency [MHz]	[P]	A.C.	S.C	Reading QP [dB(μV)]	c.f [dB(1/m)]	Result QP [dB(μV/m)]	Limit [dB(μV/m)]	Margin QP [dB]	Height [cm]	Angle [°]
1	44.891	V	OK	S	41.8	-21.4	20.4	30.0	9.7	174.0	224.0
2	194.390	V	OK	S	40.0	-21.8	18.2	30.0	11.8	103.0	32.0
3	214.187	H	OK	S	42.4	-21.6	20.8	30.0	9.2	378.0	328.0
4	742.540	H	OK	S	40.4	-7.6	32.8	37.0	4.2	113.0	317.0
5	905.260	H	OK	S	35.2	-3.9	31.3	37.0	5.7	105.0	248.0
6	296.996	H	OK	S	39.0	-16.5	22.5	37.0	14.5	261.0	21.0
7	371.260	H	OK	S	40.6	-14.4	26.2	37.0	10.8	272.0	205.0
8	519.750	H	OK	S	37.0	-10.5	26.5	37.0	10.5	152.0	236.0
9	480.070	V	OK	S	33.4	-13.6	19.8	37.0	17.2	111.0	73.0
10	163.326	V	OK	S	42.9	-21.9	20.6	30.0	9.4	101.0	9.0

EUT Photographs

[Label and Label position]

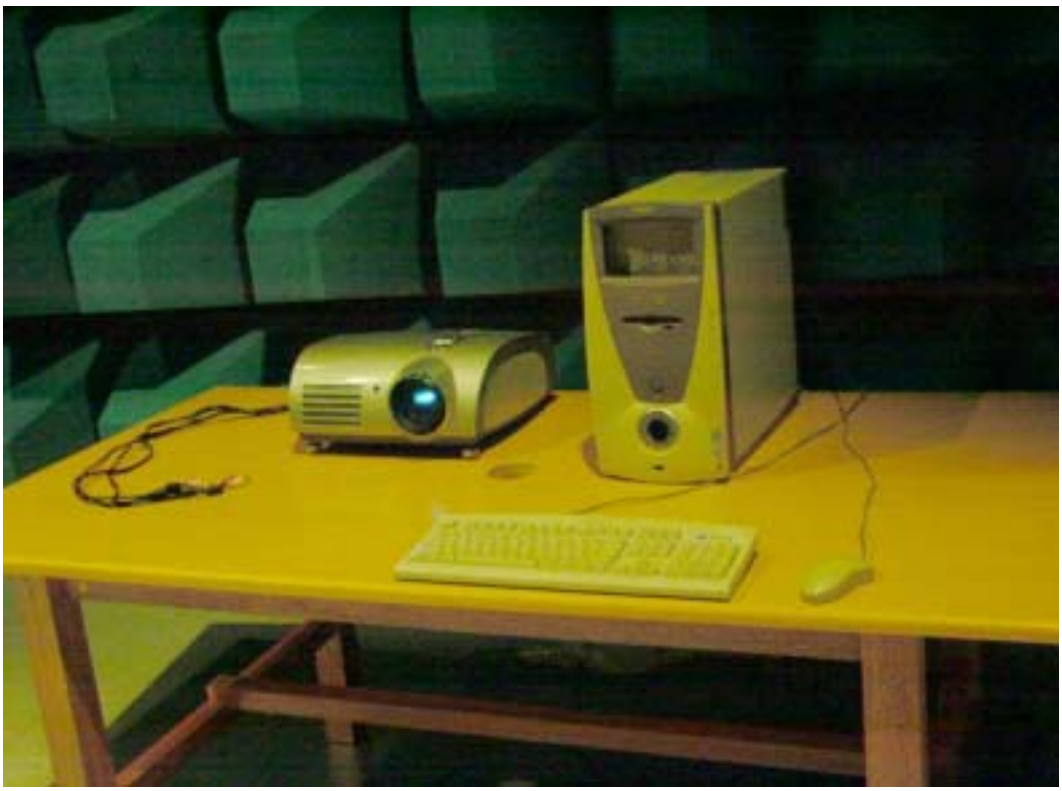



Digital Board Block Diagram
To. DMD Board
RXD/TXD
RS232C (MAX232)
RXD/TXD
IR * 2
Keypad
DVI In
(PC : ~ SXGA)
(Comp.:480i~1080i)
Inverter (74LCX14)
H Sync
V Sync
uCOM & Scaler (gm1601)
Clk = 160M
128M DDR DRAM (K4D263238D)
D-Sub In
(PC : ~ SXGA)
RGB
Analog Switch (BA7657F)
RGB
YPbPr
Component 1
(480i ~ 1080i)
Component 2
(480i ~ 1080i)
Analog Switch (CXA2171)
YPbPr
R:G:B 24Bit
Clk = 74.25M
RGB Proce (SDP31) DNIe
R:G:B 24Bit
Clk = 74.25M
LVDS Tx (DS90C385)
Only 480i (YPbPr)
Clk = 27M
R:G:B 24Bit
SDRAM 64Mx2
S-VHS In
Y/C
CVBS In
3D Comb uPD6403
Y/C
Video Decoder (VPC3230-C5)
YUV (4:2:2)
Deinterlacer (FLI2310)
Clk = 13.5M
PAL CVBS
SDRAM 64M (K4S643232)
D5V
S5V
5V-DMD
A7V
F12V
D12V
Fr. Power Board
D5V
Regulator
D3.3V
7V
Regulator
A5V
PWR_GOOD
SYNC-VAL
SYSRSTZ
D12V
5V-DMD
I2C
CLK-Tx
DAT-Tx
Lamp Cover Protection
Temp-INT
Fan-INT

Test Set up Photographs

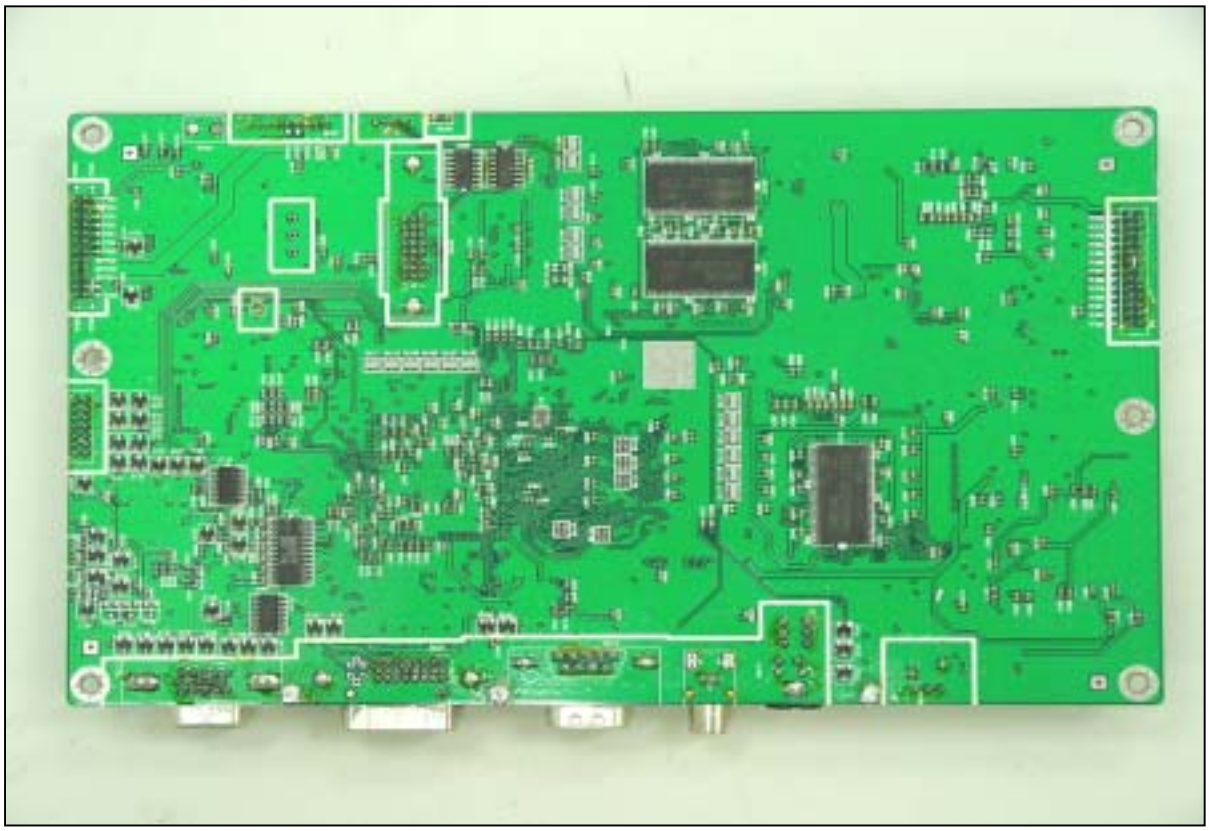
[AC POWERLINE CONDUCTED EMISSION MEASUREMENT]

[RADIATED EMISSION MEASUREMENT]

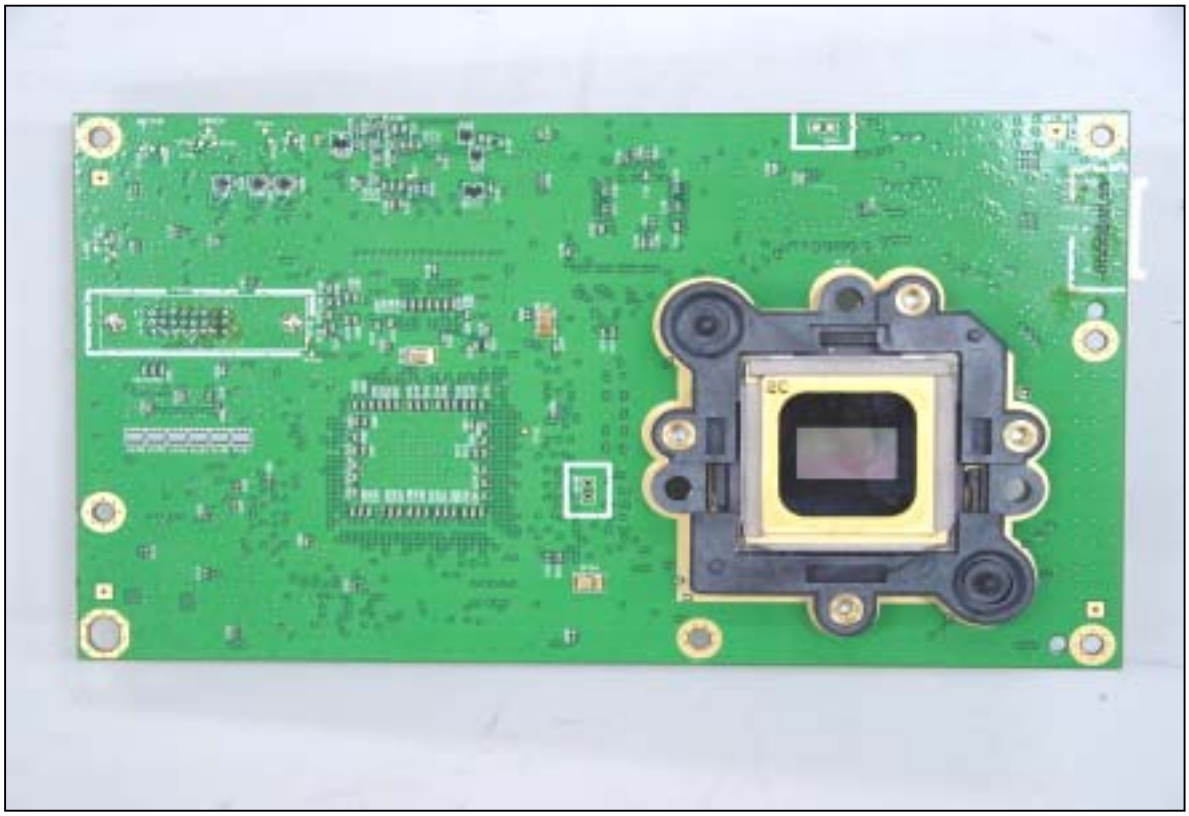


EUT Photographs

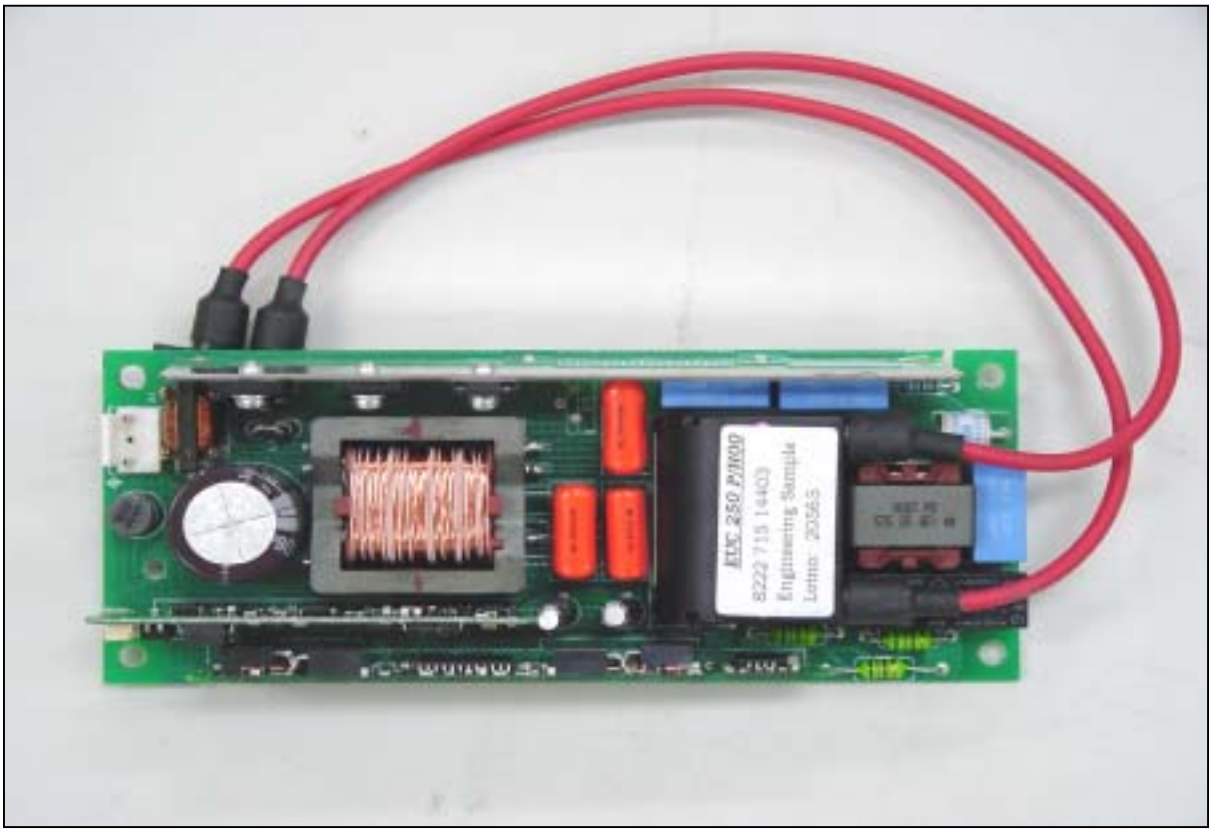
EUT Photographs

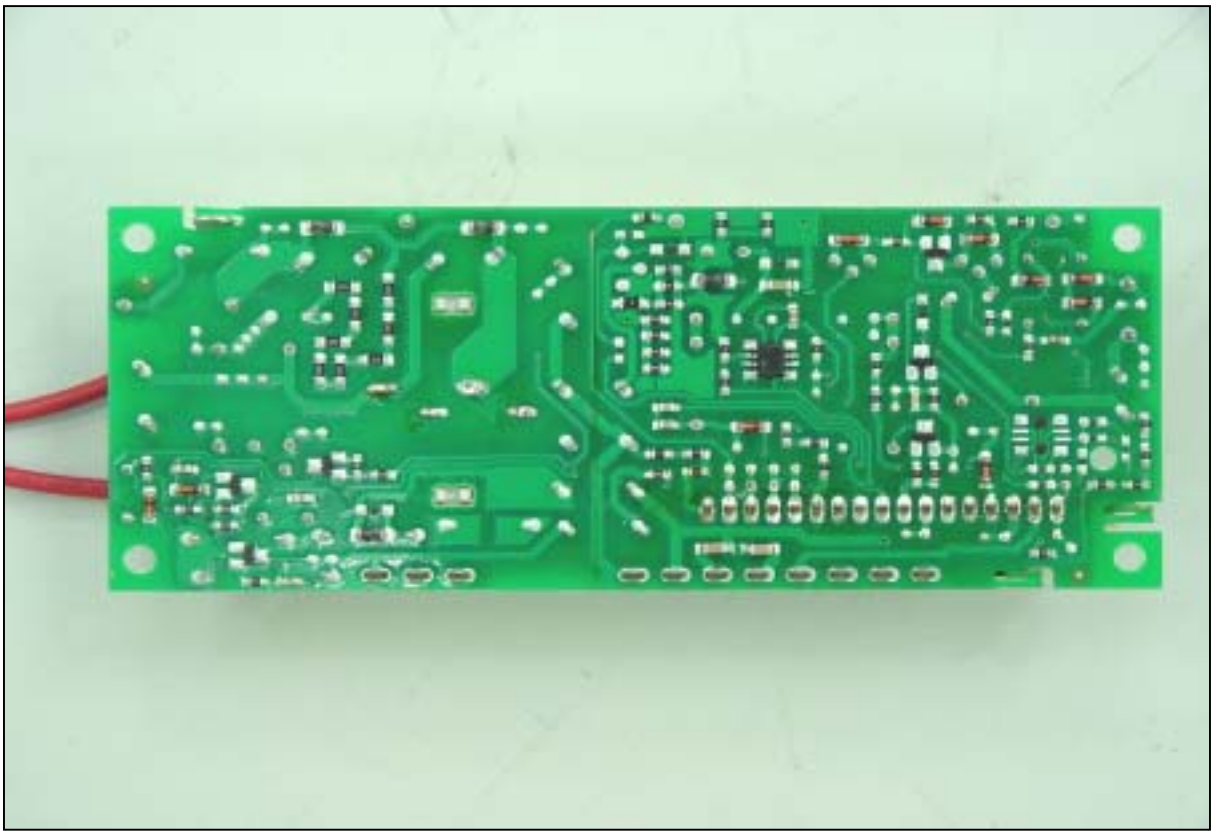


EUT Photographs



EUT Photographs





EUT Photographs