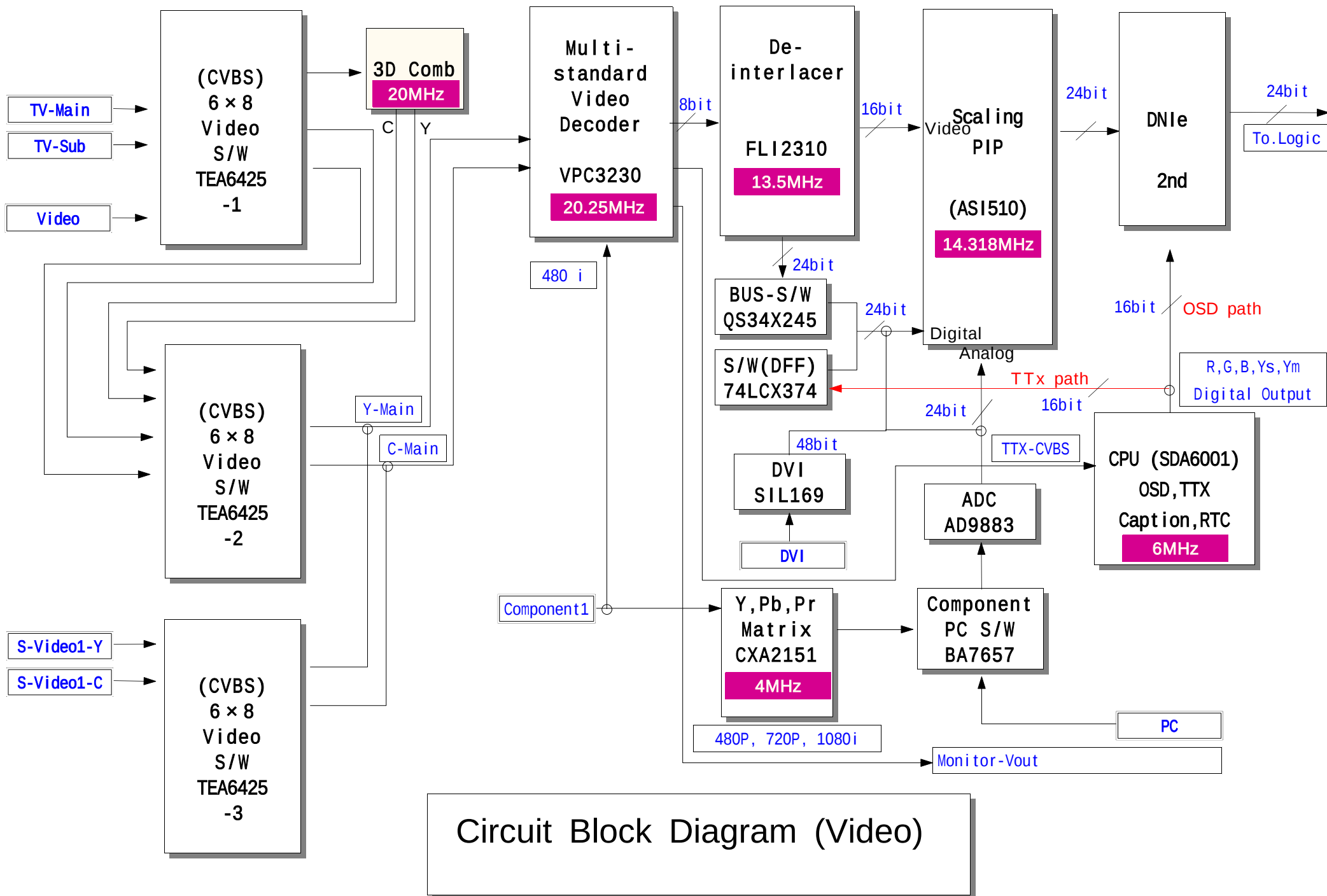




Circuit Block Diagram (Sound)

Component

PC

DVI

TV
SIF
MONO IN

Video

6 × 3
Stereo
Input
Output
S/W
TEA6422-1

6 × 3
Stereo
Input
Output
S/W
TEA6422-2

Multi-
standard
Sound
Processor
MSP4440G
18.432MHz

5 = I2S_WS → WCK_M= 4
4 = I2S_CL → BCK_M= 5
6 = I2S_DA_OUT → SDATA12= 6
74= AUD_CL_OUT → CKIN=23

30=DACM_Sub → Sub-Woofer Out
25=DACA_L → Blank (Line out)
24=DACA_R →
28=DACM_L → Monitor Out
27=DACM_R →
37=SC1_OUT_L
36=SC1_OUT_R
34=SC2_OUT_L
33=SC2_OUT_R

Digital
Audio
Signal
Processor
NSP6241A

Out1A Out1B Out2A Out2B
56 55 54 53
7 10 8 11 17 20 18 21
DP CM DM CP BP AM BM AP

True Digital
Audio
Amp
TAS5112

34/35 38/39 46/47 50/51
OutA OutB OutC OutD

