

Operational Description

1. Data from the host processor is sent to the MAC through the CompactFlash interface
2. ISL3871 packetizes to compliant with 802.11b and translates into BPSK, QPSK and CCK I/Q signal by different rate –1, 2, 5.5 and 11Mbps
3. Through 3-wire control setting the communication channel at ISL3684
4. ISL3684 up-convert the I/Q to RF 2.4GHz and a variable gain block amplifies the RF signal by TX close-loop control
5. ISL3684 feeds the 2.4GHz modulated signal to PA for last stage power boost
6. After the power amplified, a BPF rejects the harmonics and spurious
7. Through software diversity selection, one antenna will be chosen for signal emission