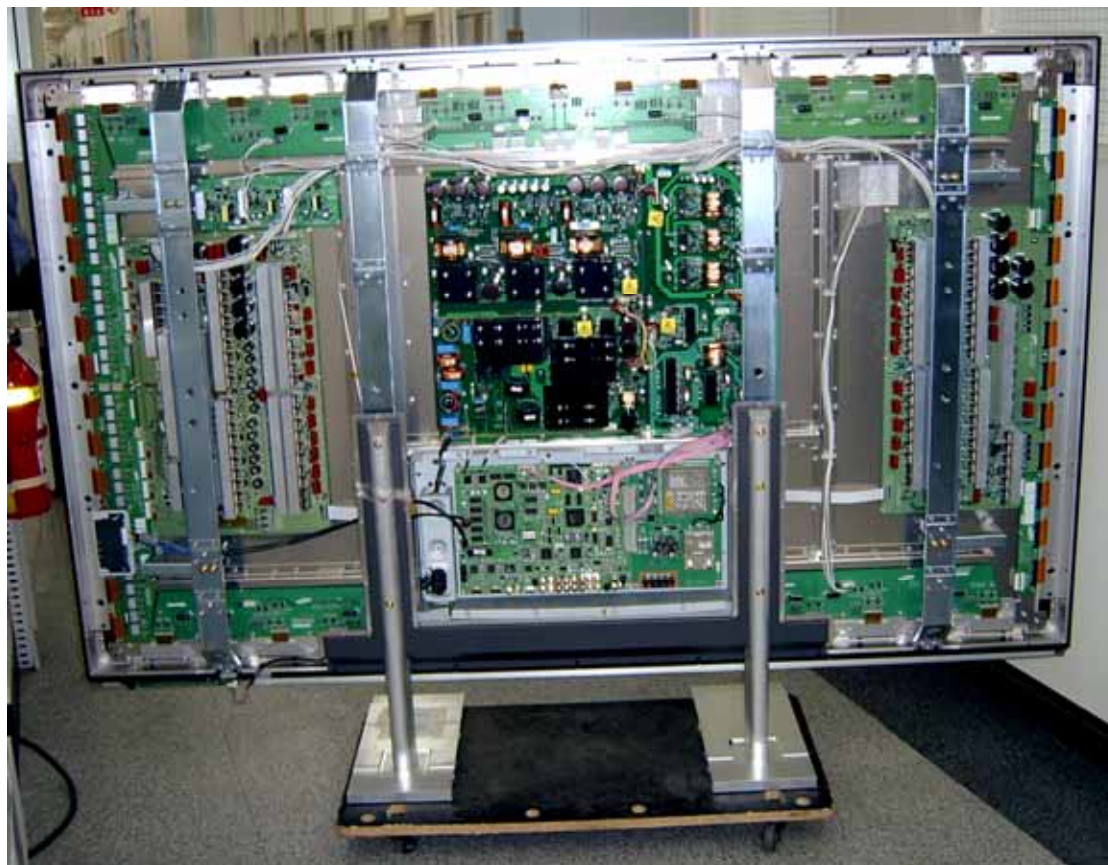
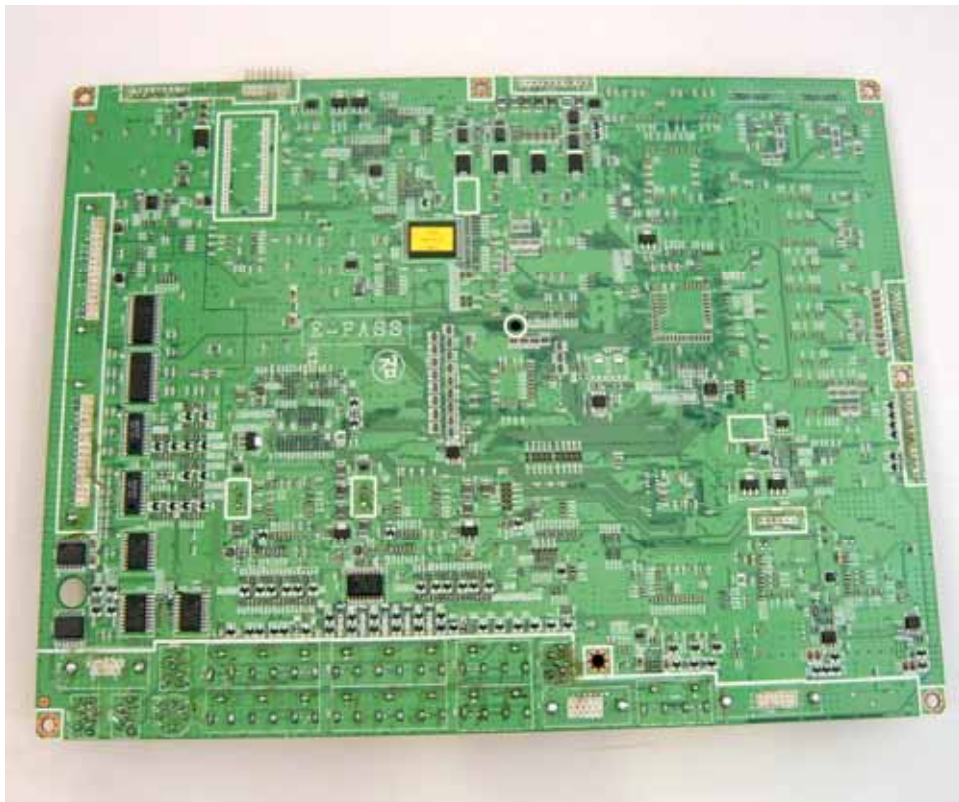




EUT internal view



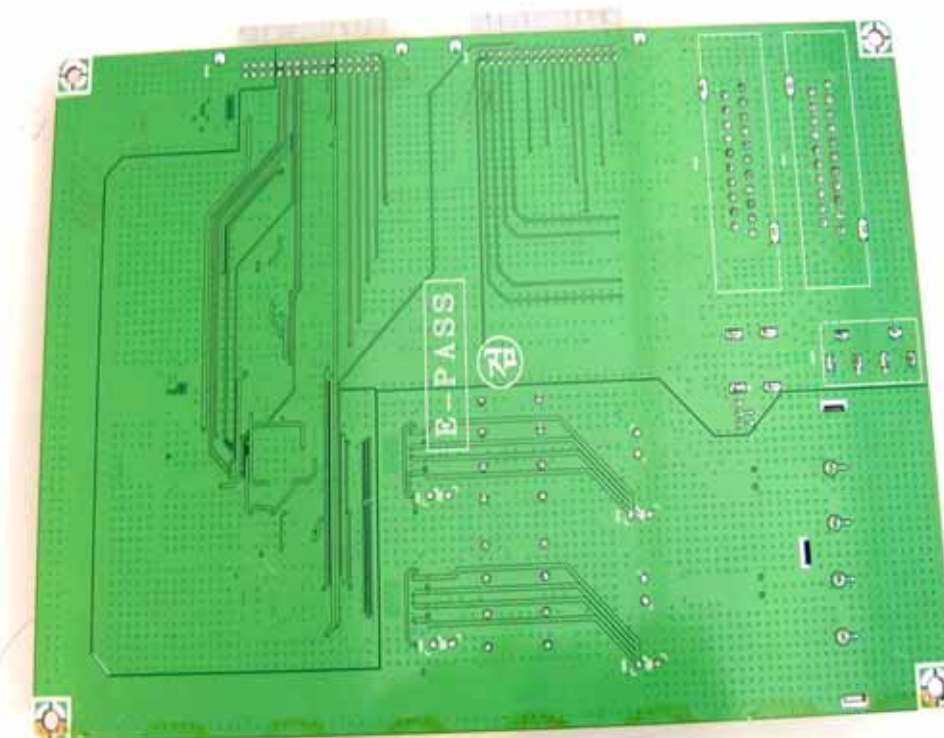
Visual board top view



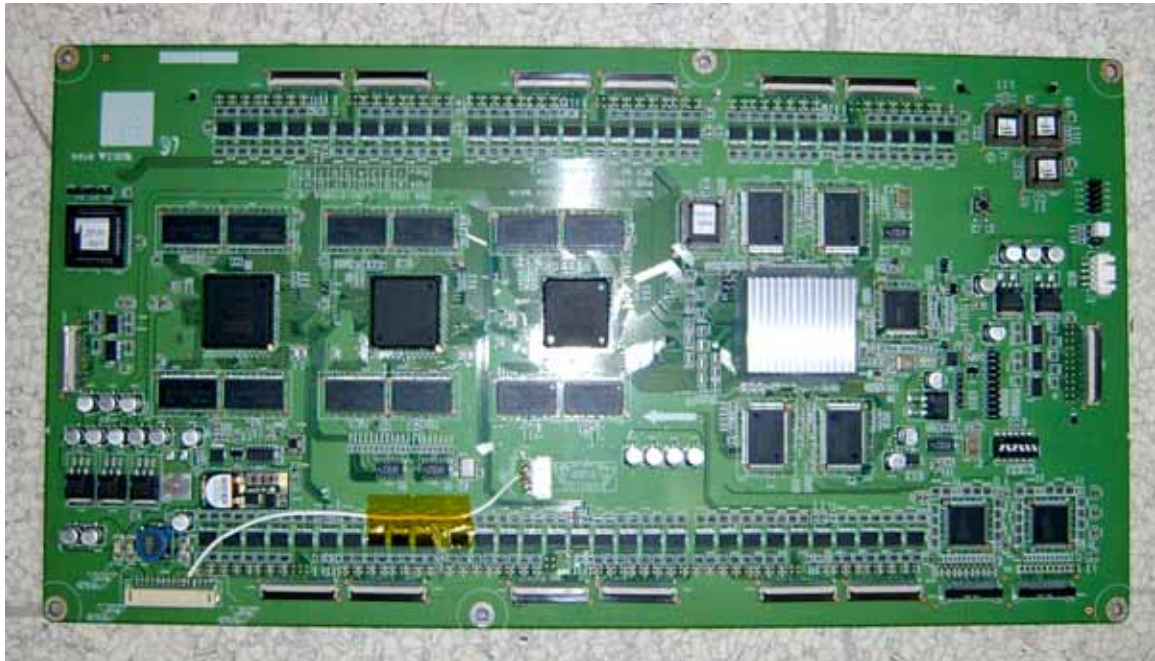
Visual board bottom view



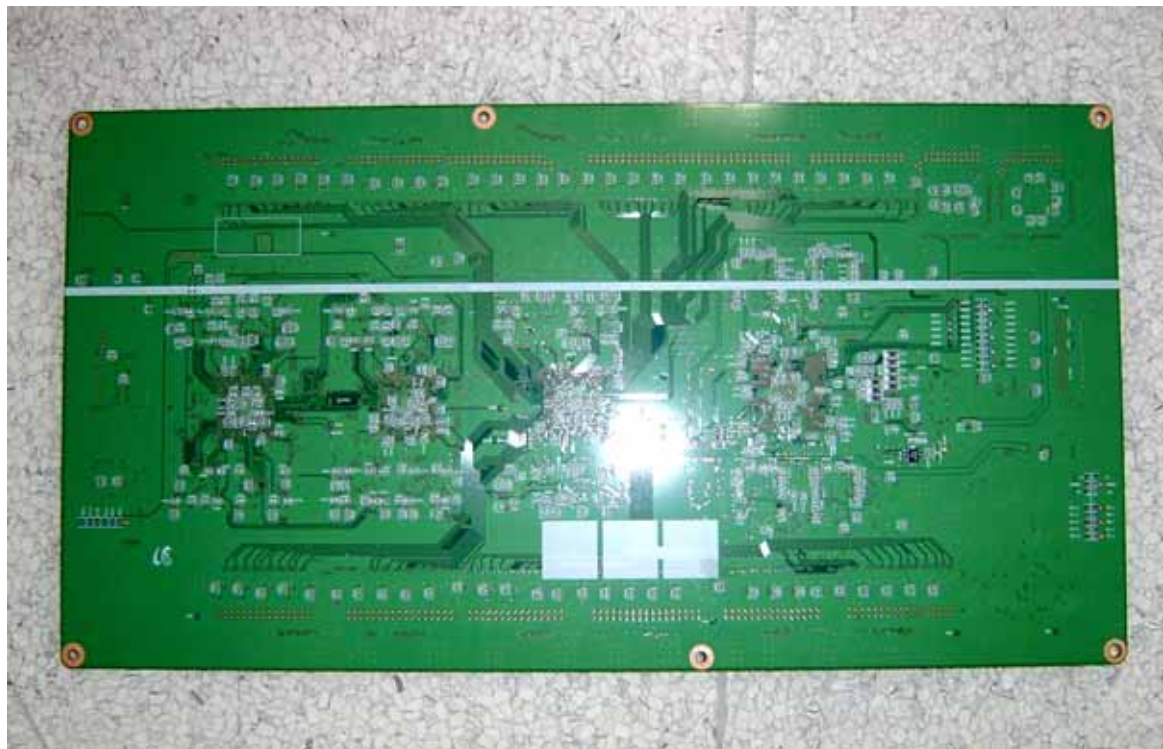
Visual board top view



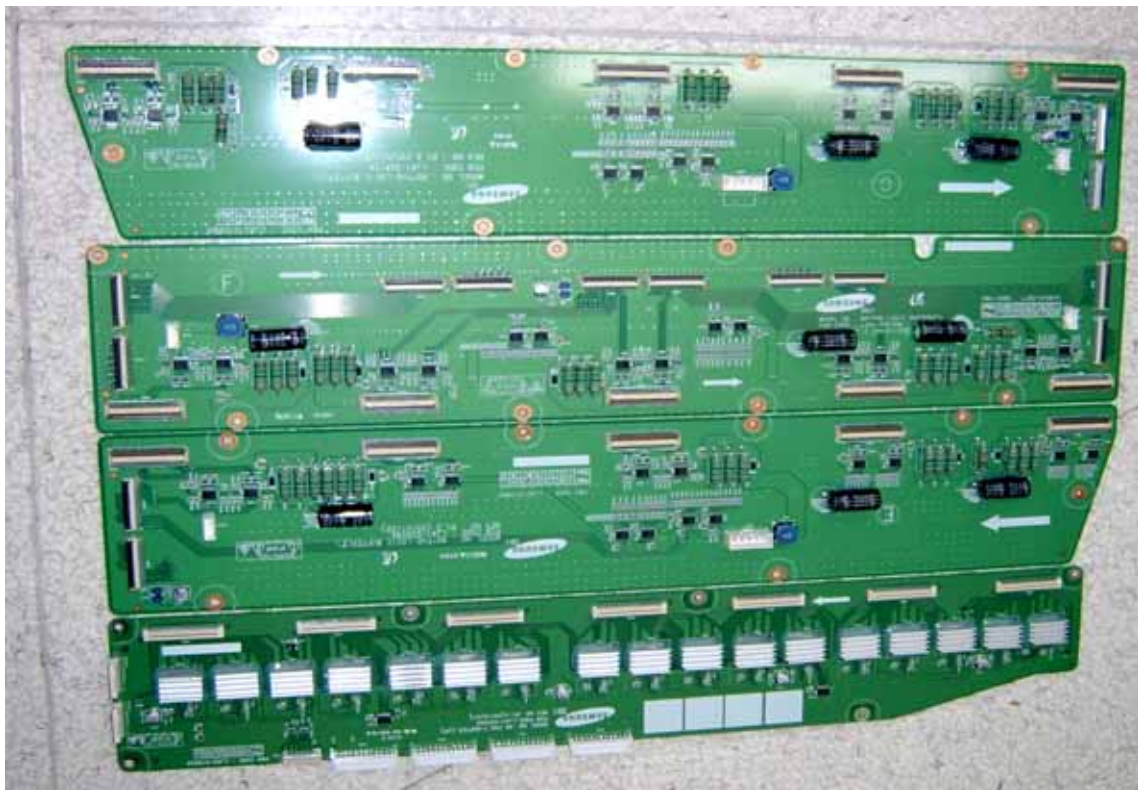
Visual board bottom view



Logic board top view



Logic board bottom view



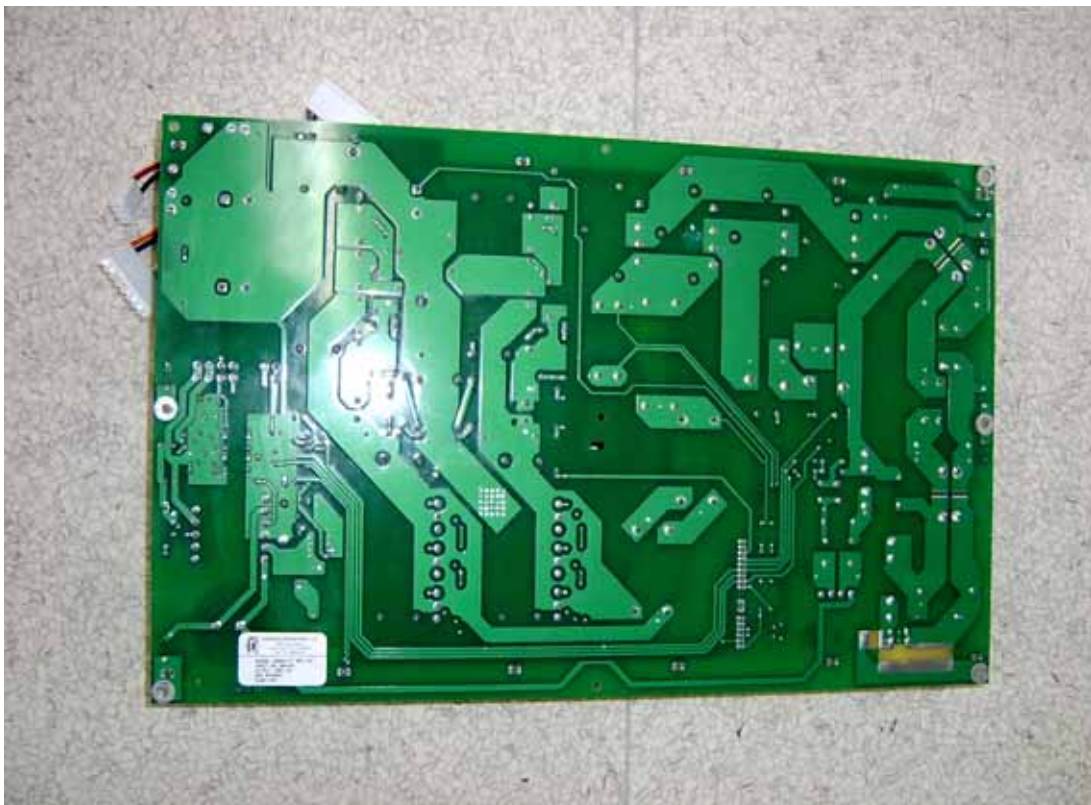
Logic buffer top view



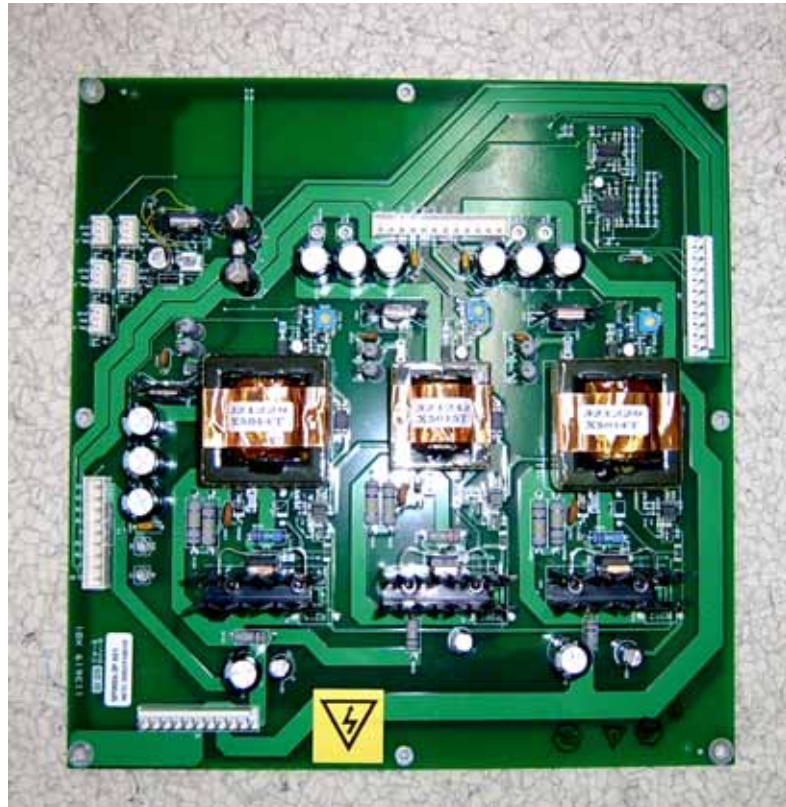
Logic buffer bottom view



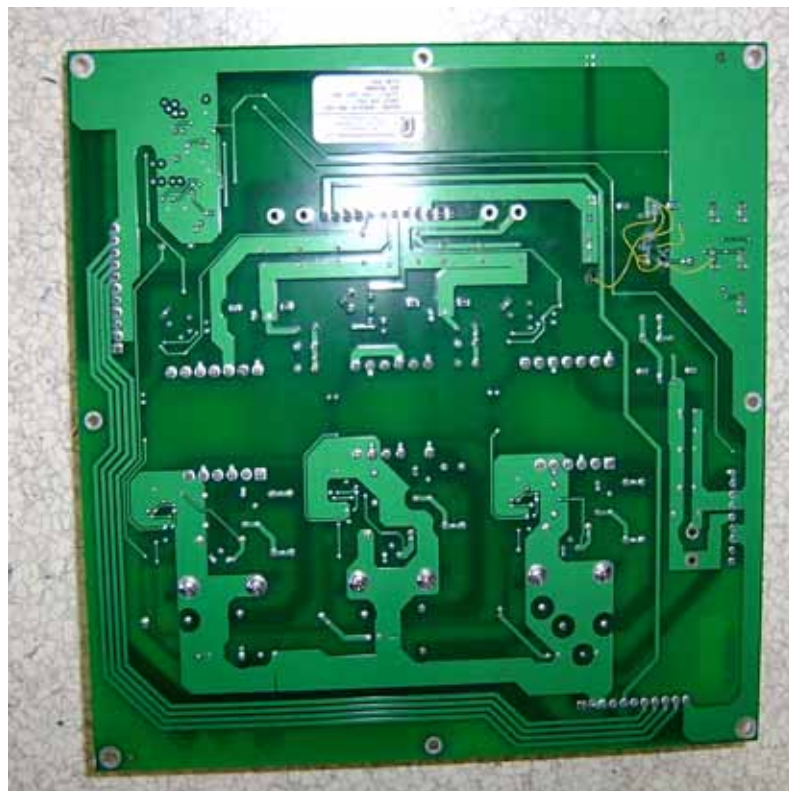
SMPS1 board top view



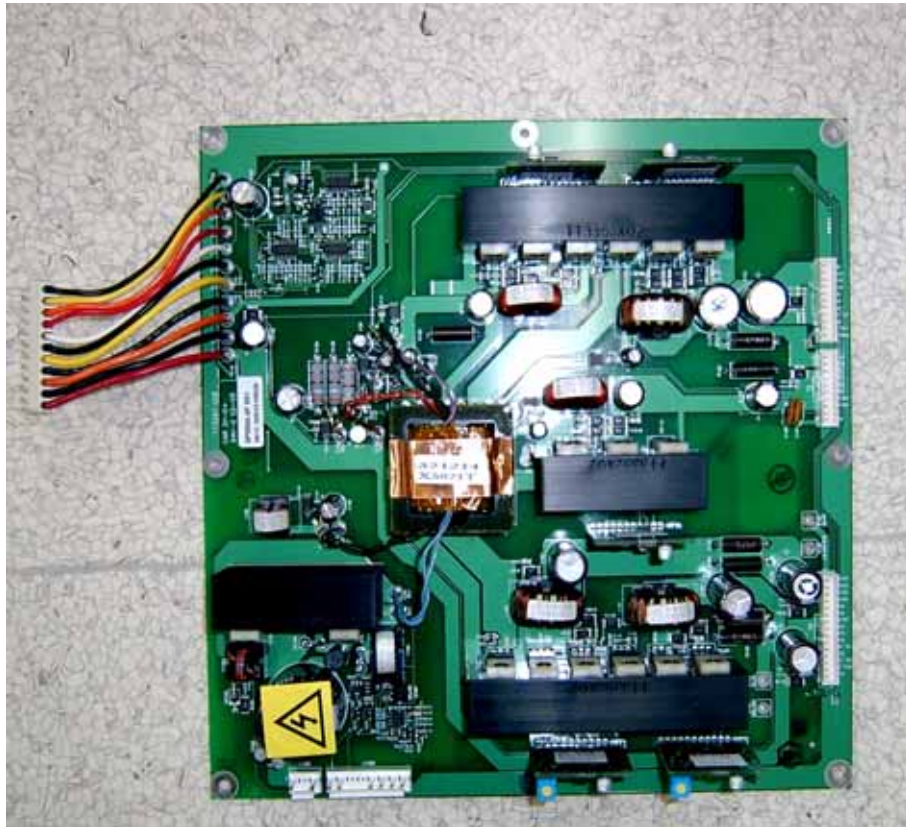
SMPS1 board bottom view



SMPS2 board top view



SMPS2 board bottom view



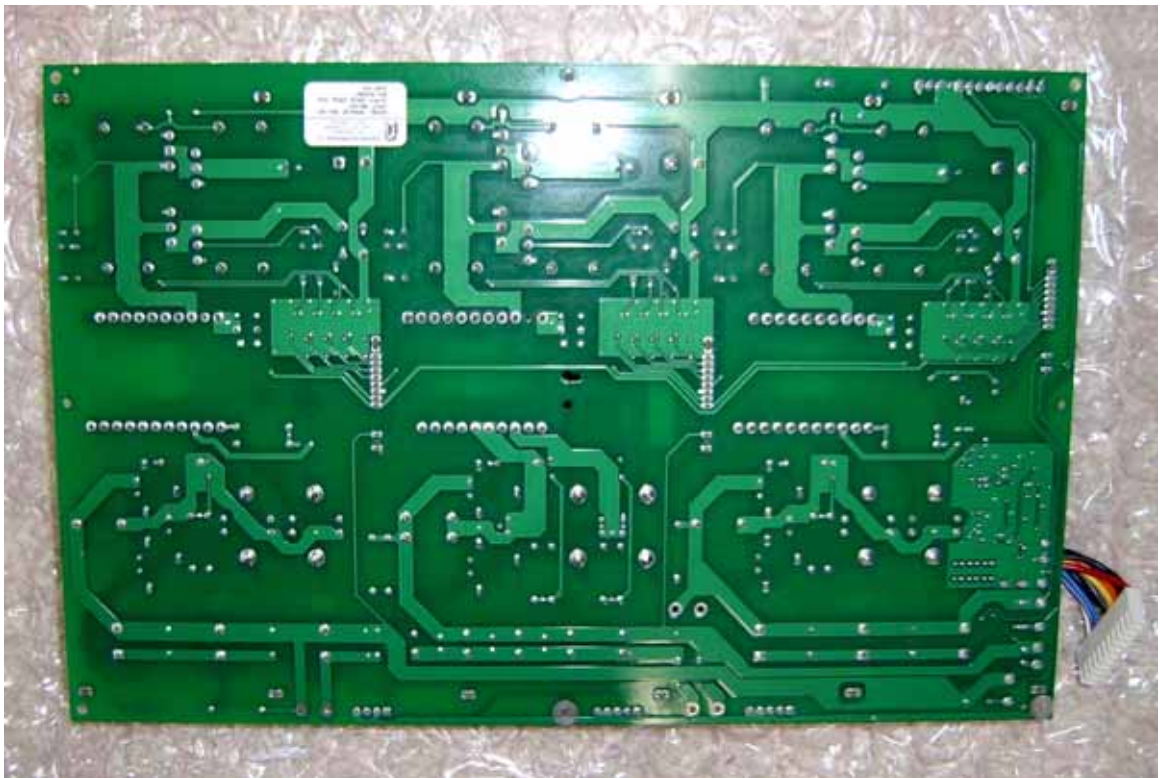
SMPS3 board top view



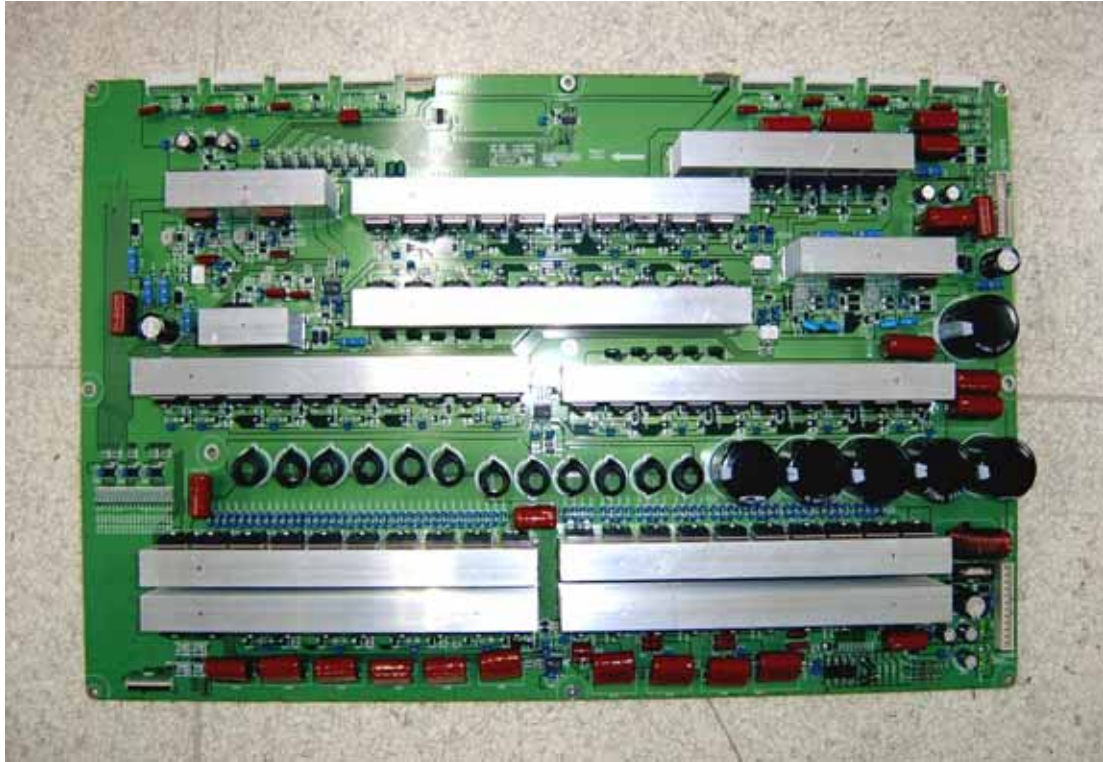
SMPS3 board bottom view



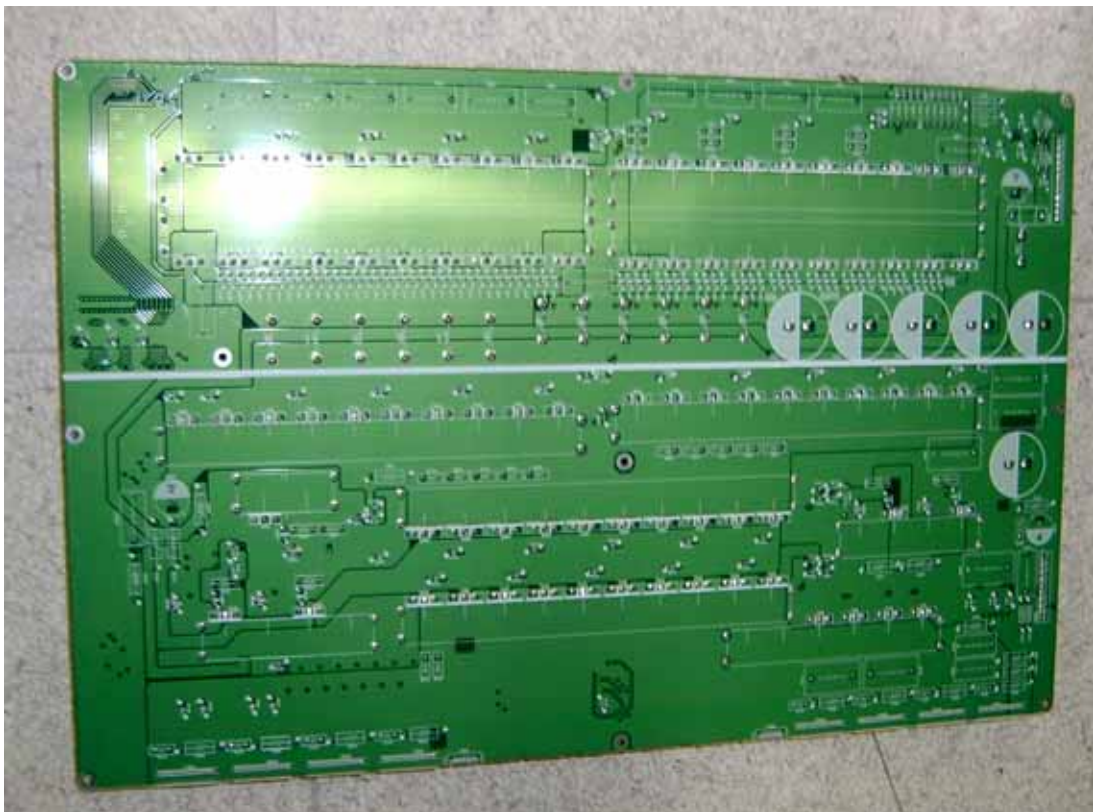
SMPS4 board top view



SMPS4 board bottom view



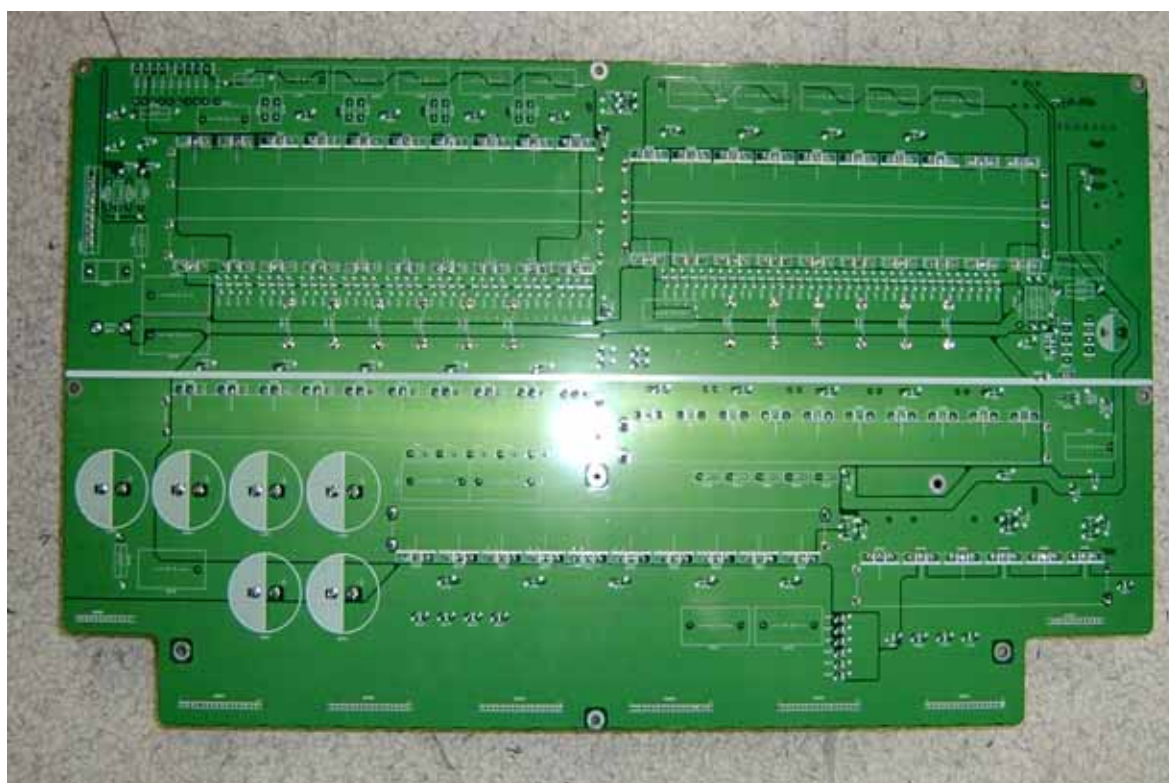
Y driver board top view



Y driver board bottom view



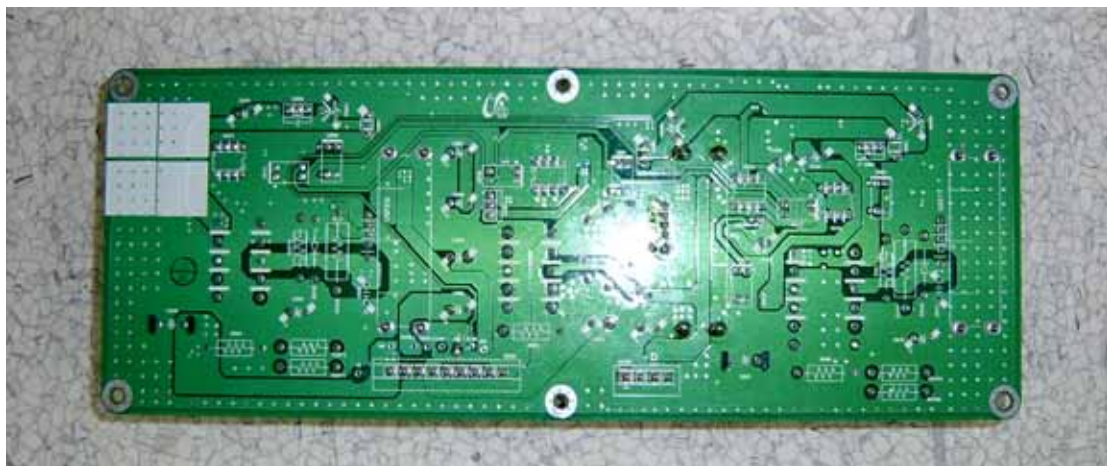
X driver board top view



X driver board bottom view



DC-DC board top view



DC-DC board bottom view