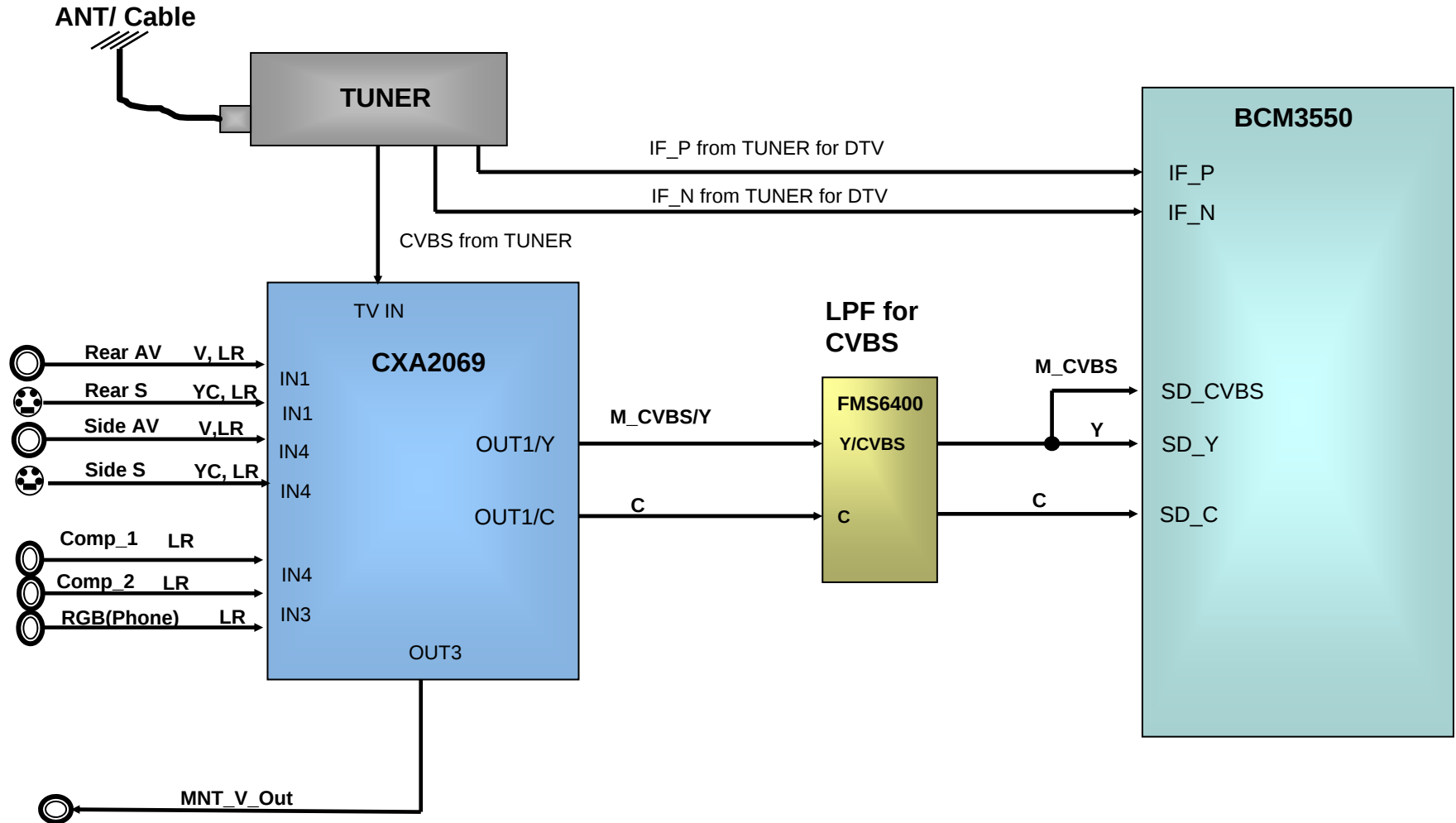


APPENDIX D
: BLOCK DIAGRAM

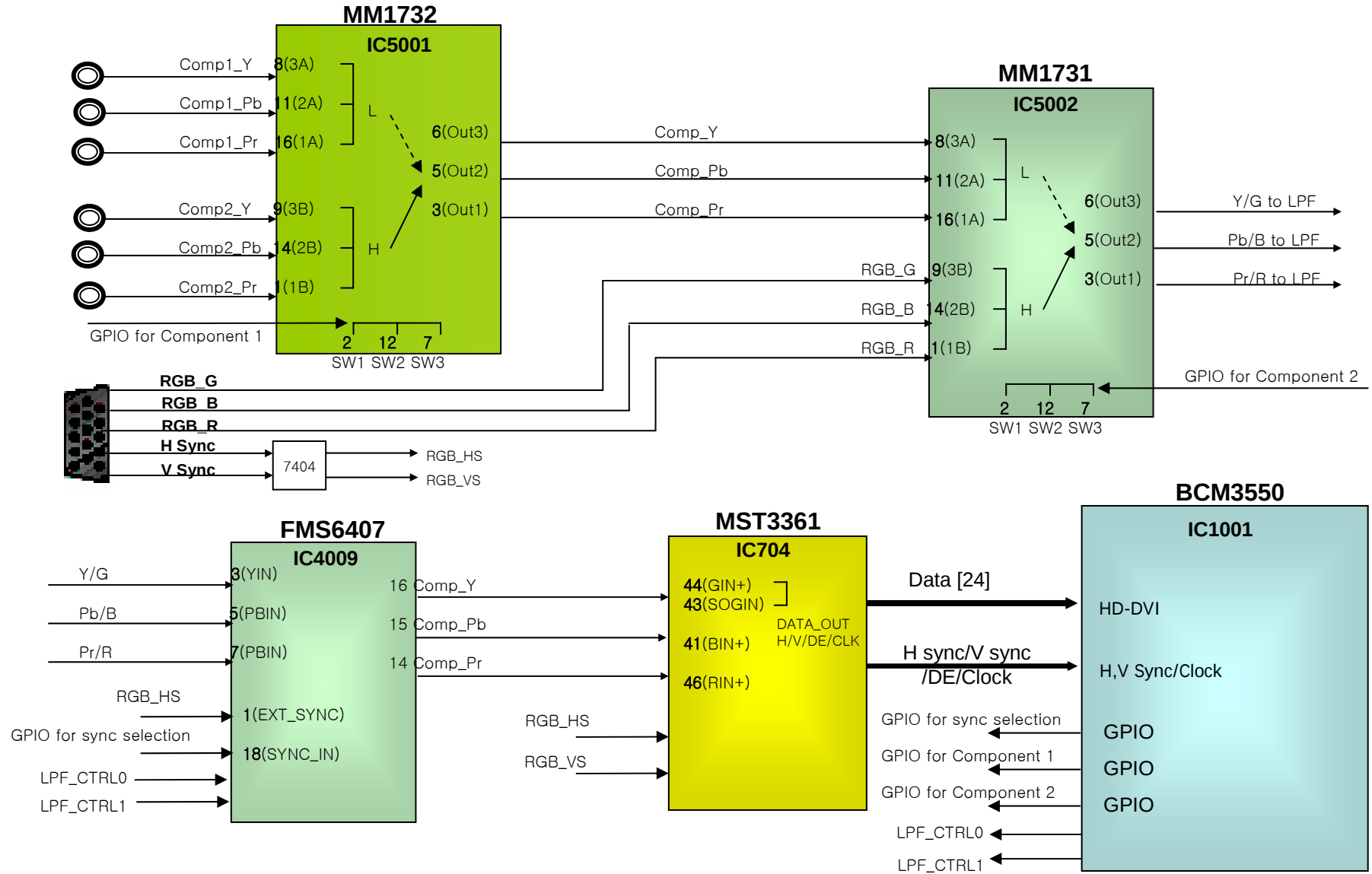
The diagram illustrates the system architecture centered around the BCM3560 processor. Key components and connections include:

- Inputs:**
 - ANT. Cable:** Connected to the ATSC/NT SC Tuner.
 - ATSC/NT SC Tuner:** Outputs SIF for NTSC and CVBS to the BCM3560.
 - CXA2069(AV S/W):** Receives multiple video inputs (Rear AV, Rear S, Side AV, Side S, MNT_V_Out, Comp_1, Comp_2, RGB(Phone)) and outputs to the BCM3560.
 - MST3361:** Receives H/V Sync, R/G/B, and YCbCr inputs, and outputs Data [24], Hsync/Vsync/DE/Clk, and SPDIF to the BCM3560.
 - HDMI & Component RGB-PC:** Provides TMDS and DDS signals to the MST3361.
 - Reset IC:** Provides a reset signal to the BCM3560.
 - SDRAM 256Mbit x2:** Connected via 64-Bit I/F.
 - X-tal 54MHz:** Provides a clock signal to the BCM3560.
- Outputs:**
 - CVBS:** Output from the BCM3560 to the CXA2069.
 - VSB/QAM/NTSC/SIF:** Output from the BCM3560 to the ATSC/NT SC Tuner.
 - VDEC_CVBS/S_Y:** Output from the BCM3560 to the CXA2069.
 - I2S Audio Input:** Receives audio from the AUDIO ADC.
 - SPDIF_OUT:** Output to the SPDIF Out interface.
 - R/L Output:** Output to the D-class AMP.
 - Flash Memory 16MByte:** Connected via IIC.
 - Sub Micom (MTV416):** Connected via IIC, with EEP ROM.
 - AUDIO DAC:** Outputs MNT_Out LR.
- Internal BCM3560 Blocks:**
 - VSB/QAM/NTSC/SIF**
 - VDEC_CVBS/S_Y**
 - I2S Audio Input**
 - DVI Input**
 - HDMI/DVI**
 - IIS**

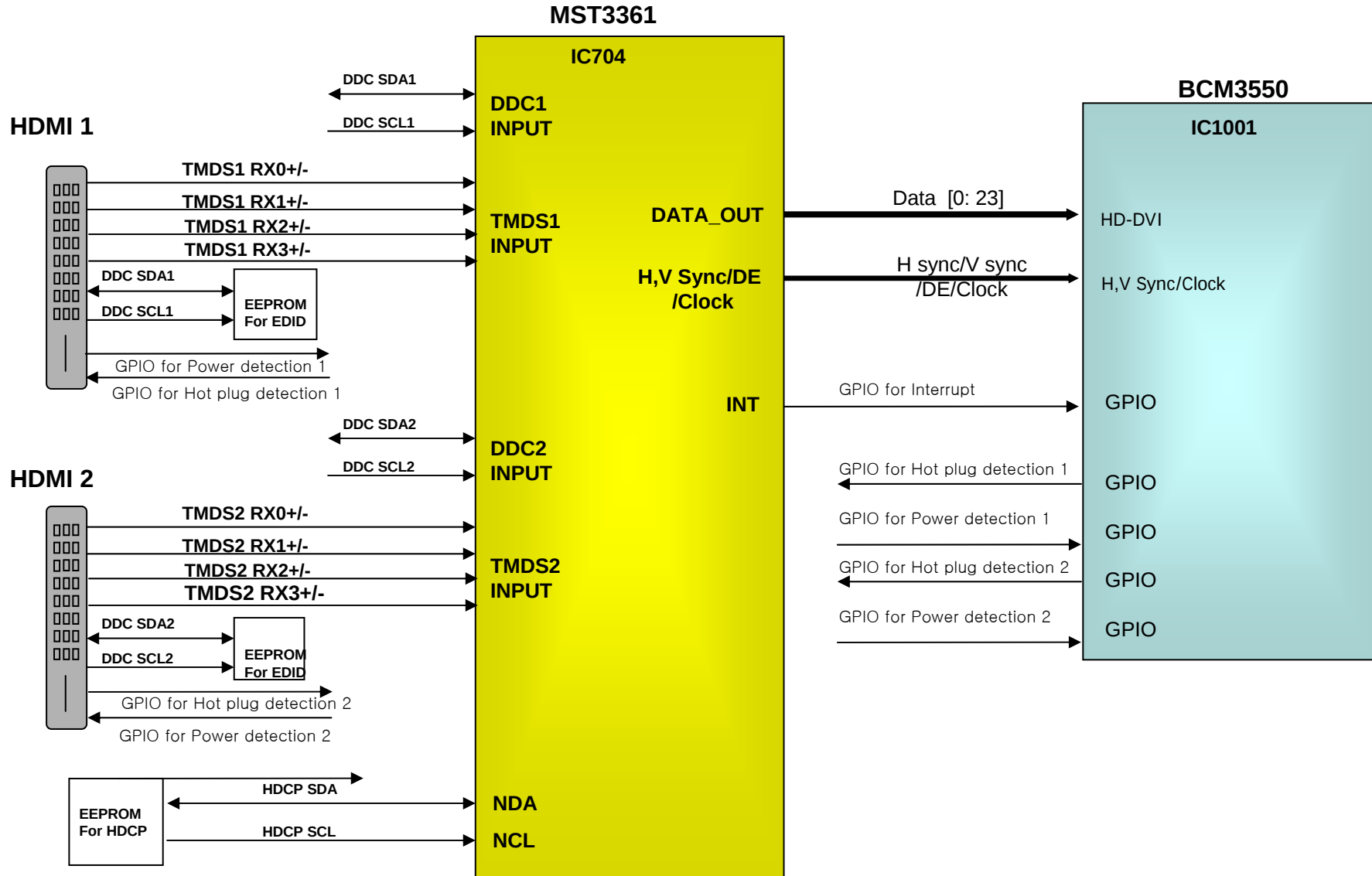
Video signal path for Composite / S-video / IF



Video signal path for Component



Video signal path for HDMI



Audio signal path for all

