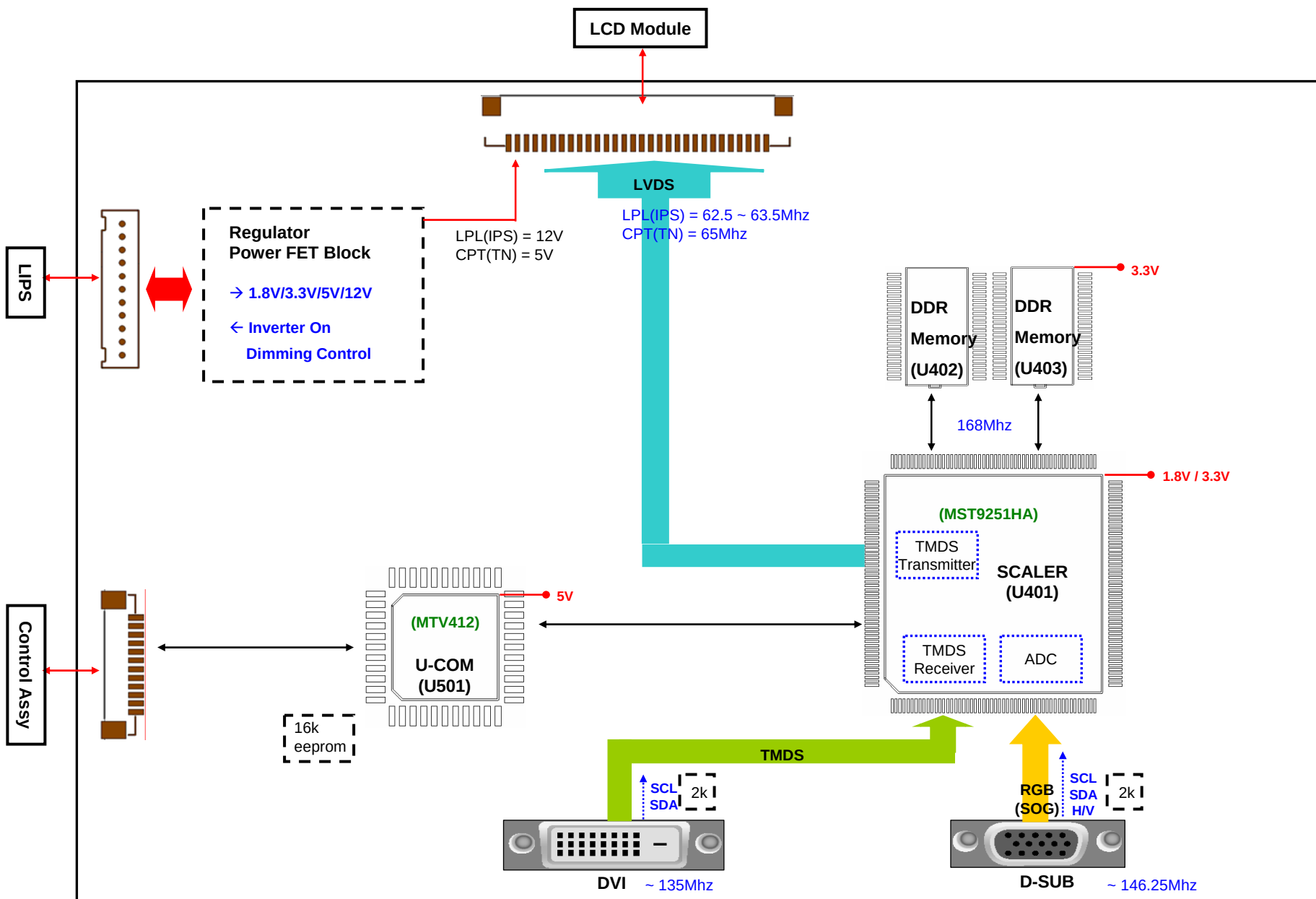


APPENDIX D
: BLOCK DIAGRAM

L203WT Block diagram



DESCRIPTION OF BLOCK DIAGRAM

1. Video Controller Part & Display Data Transmitter Part.(MST9251HA)

In case of analog input, this part amplifies the level of video signal for the digital conversion and converts from the analog video signal to the digital video signal using a pixel clock.

In case of digital input, internal TMDS receiver decodes digital signal for scaler operation.

The pixel clock for each mode is generated by the PLL.

The range of the pixel clock is from 25MHz to 162MHz.

This part consists of the All-in-one Scaler with ADC, PLL, TMDS Receiver, Transmitter and scaler.

The scaler interpolates PC input resolution to 1680×1050 resolution signal and outputs 8-bit R, G, B signal to transmitter.

Transmitter codes digital signal from the Scaler and output coded data to receiver of module.

2. Micom Part

This Part consists of EEPROM IC which stores control data, Reset IC and the Micom.

The Micom distinguishes polarity and frequency of the H/V Sync are supplied from signal cable.

The controlled data of each modes is stored in EEPROM.

3. Power Part

LIPS's outputs are 15V and 5V.

15V is supplied to Module changing by 12V(Regulator) and 5V(DC-DC Converter).

5V is supplied on u-COM and each signal part.

Also, 5V is converted 3.3V and 1.8V by regulator. Converted power is provided for IC on the main board.