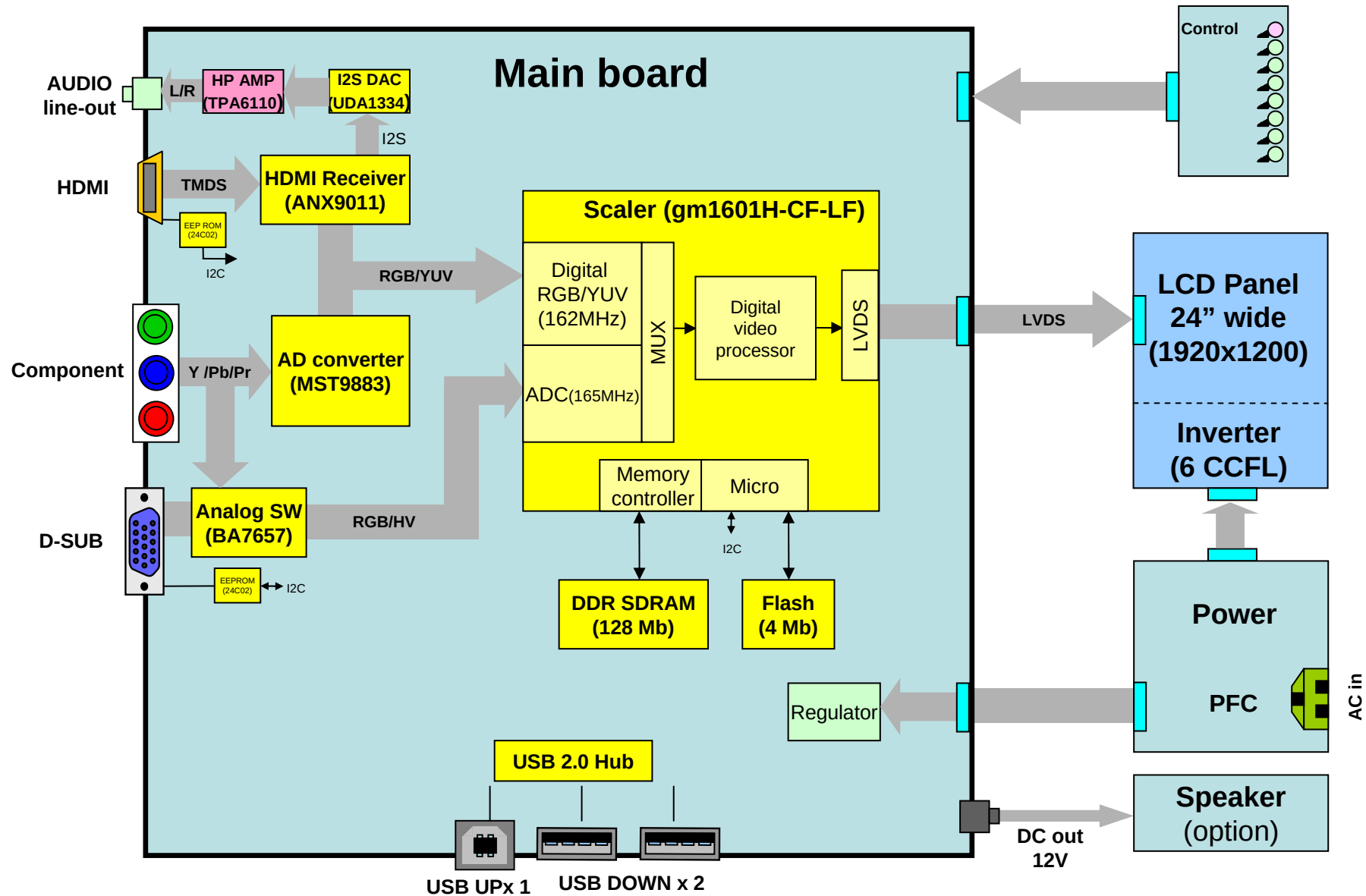


APPENDIX D
: BLOCK DIAGRAM

L245WP System Architecture

2006. 05. 16



1. HDMI Receiver (ANX9011): HDMI input signal is converted to digital R/G/B.
2. A/D Converter (MST9883) : Component input signal is converted to ITU656 format.
3. Analog Switch (BA7657) : Select one signal between D-sub and Component.
4. I2S D/A Converter (UDA1334) : I2S signal is converted to analog L/R sound signal.
5. Scaler (gm1601H) : Input signal is converted to LVDS signal.
6. USB HUB (uPD720114) : The USB port of the monitor supports High-Speed Certified USB 2.0

Clock Frequency

1. Crystal
 - Scaler : 14.3 MHz
 - HDMI Receiver : 27 MHz
 - USB HUB : 30 MHz
2. LVDS : 85 MHz
3. Memory : 165 MHz
4. ADC 출력 : 27 MHz

POWER BLOCK DIAGRAM **& DESCRIPTION**

