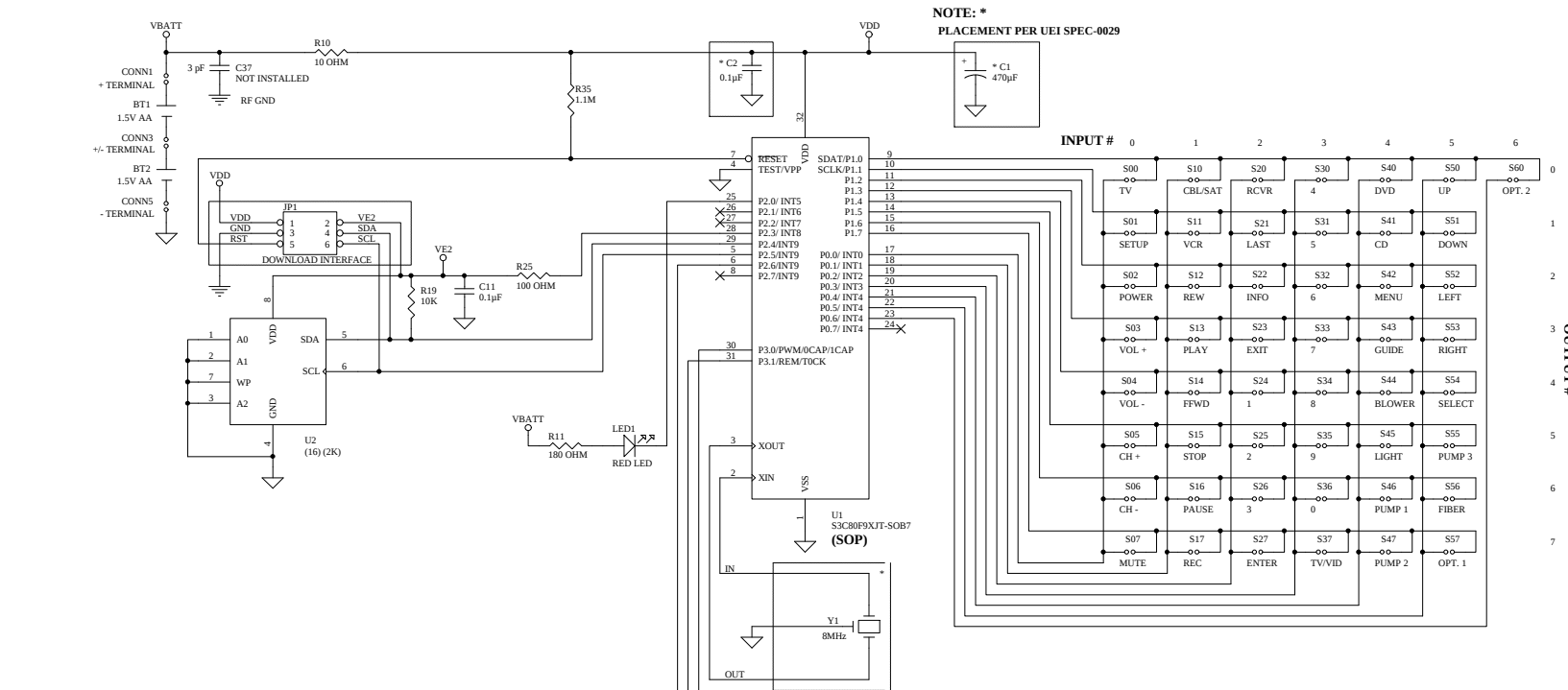
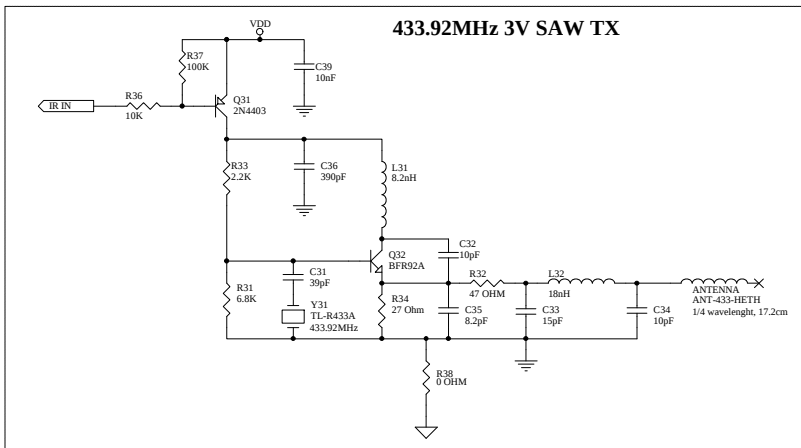


Layout Notes: **
Align IR1 with mark on plastics.

Layout Notes: àà
1.) Place Learner circuit as close to IR1 as possible.
2.) Place DATA OUT TRACE well away from the RED LED TRACE and the VBATT TRACE.



NOTE
RF GND
Vbatt - (Negative battery terminal)