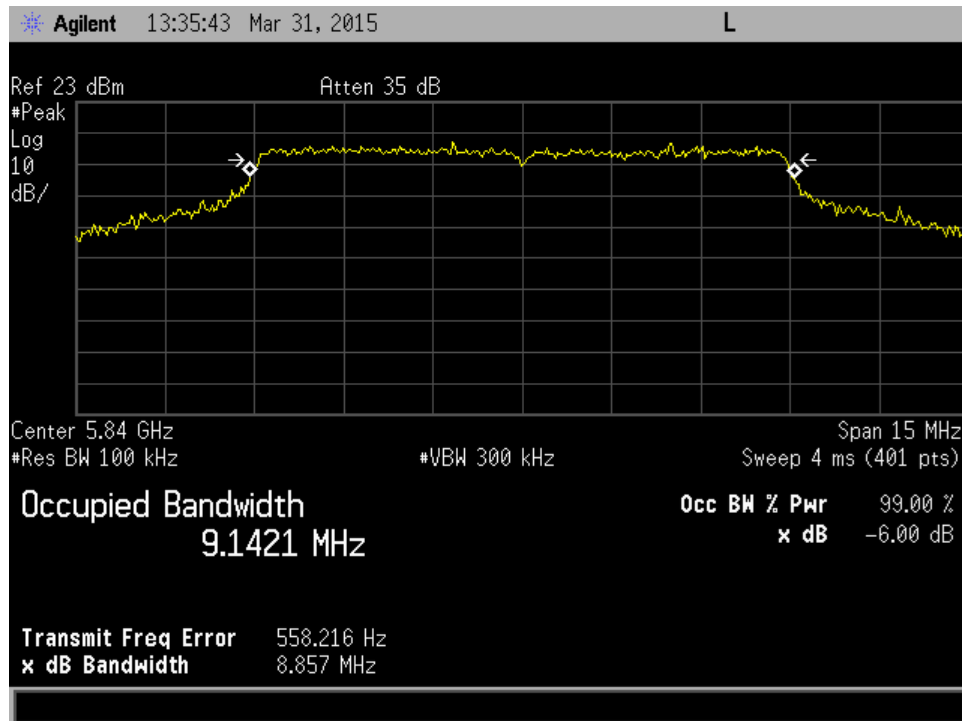


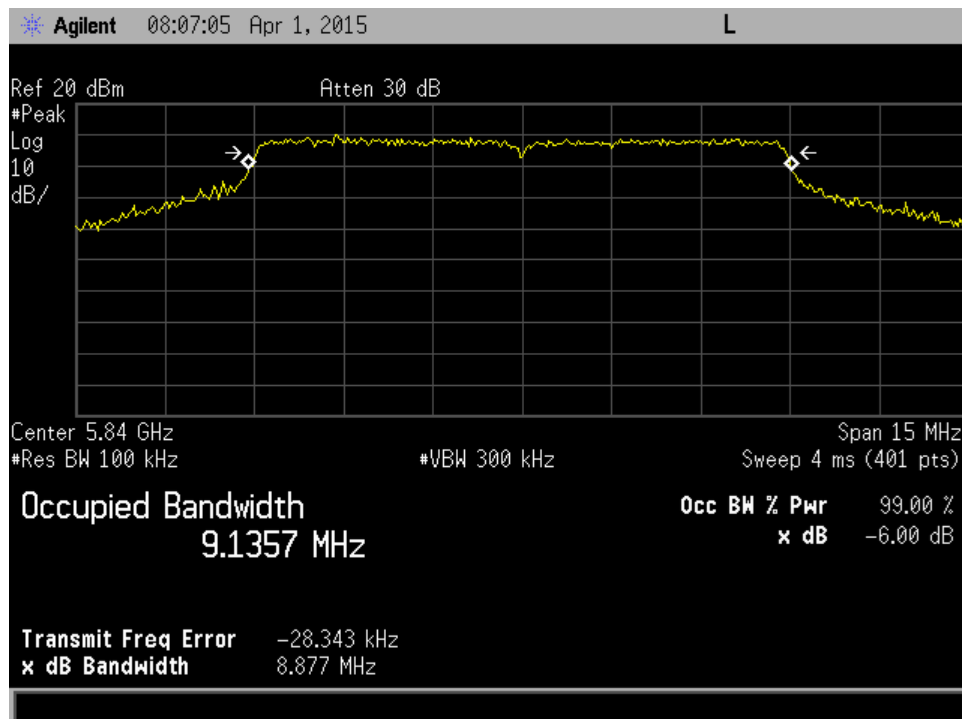


Annex D

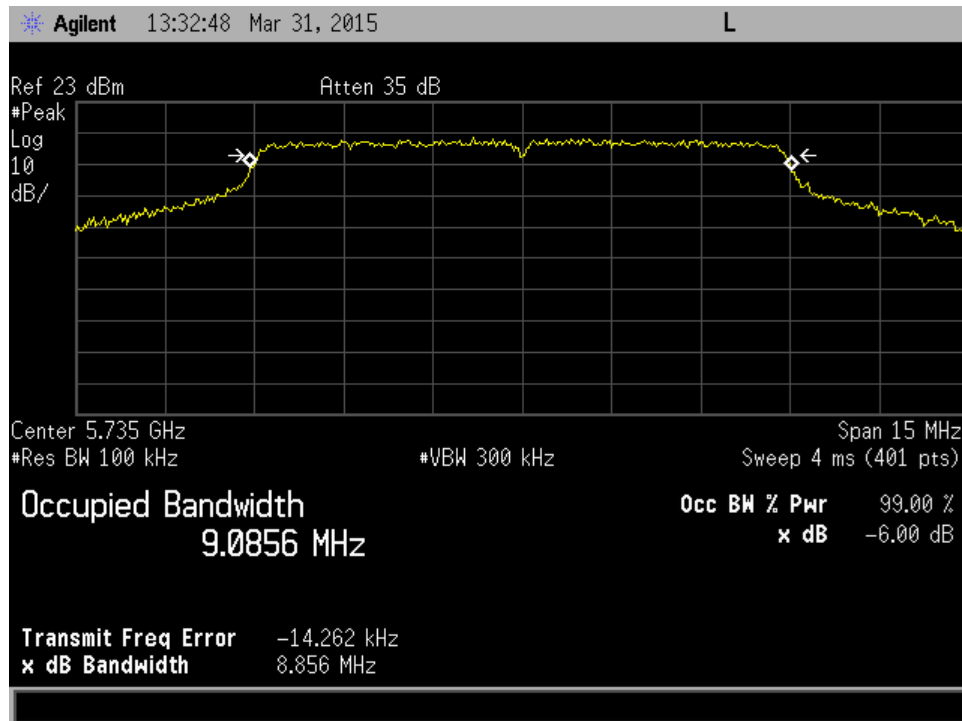
Occupied Bandwidth



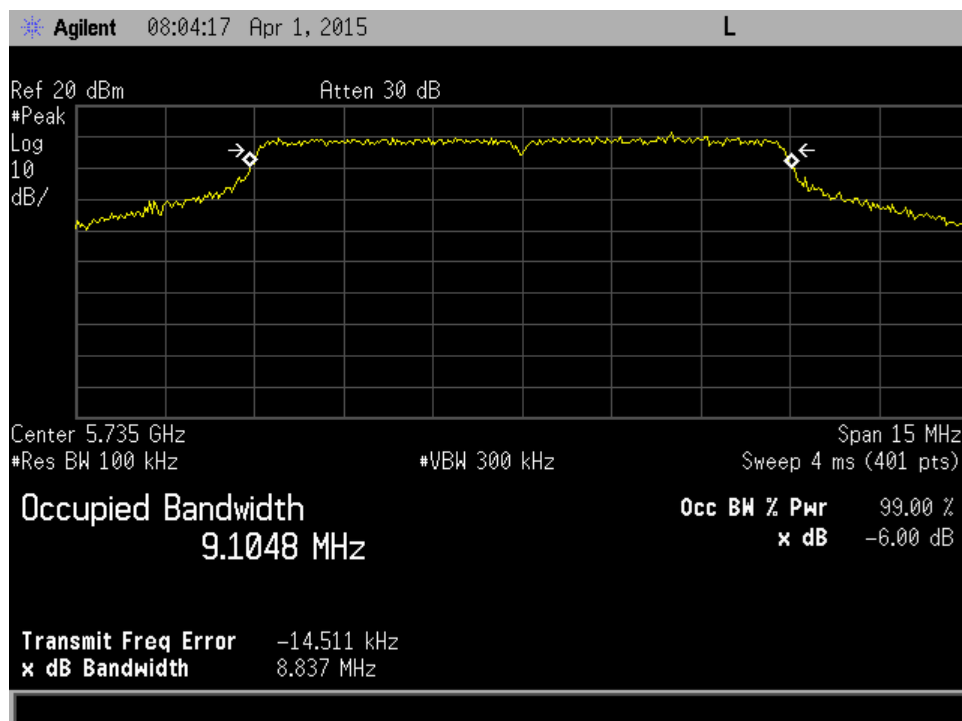
10Mhz high ch_J7



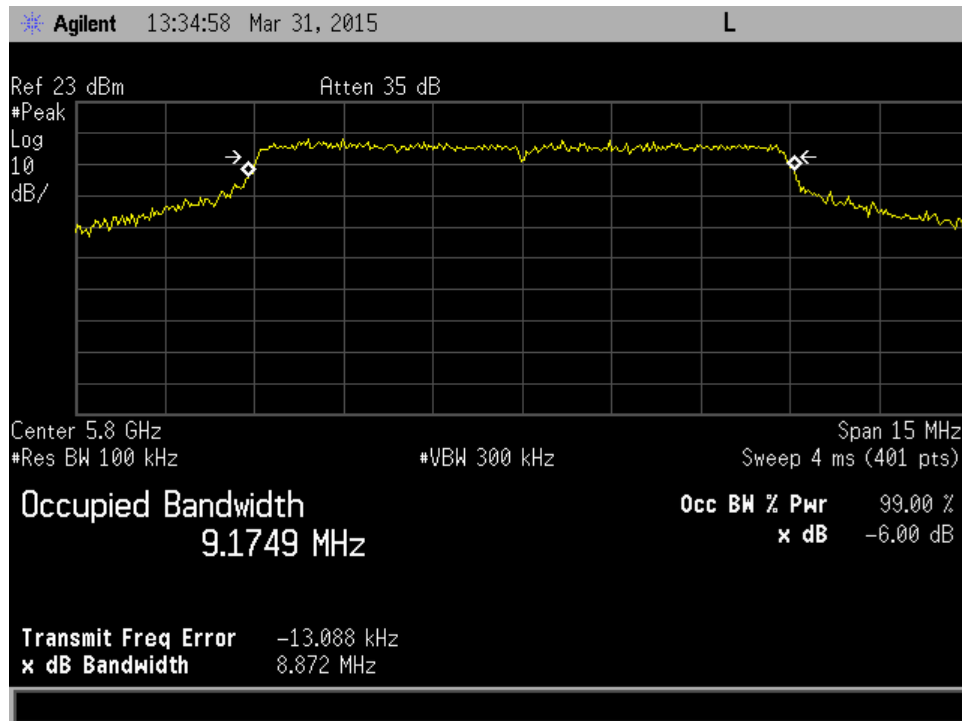
10Mhz high ch_J8



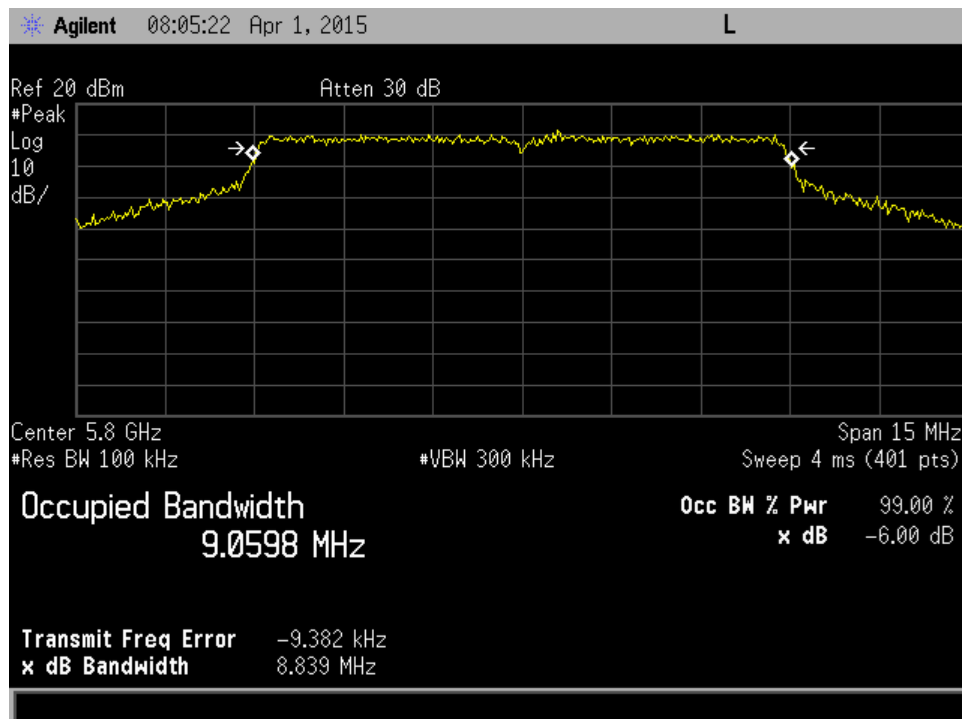
10Mhz low ch_J7



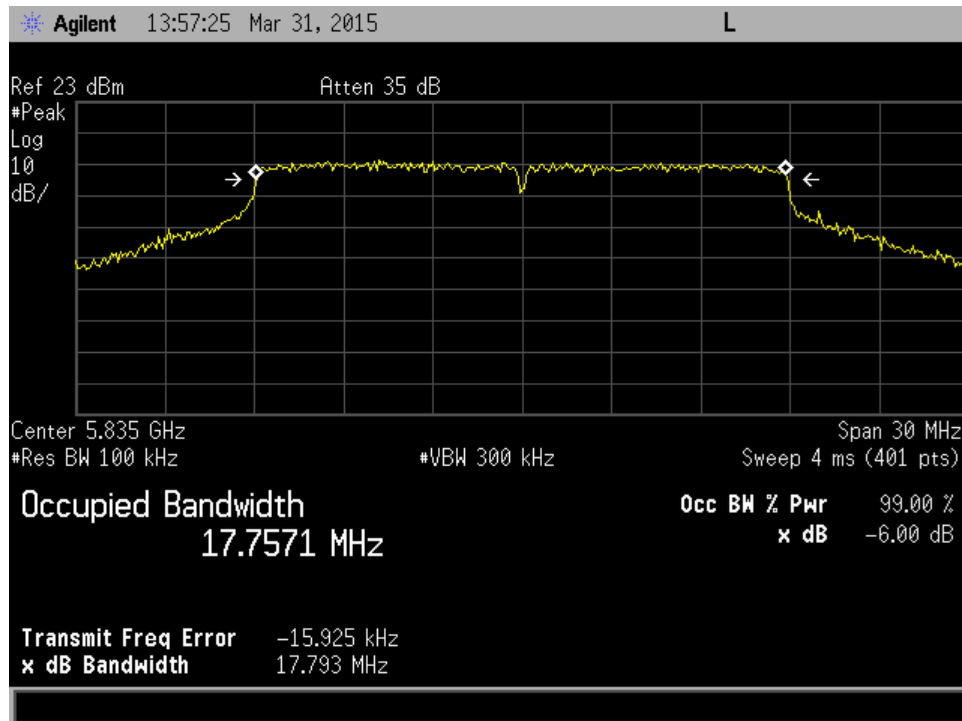
10Mhz low ch_J8



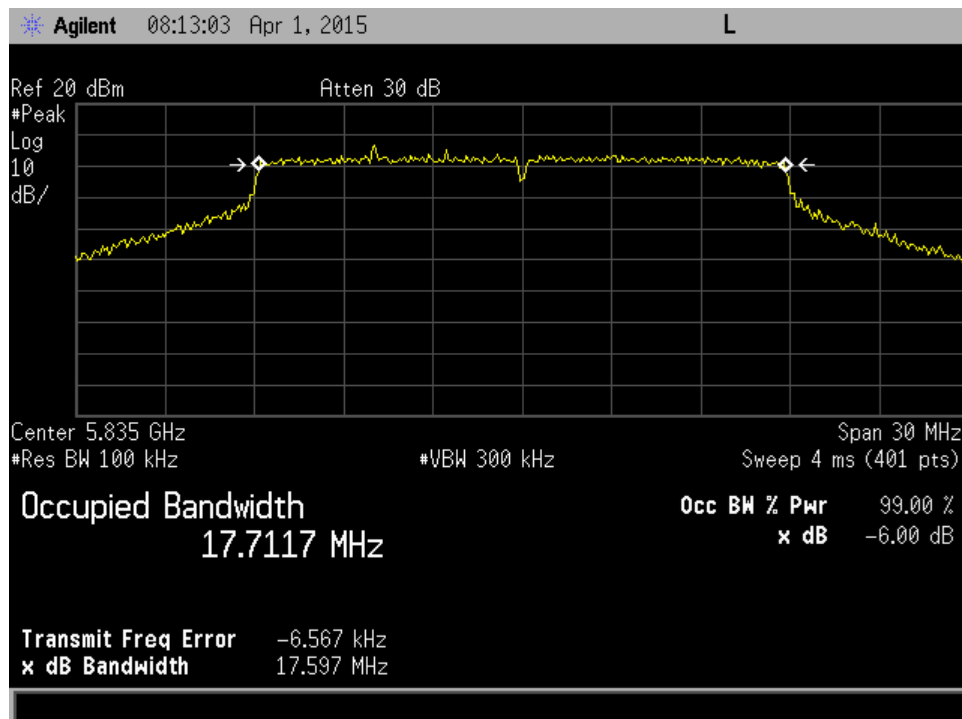
10Mhz mid ch_J7



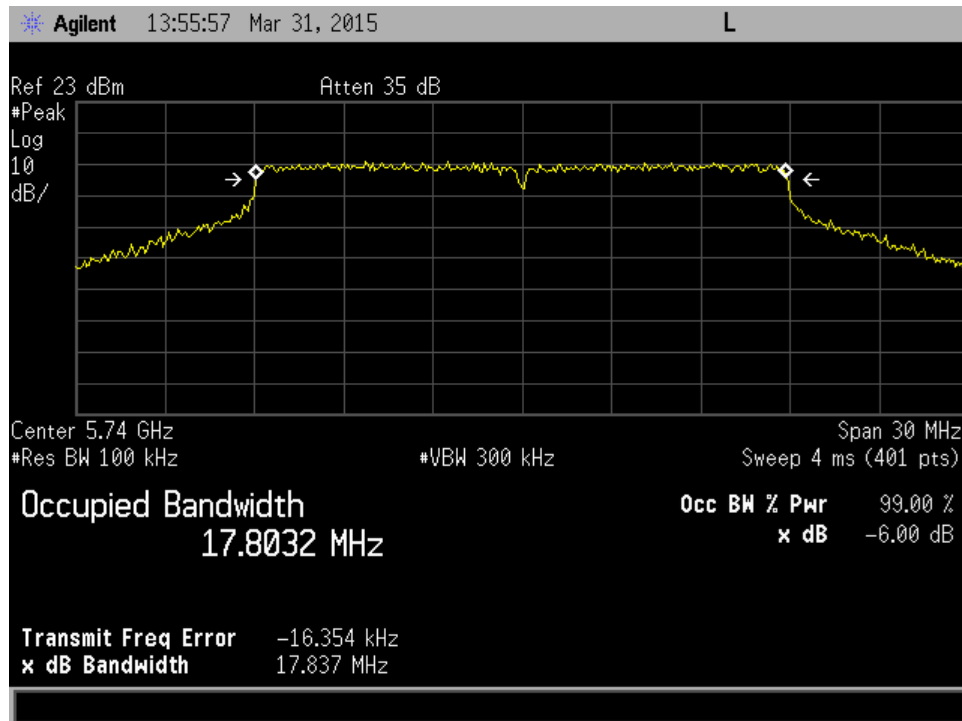
10Mhz mid ch_J8



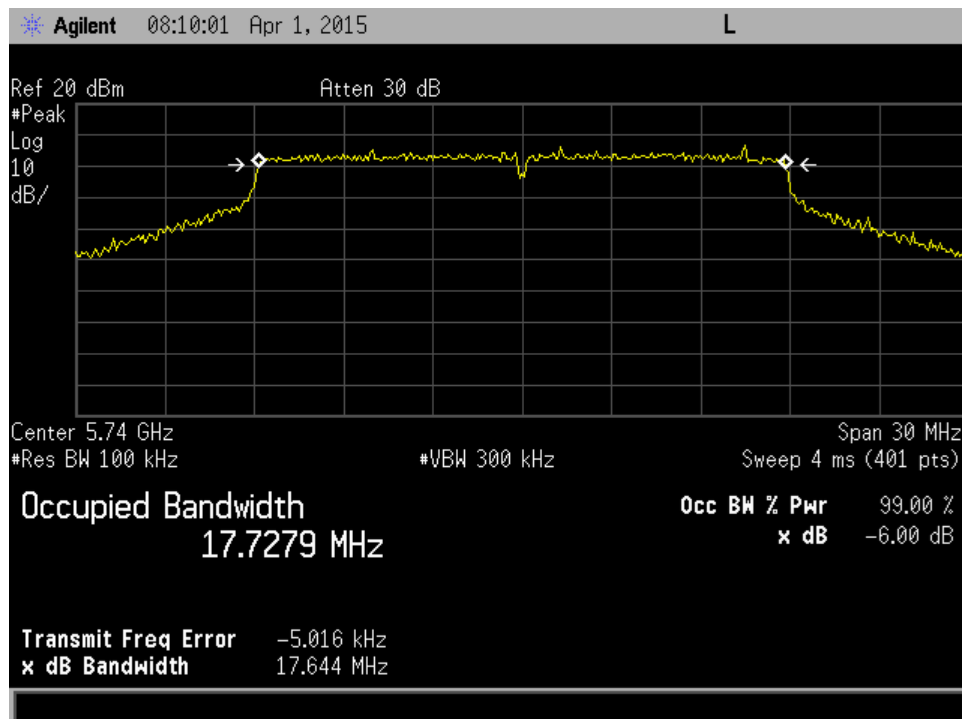
20Mhz high ch_J7



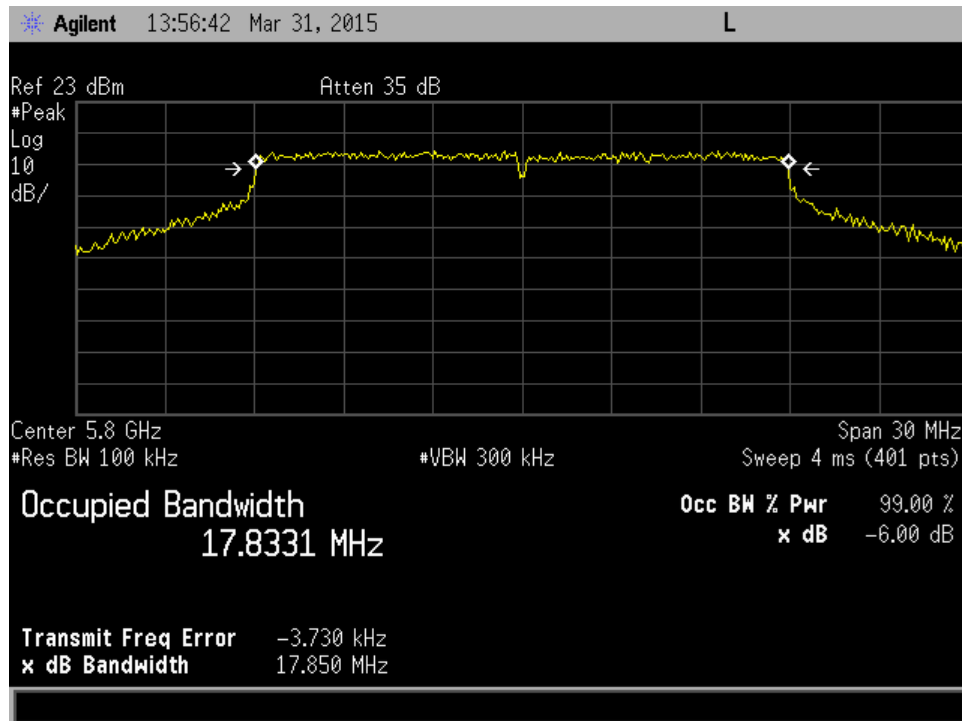
20Mhz high ch_J8



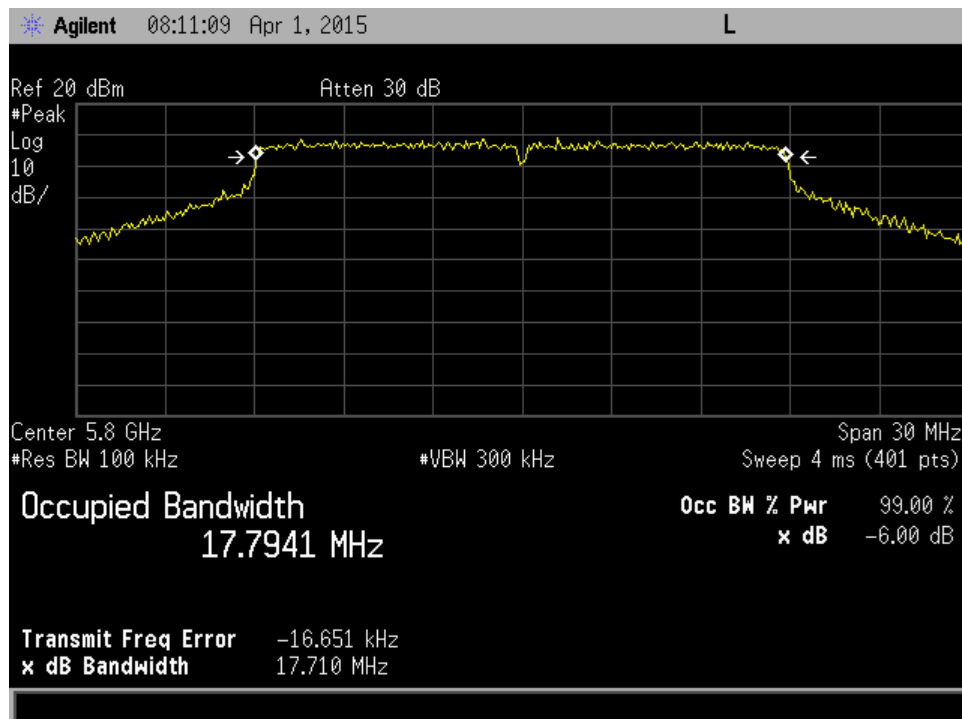
20Mhz low ch_J7



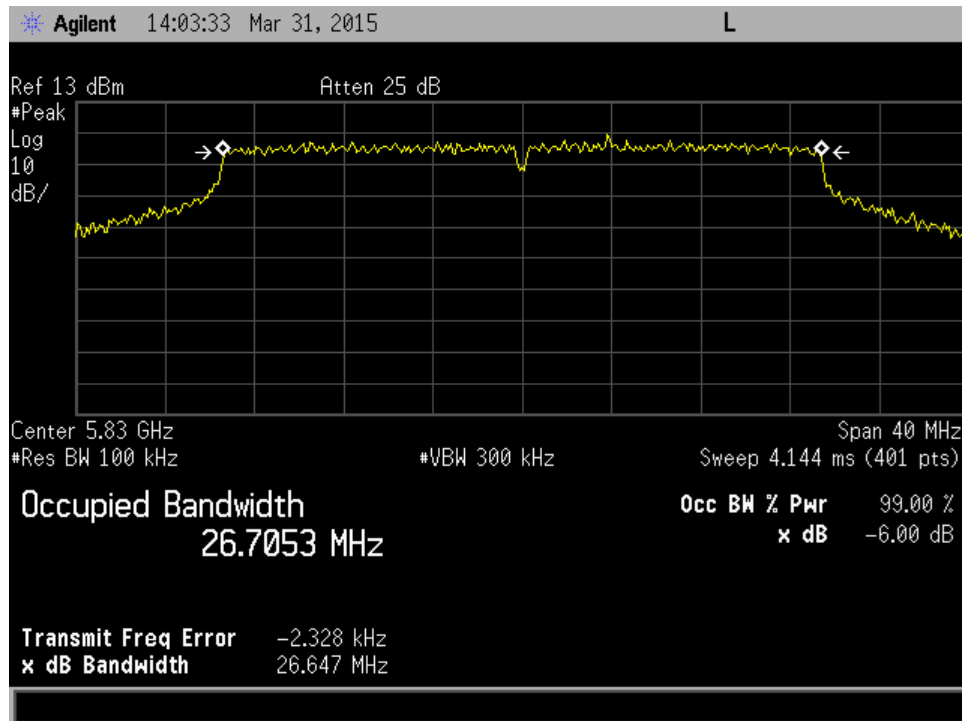
20Mhz low ch_J8



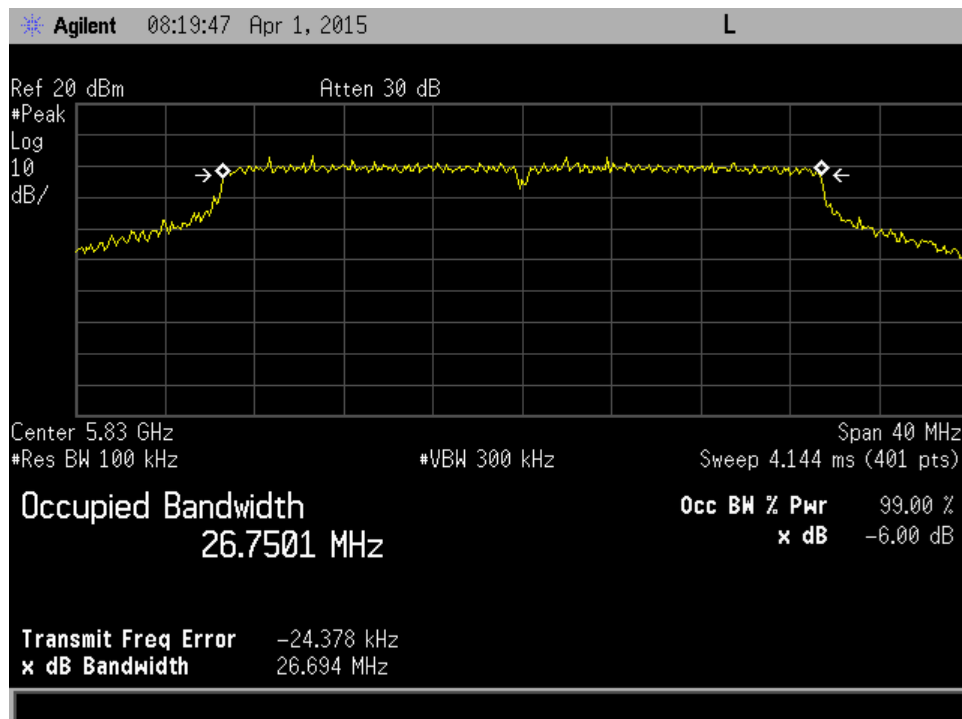
20Mhz mid ch_J7



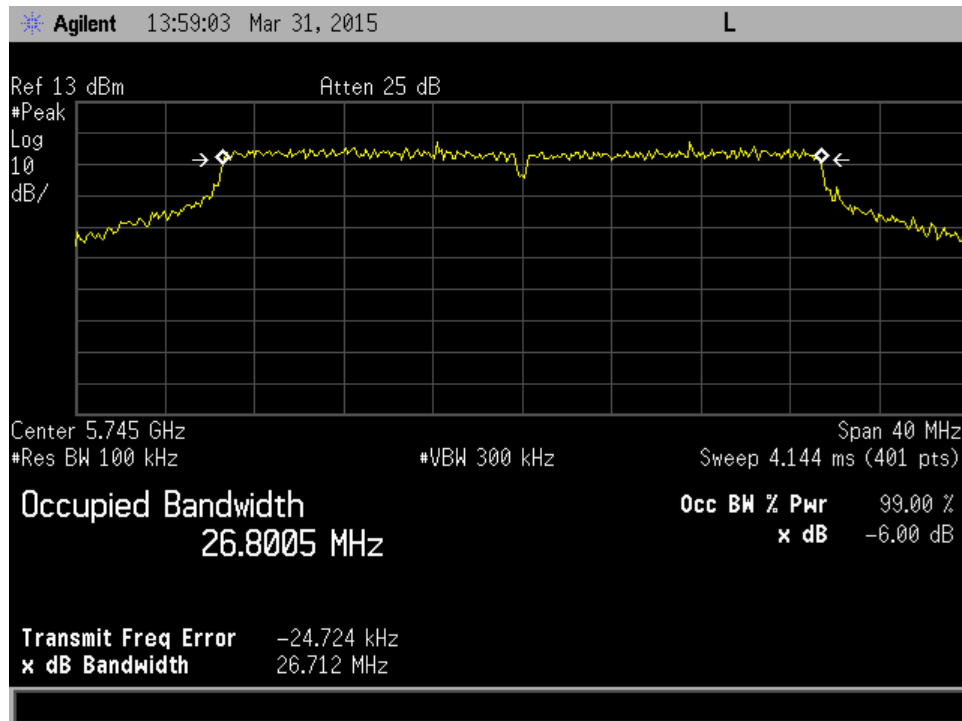
20Mhz mid ch_J8



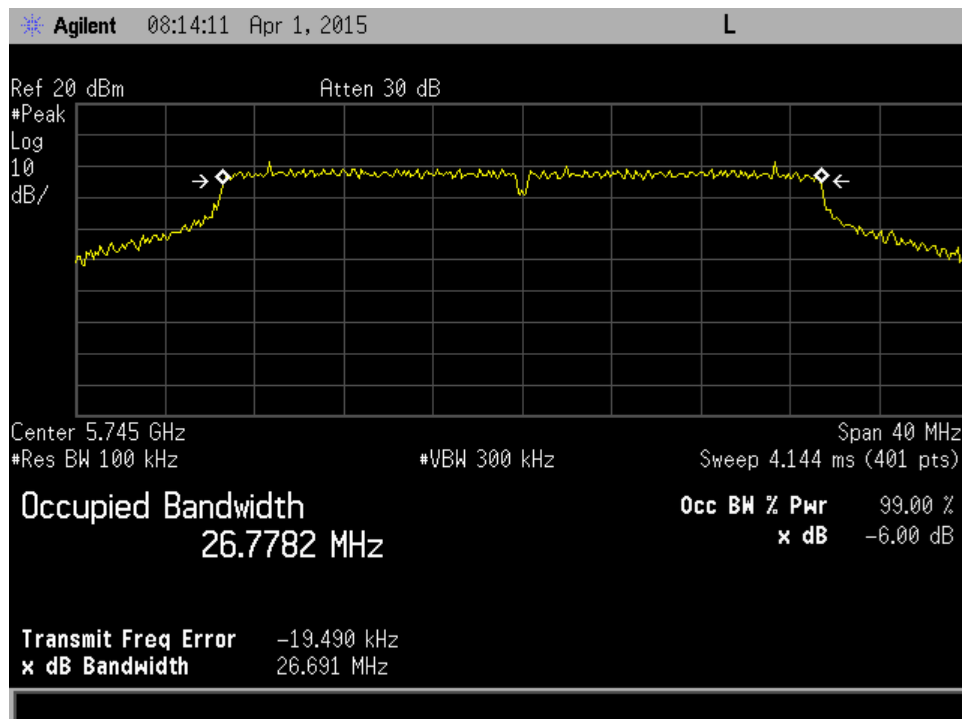
30Mhz high ch_J7



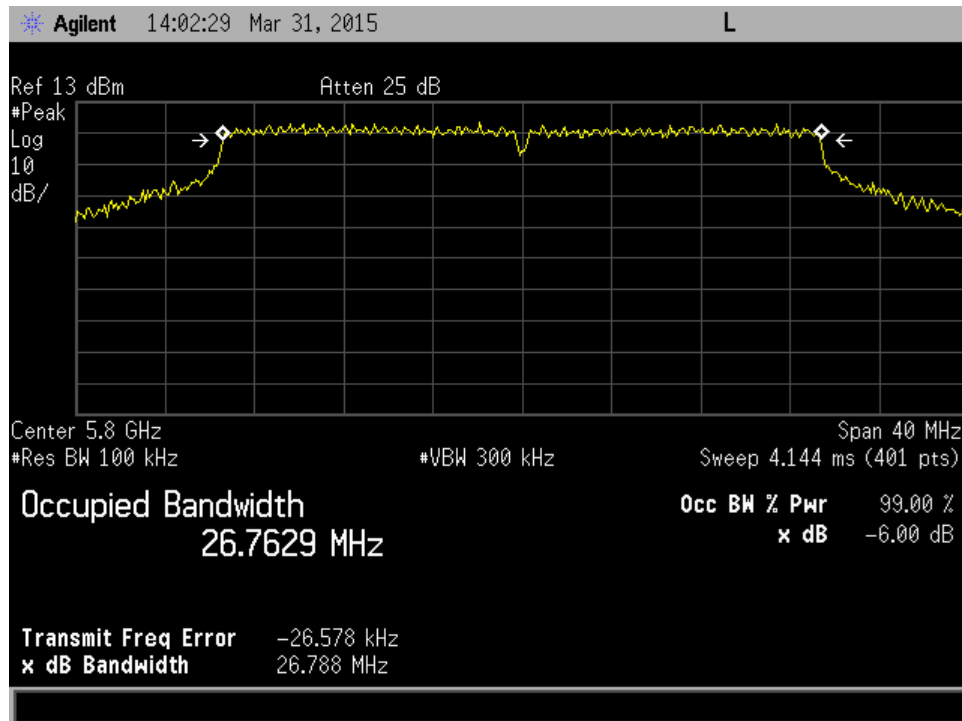
30Mhz high ch_J8



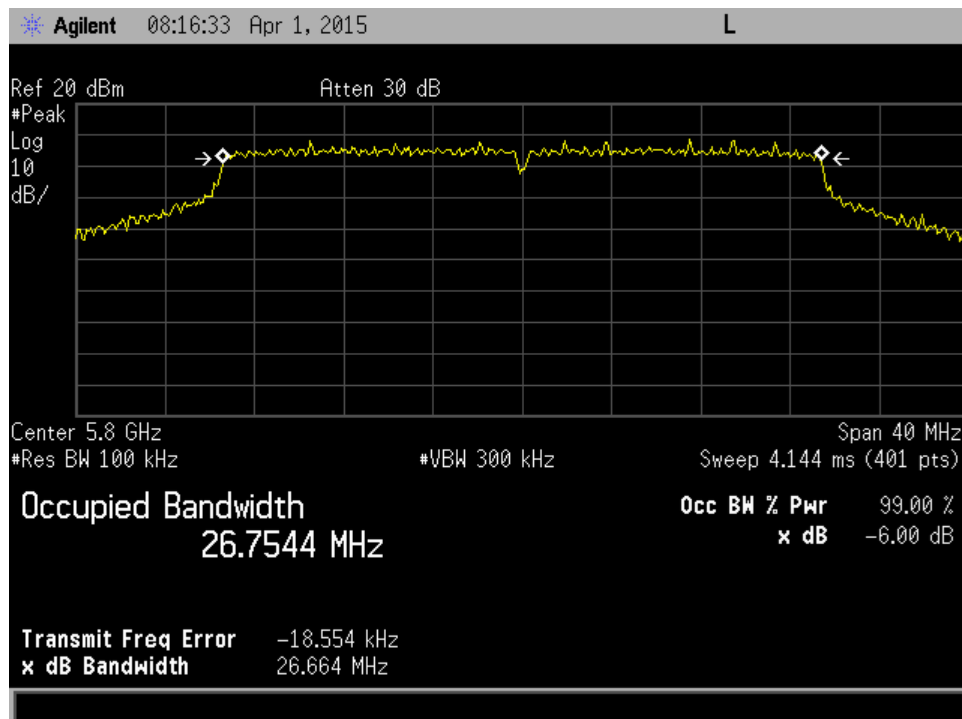
30Mhz low ch_J7



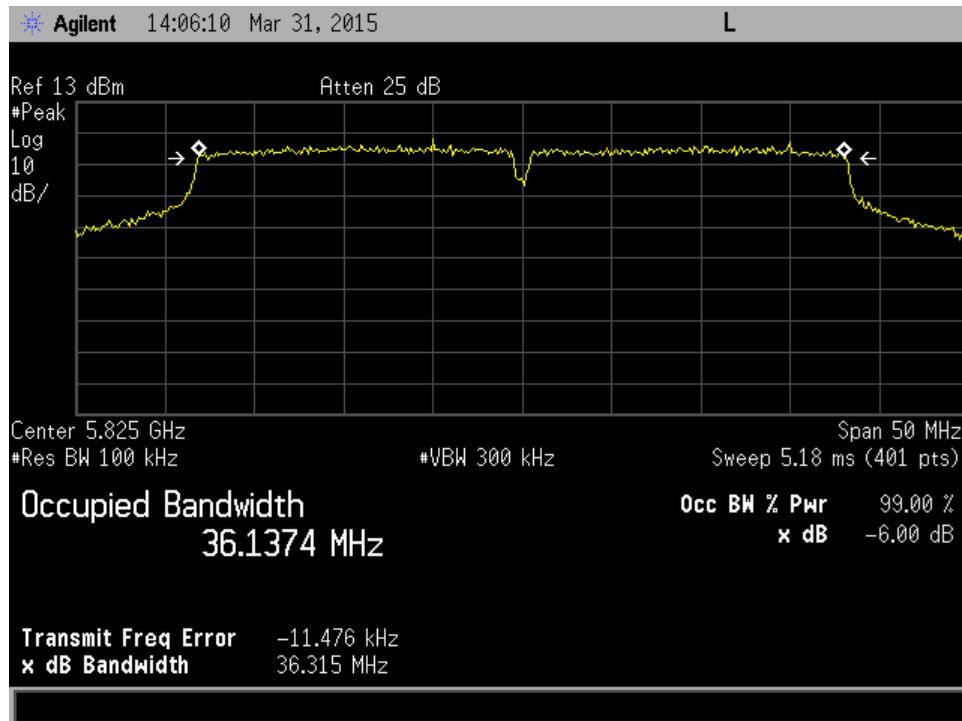
30Mhz low ch_J8



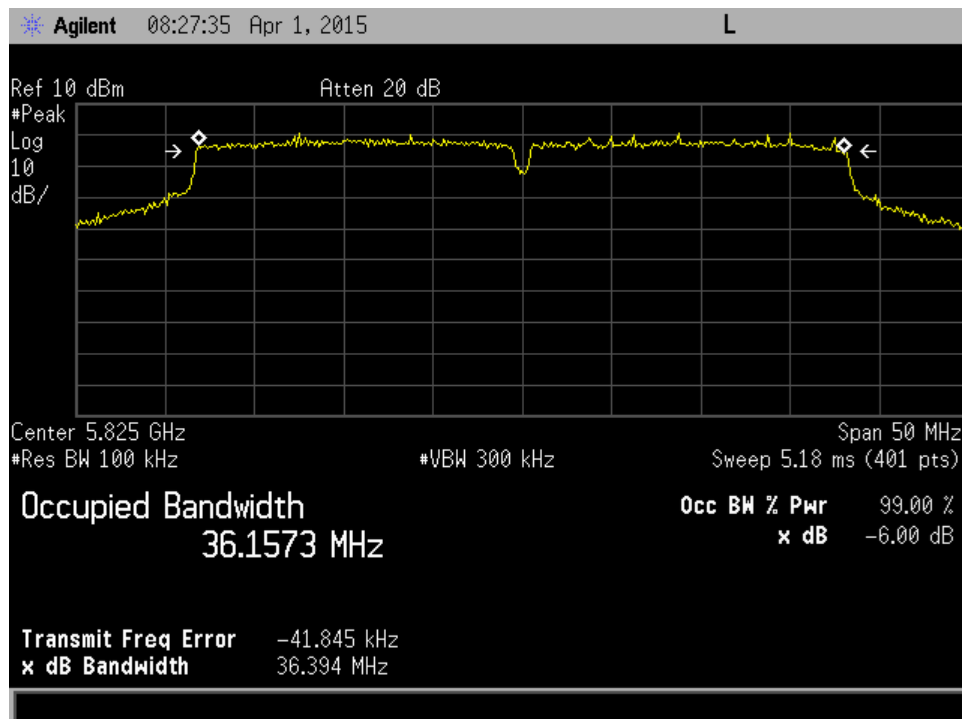
30Mhz mid ch_J7



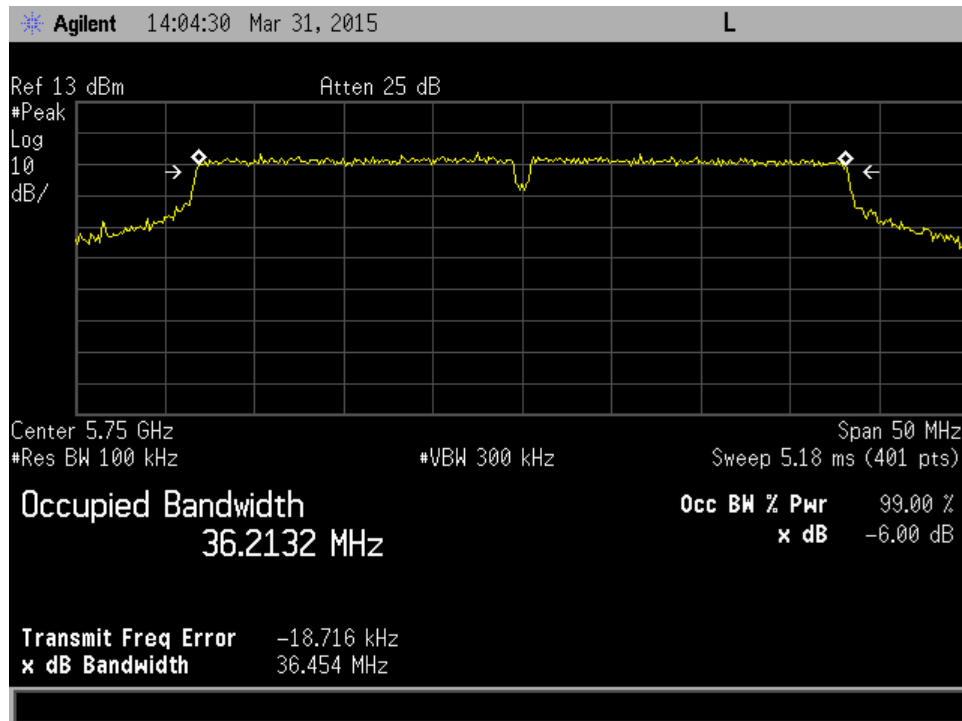
30Mhz mid ch_J8



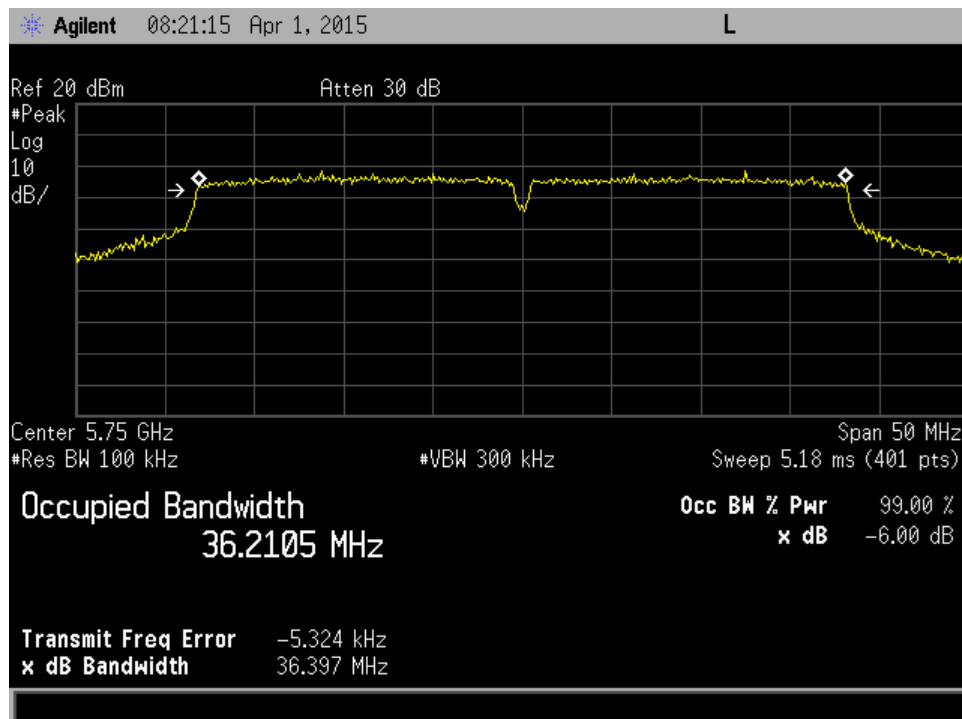
40Mhz high ch_J7



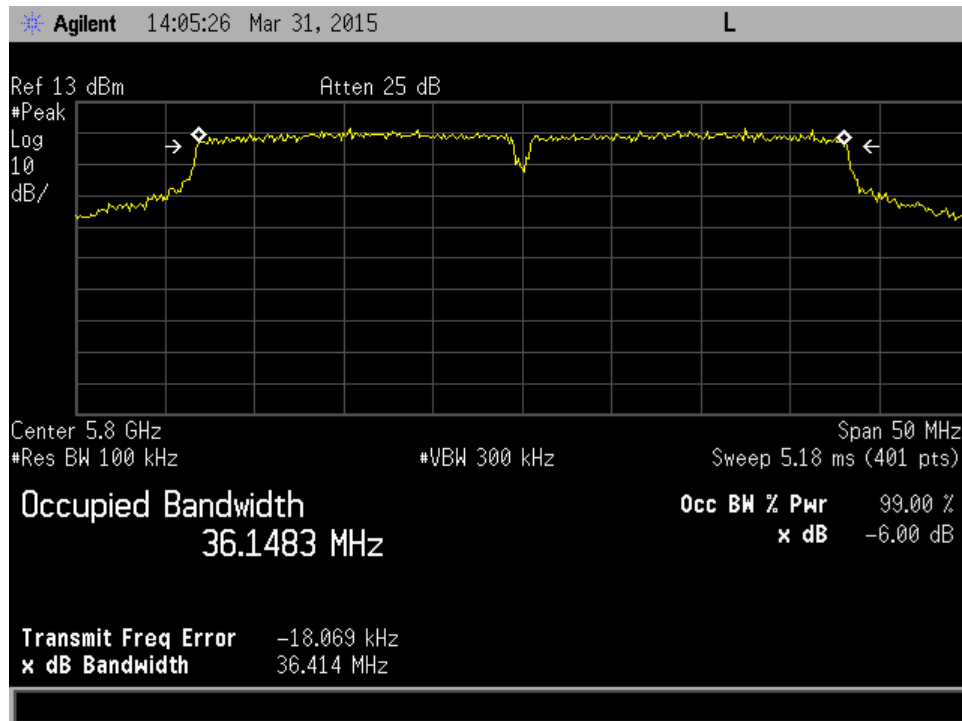
40Mhz high ch_J8



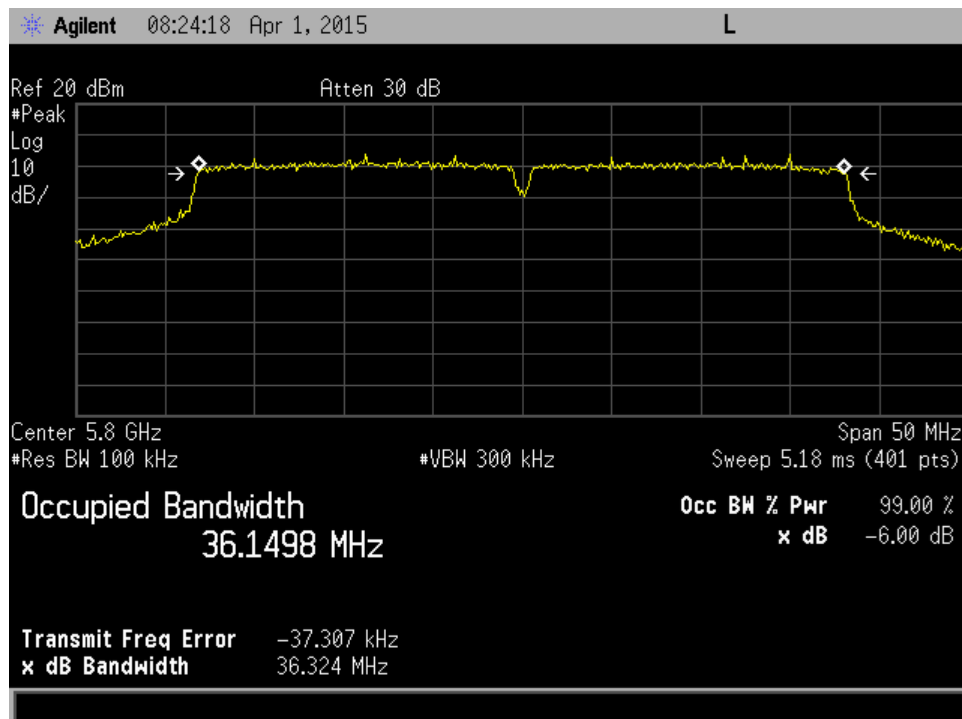
40Mhz low ch_J7



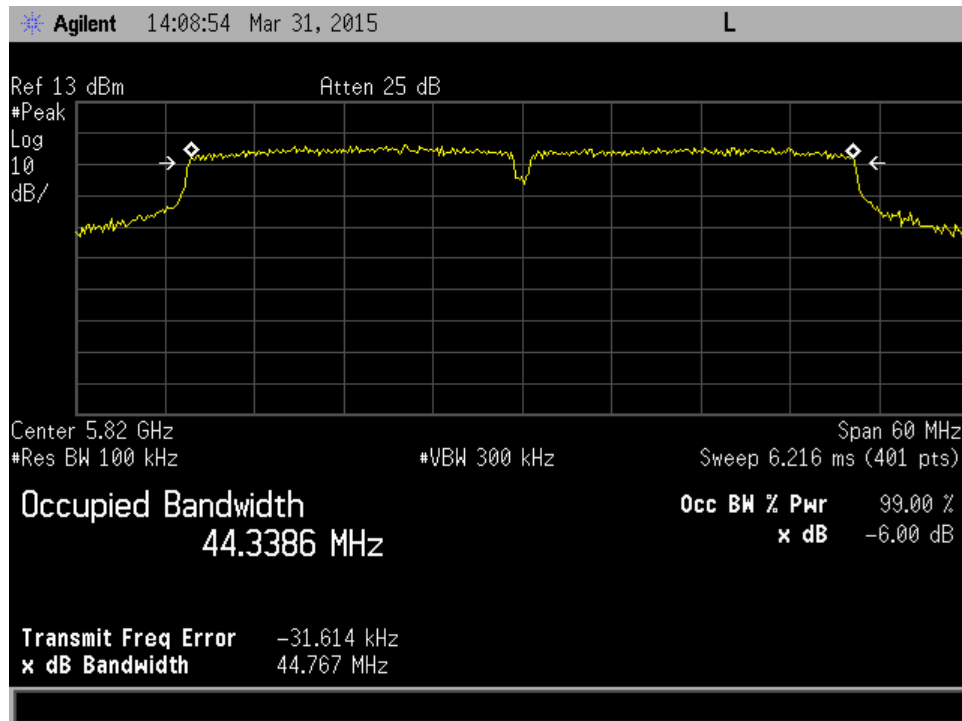
40Mhz low ch_J8



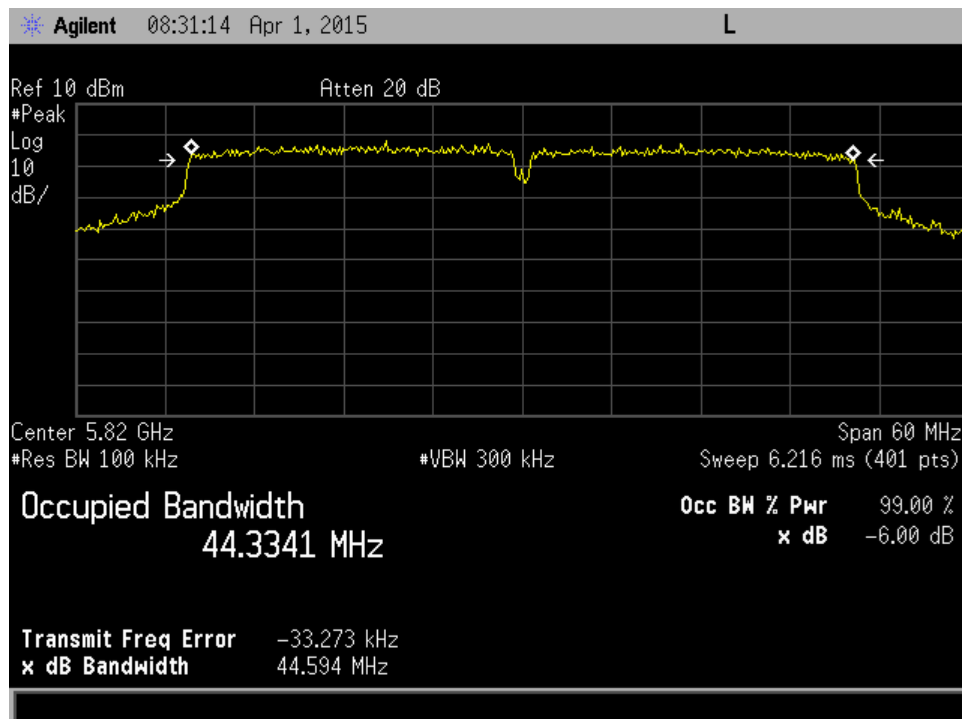
40Mhz mid ch_J7



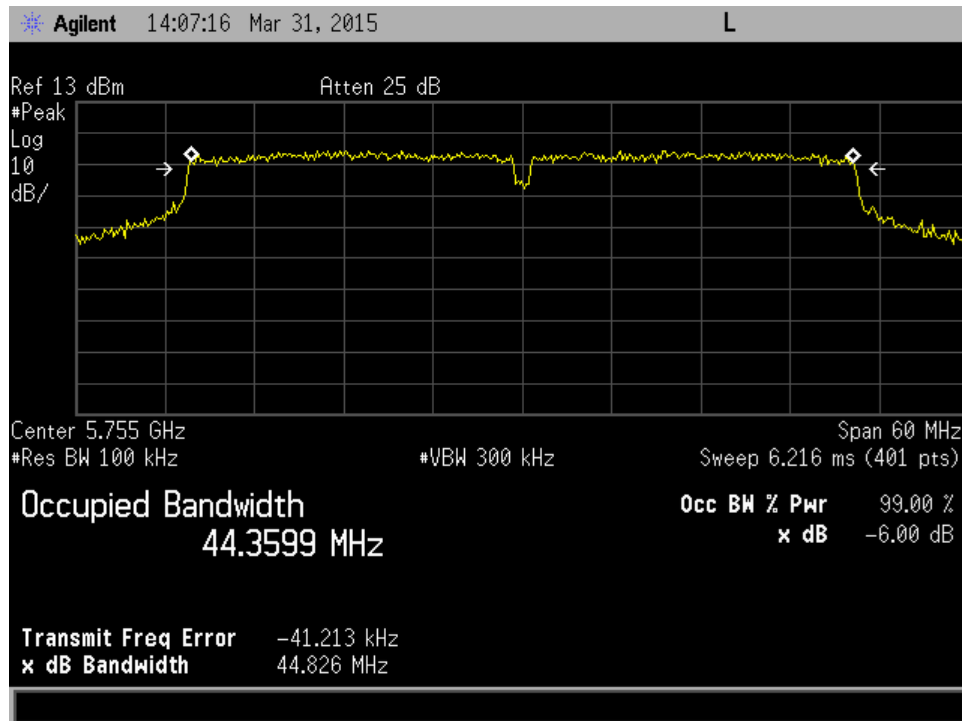
40Mhz mid ch_J8



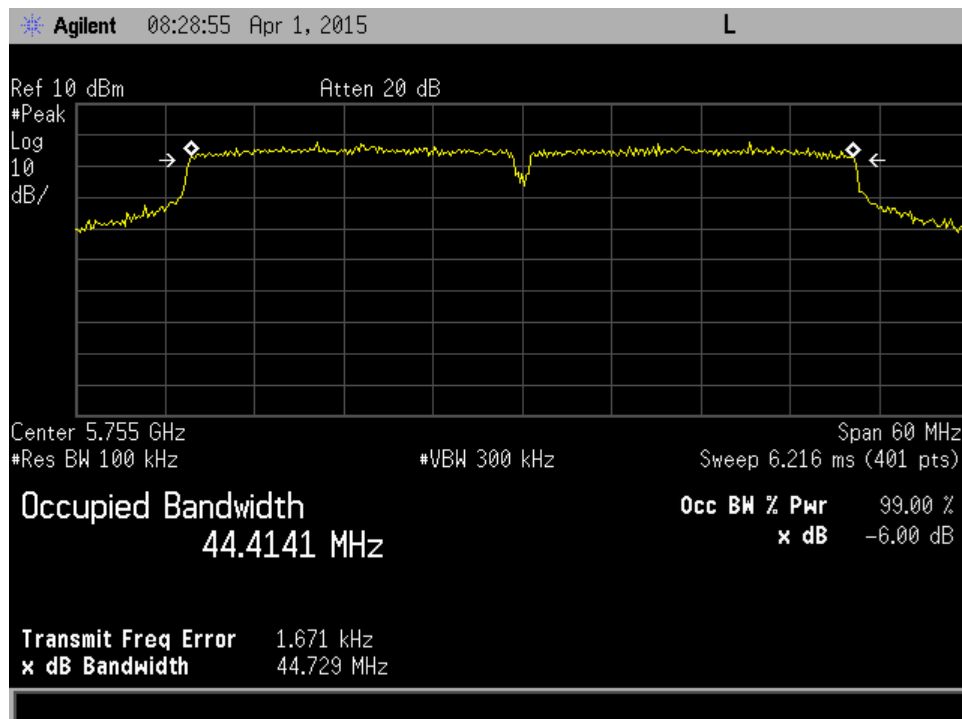
50Mhz high ch_J7



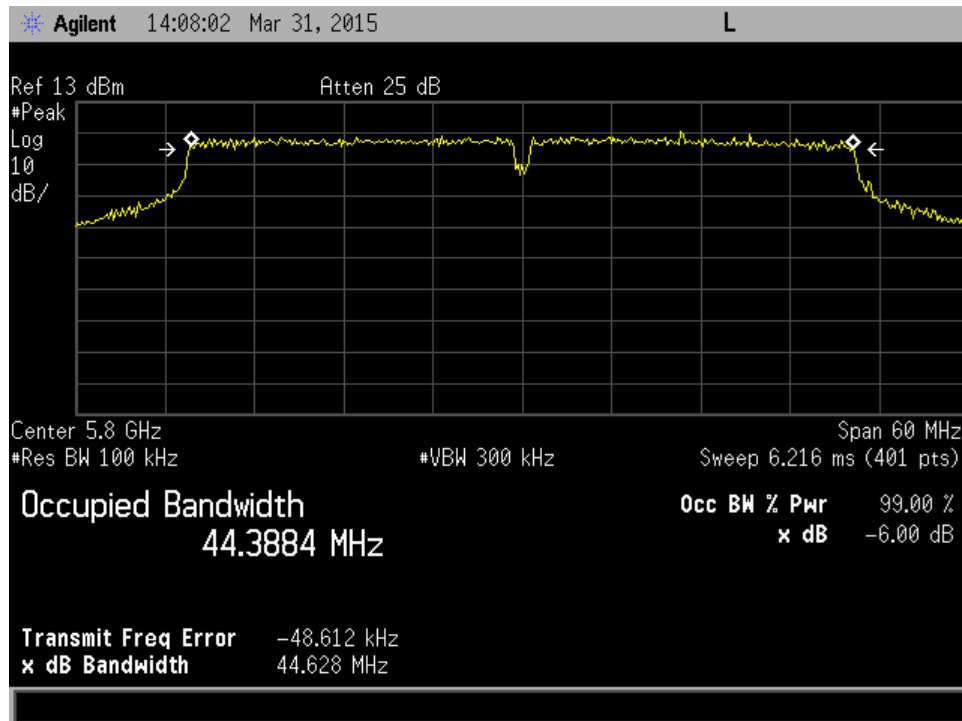
50Mhz high ch_J8



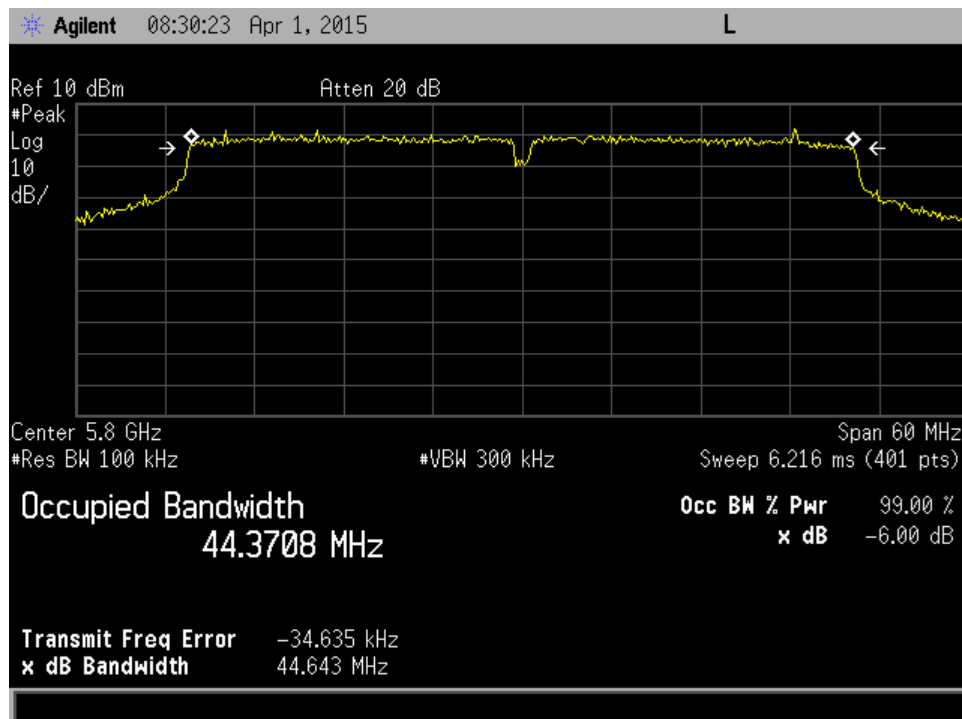
50Mhz low ch_J7



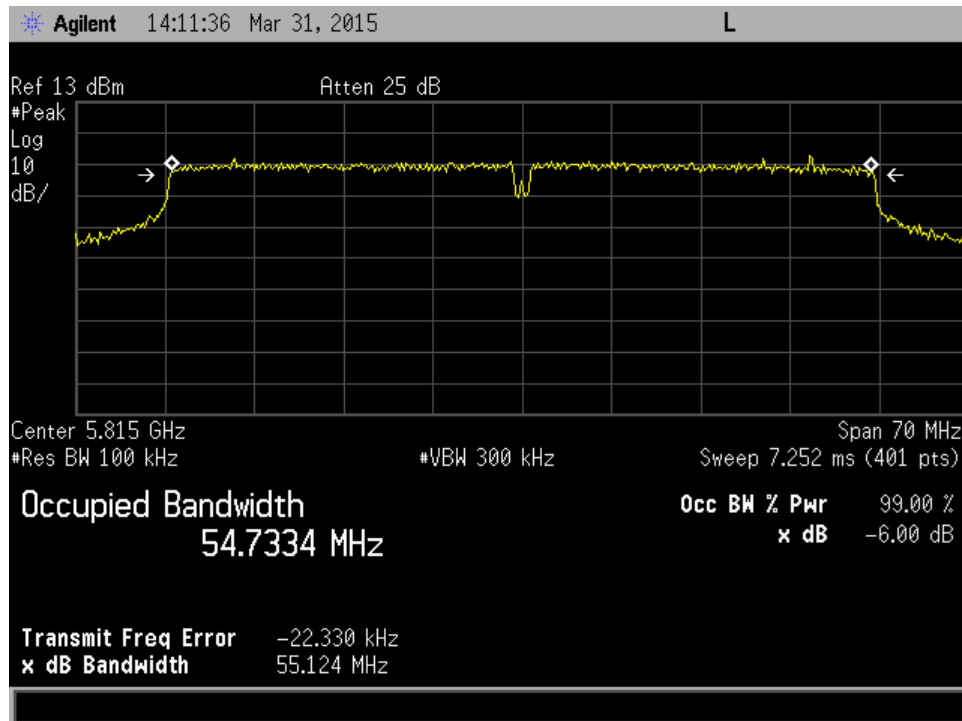
50Mhz low ch_J8



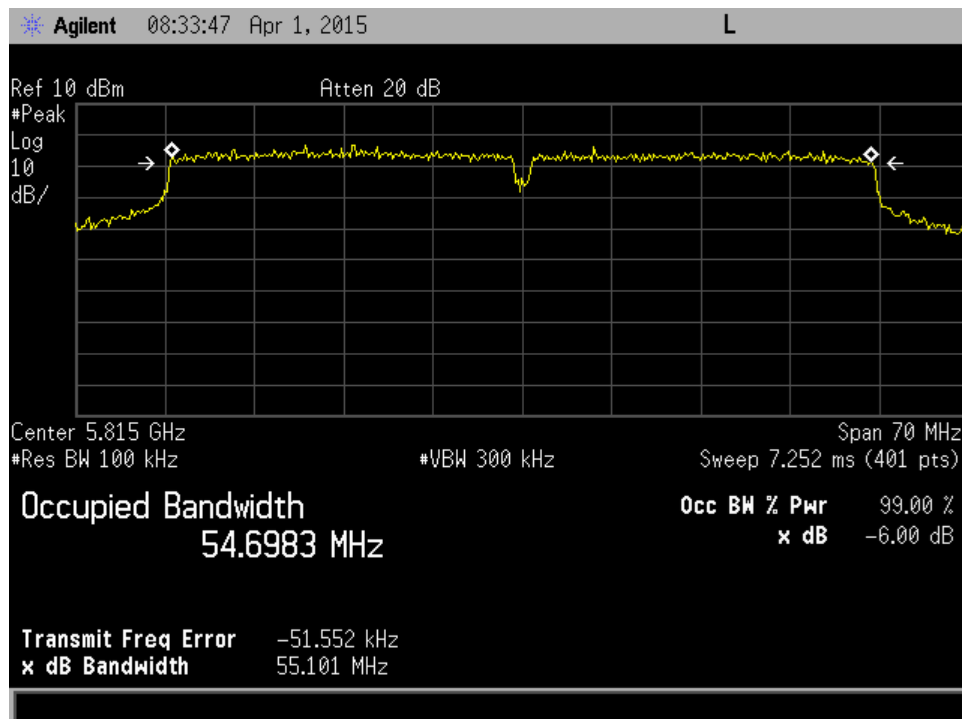
50Mhz mid ch_J7



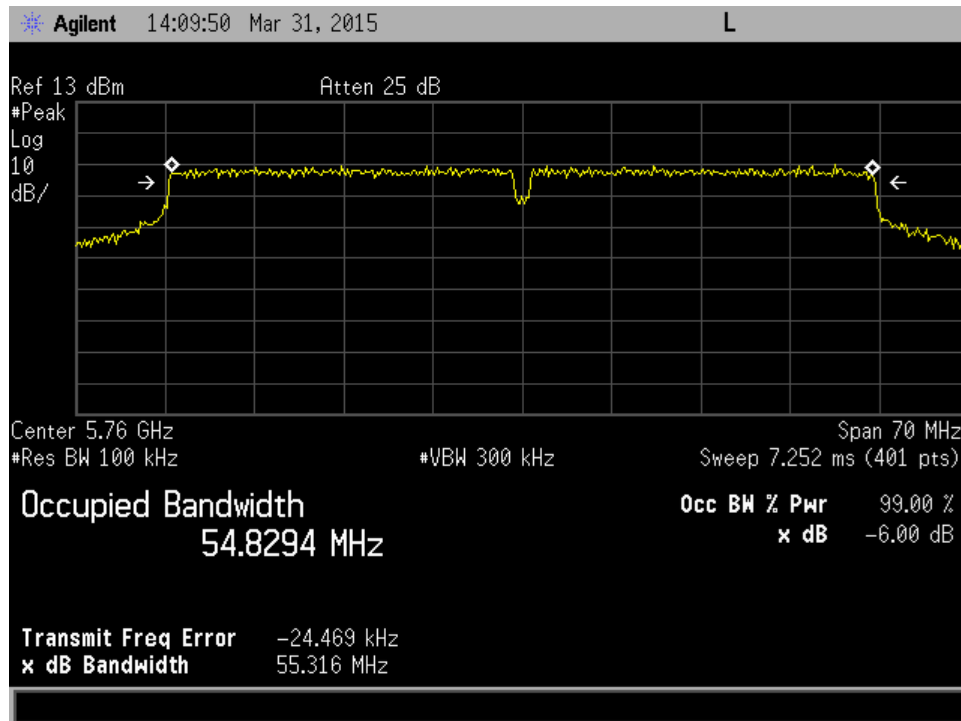
50Mhz mid ch_J8



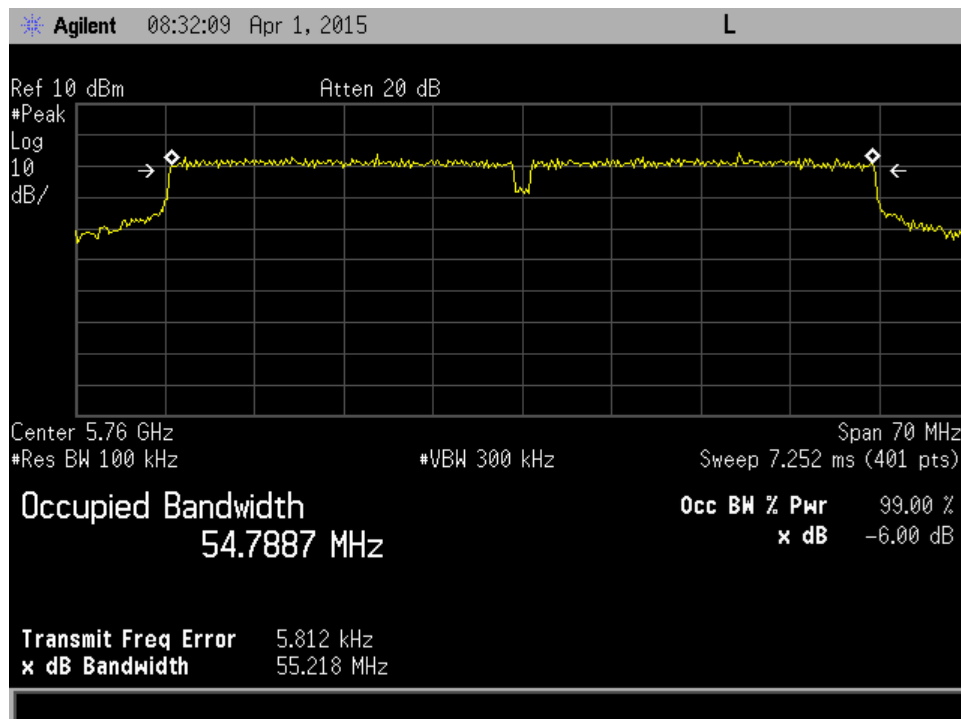
60Mhz high ch_J7



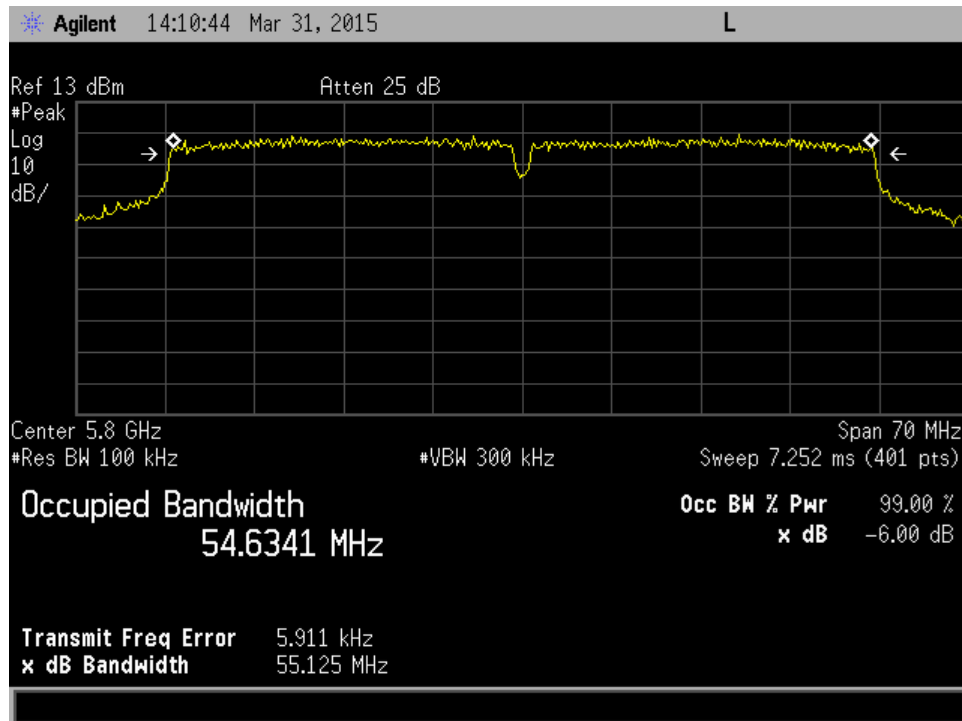
60Mhz high ch_J8



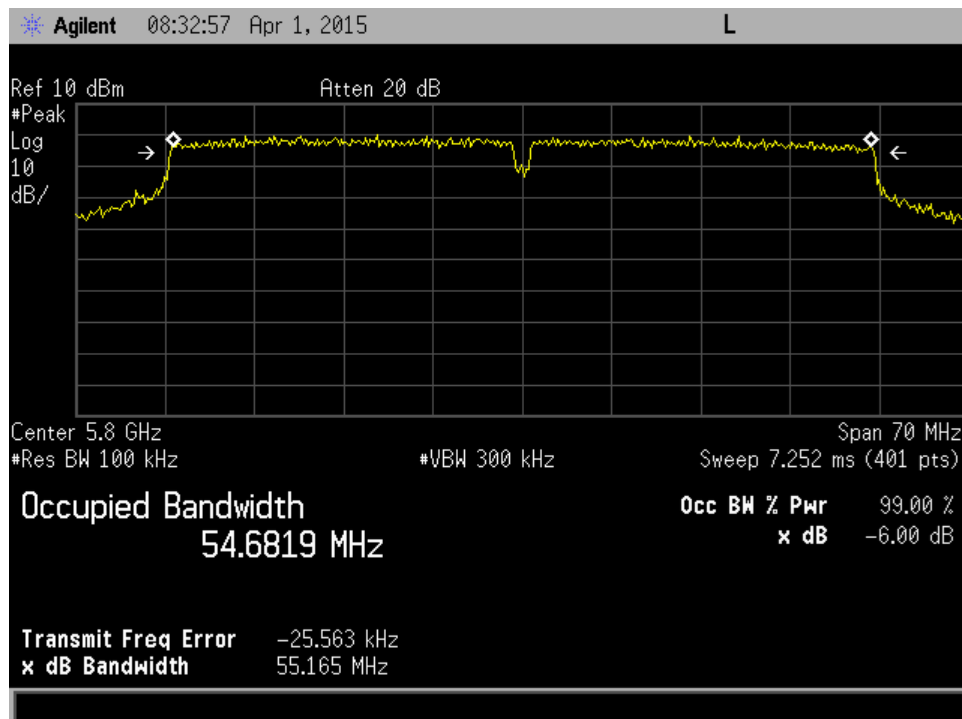
60Mhz low ch_J7



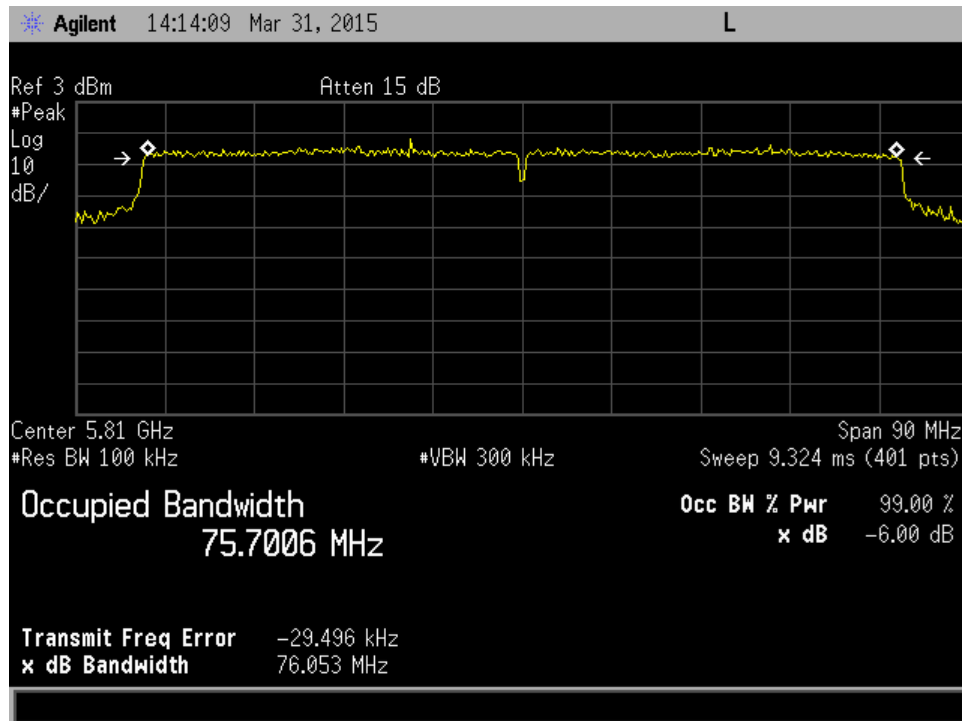
60Mhz low ch_J8



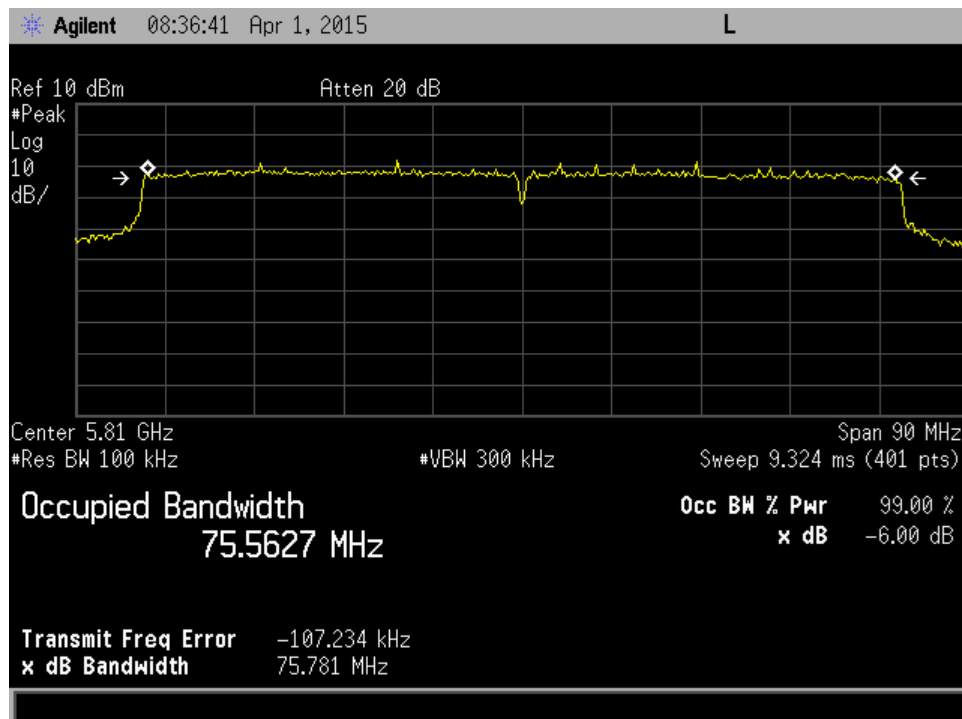
60Mhz mid ch_J7



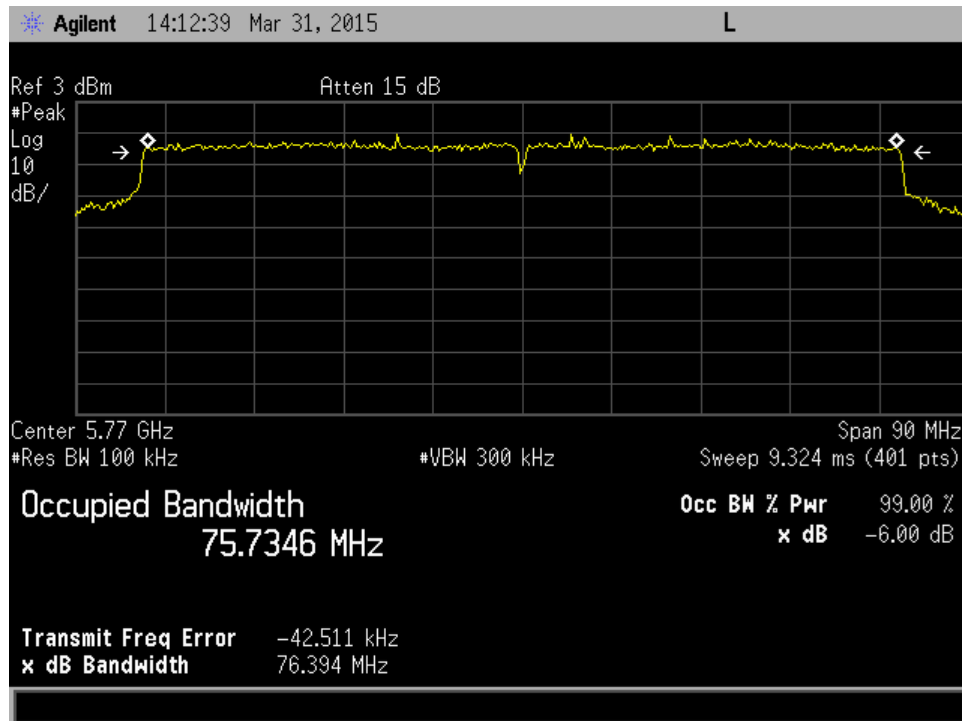
60Mhz mid ch_J8



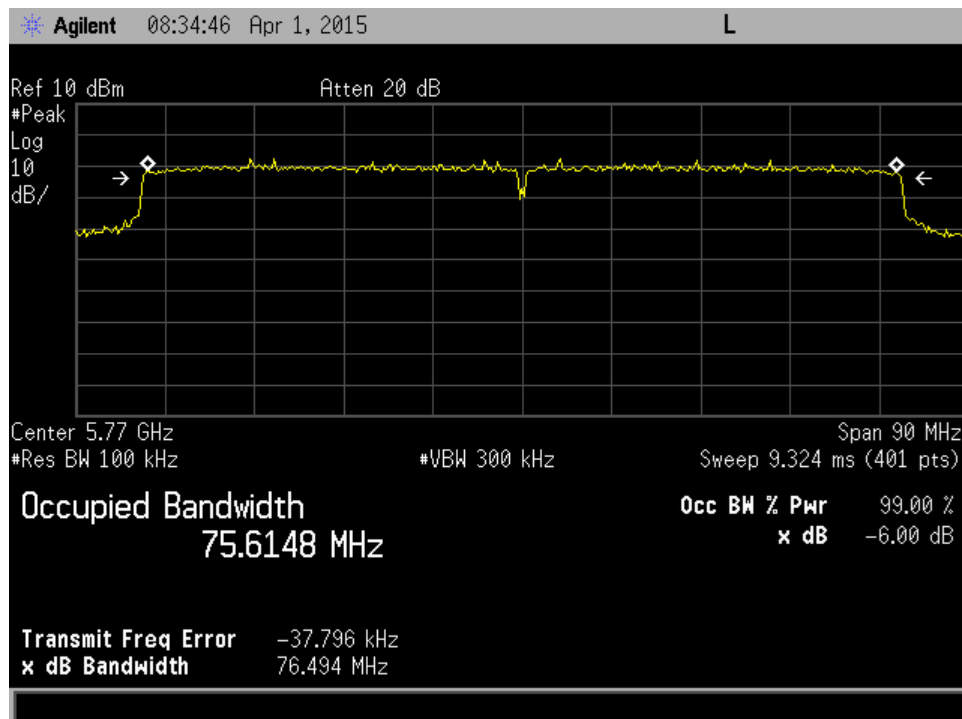
80Mhz high ch_J7



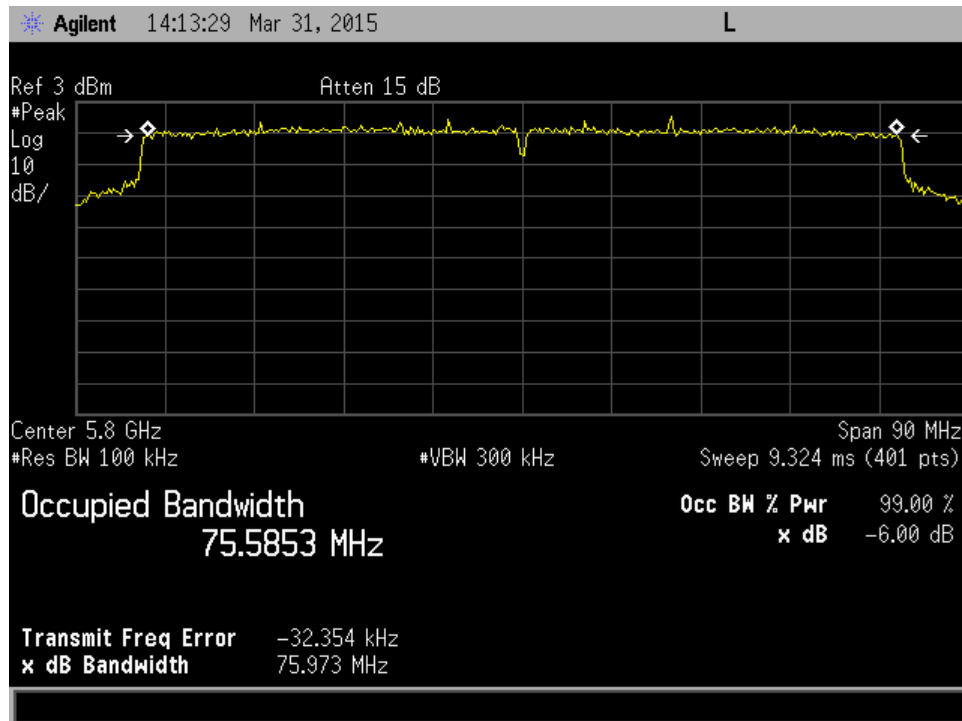
80Mhz high ch_J8



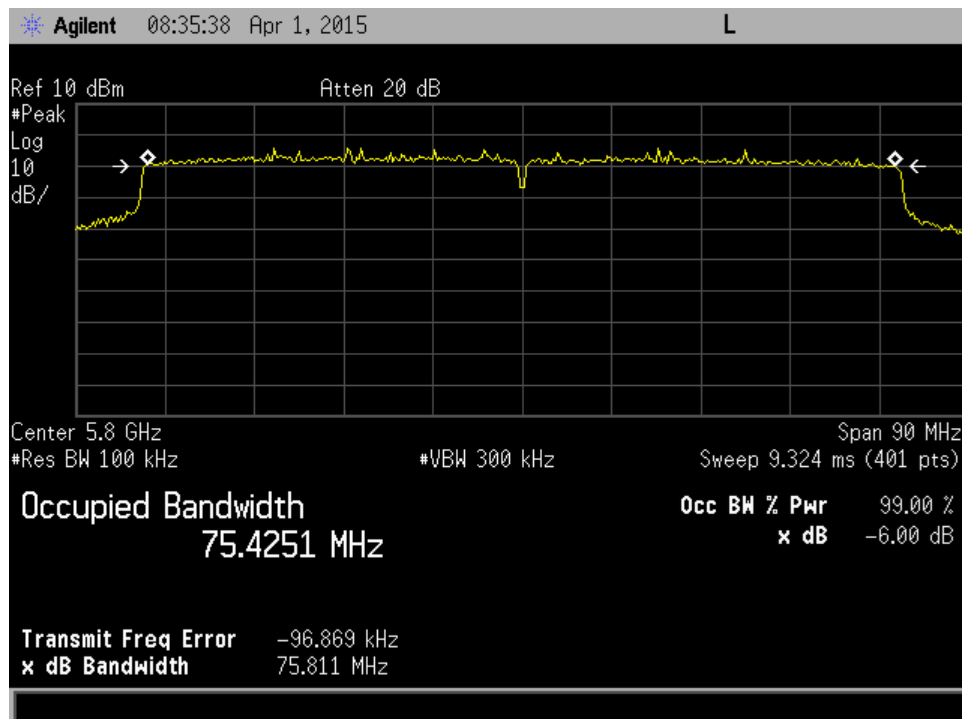
80Mhz low ch_J7



80Mhz low ch_J8



80Mhz mid ch_J7



80Mhz mid ch_J8