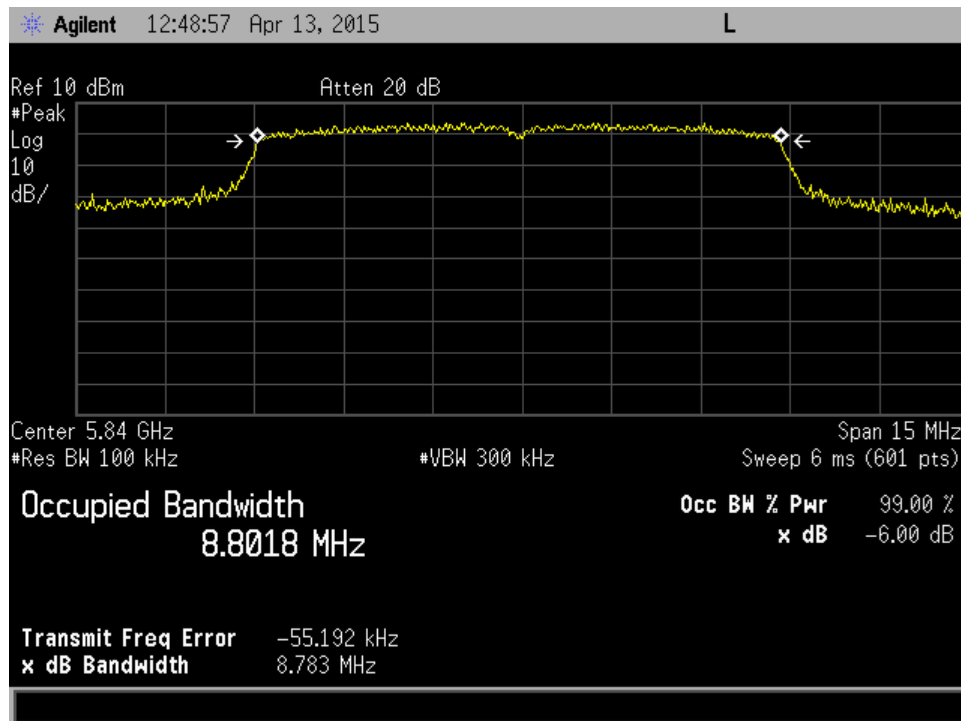




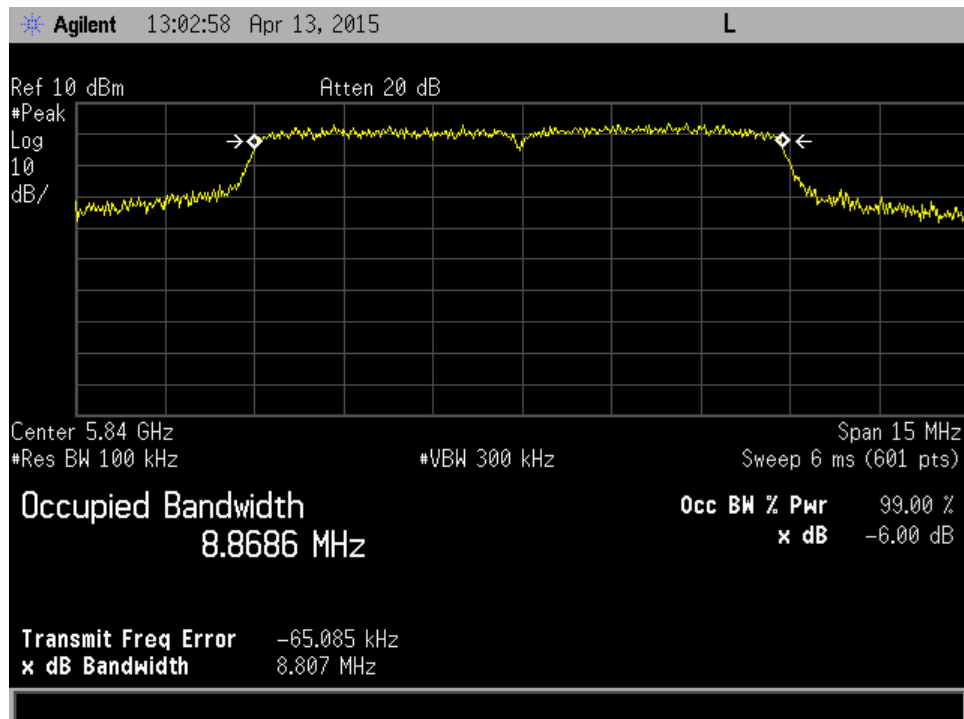
Annex D

Occupied Bandwidth

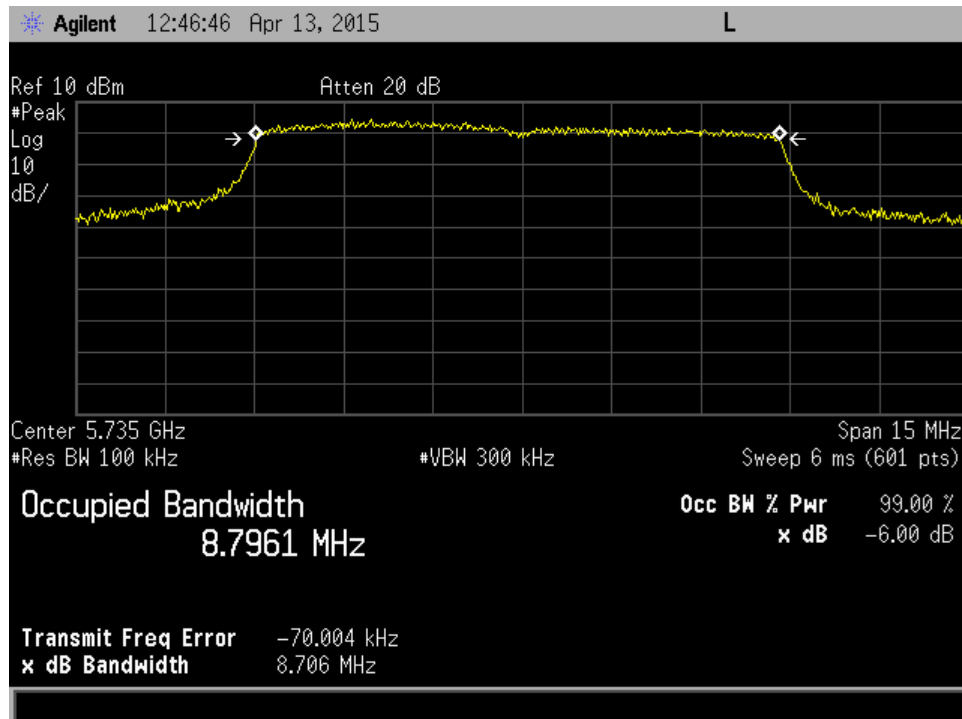
Ports J3 and J4



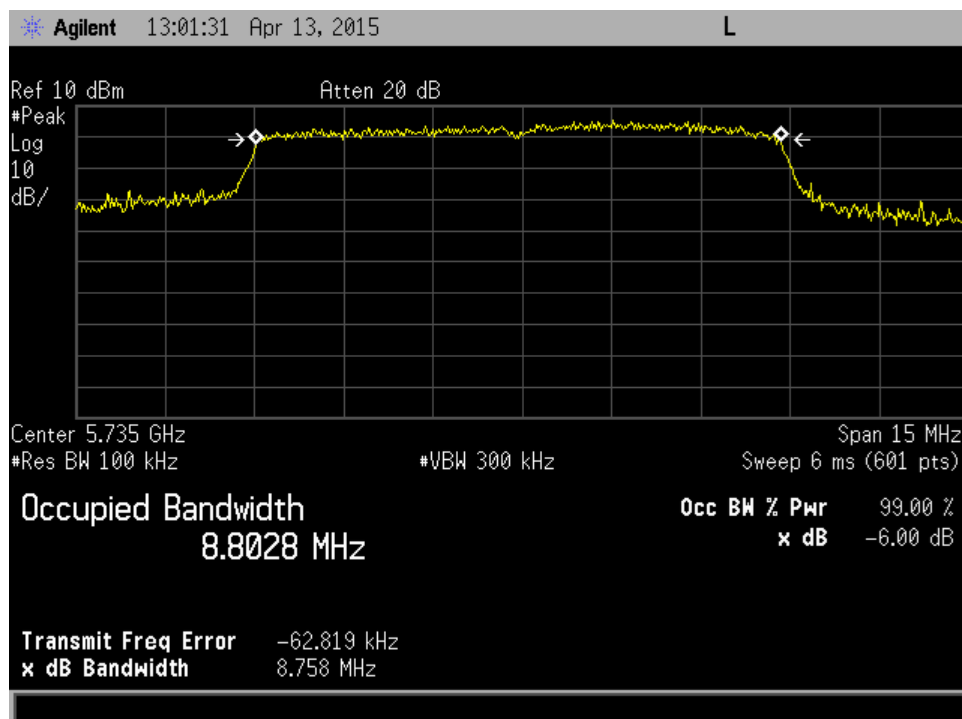
10MHz high ch_J3



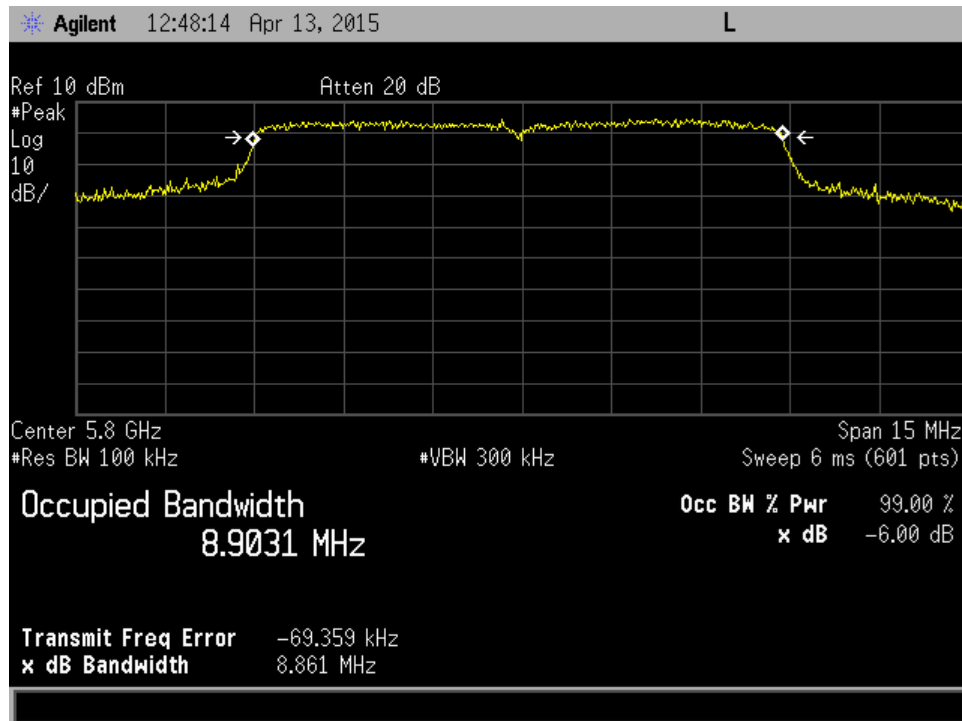
10MHz high ch_J4



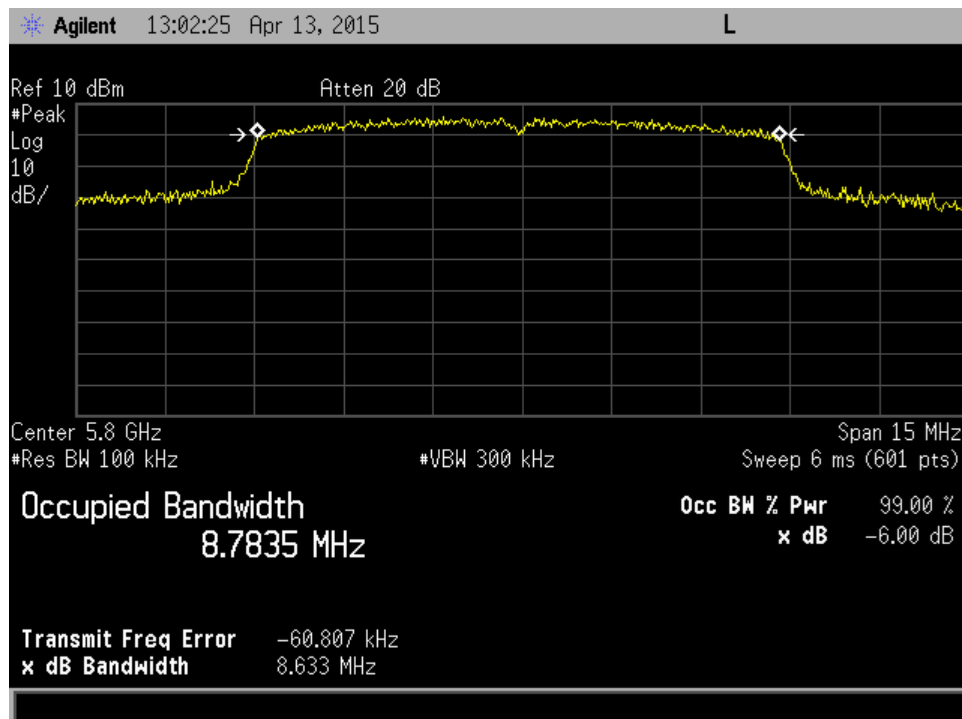
10MHz low ch_J3



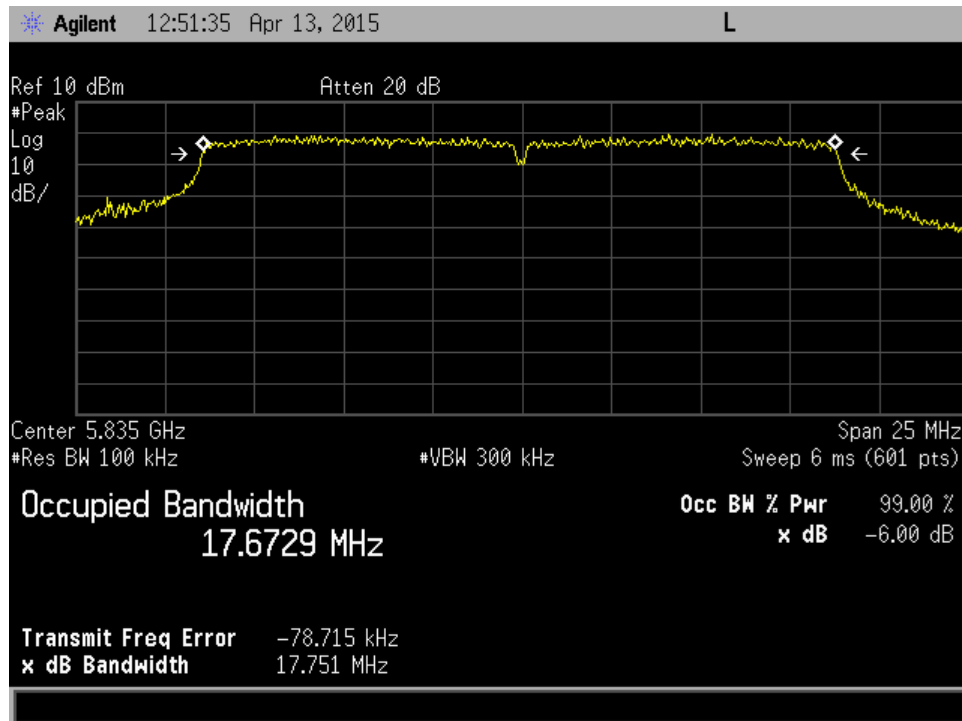
10MHz low ch_J4



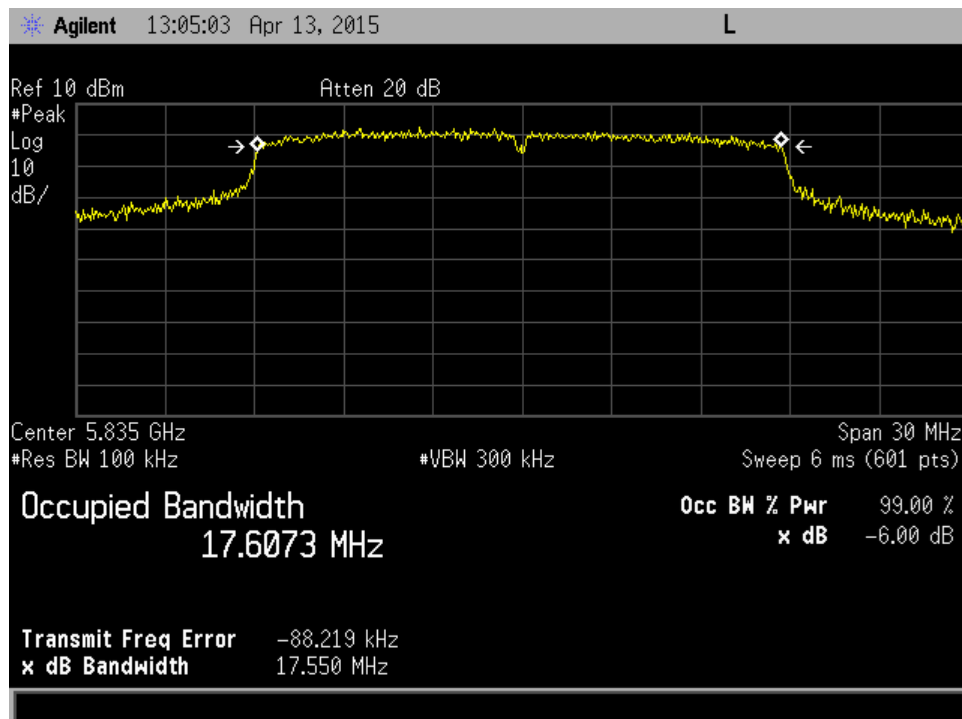
10MHz mid ch_J3



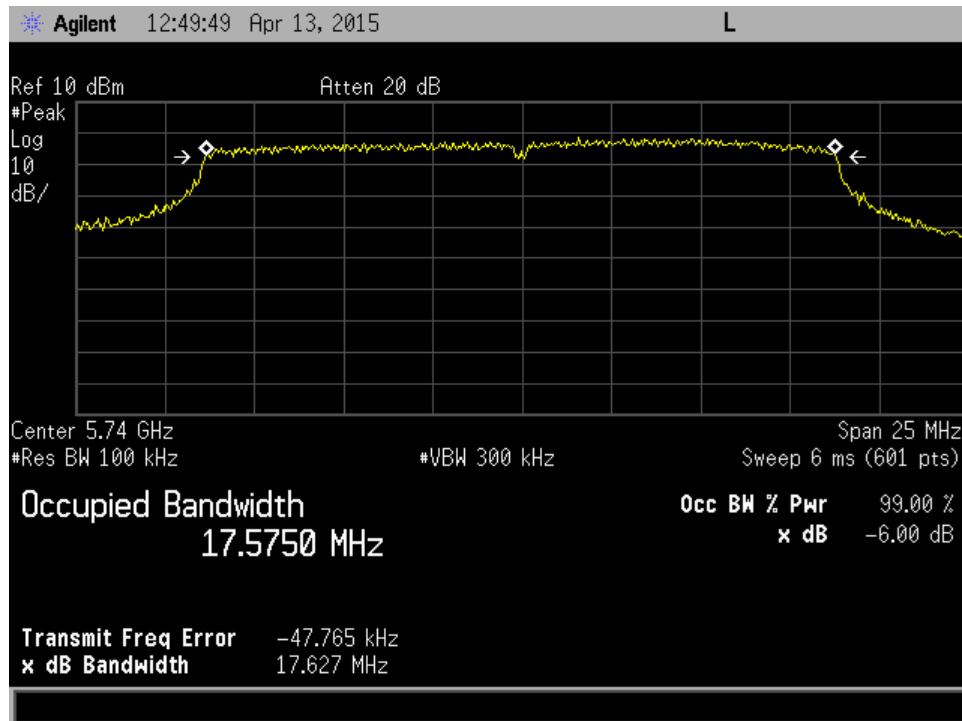
10MHz mid ch_J4



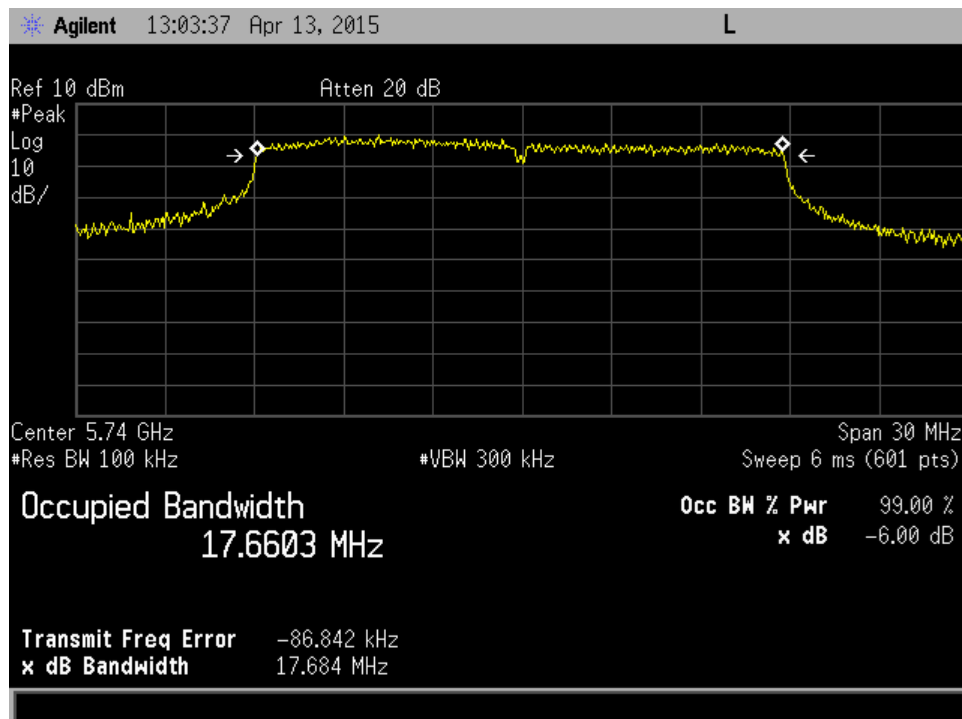
20MHz high ch_J3



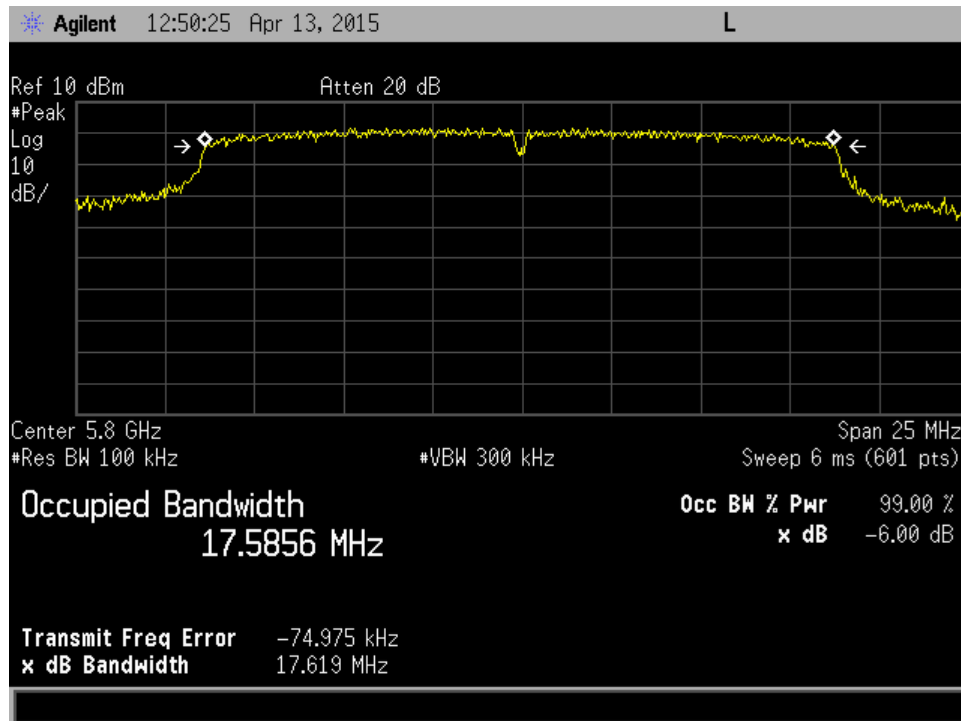
20MHz high ch_J4



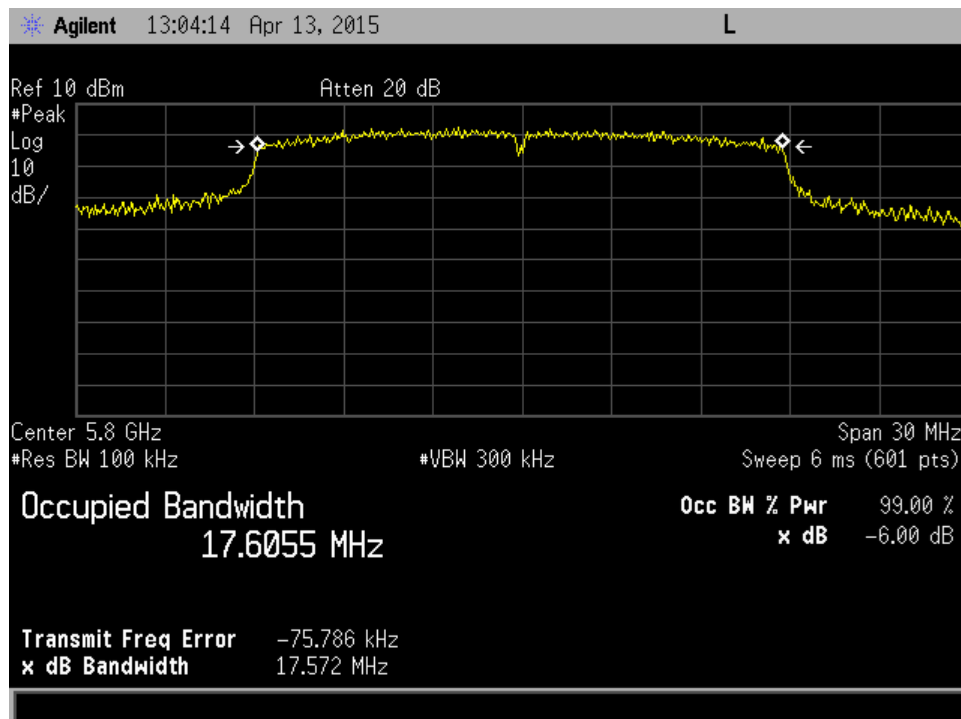
20MHz low ch_J3



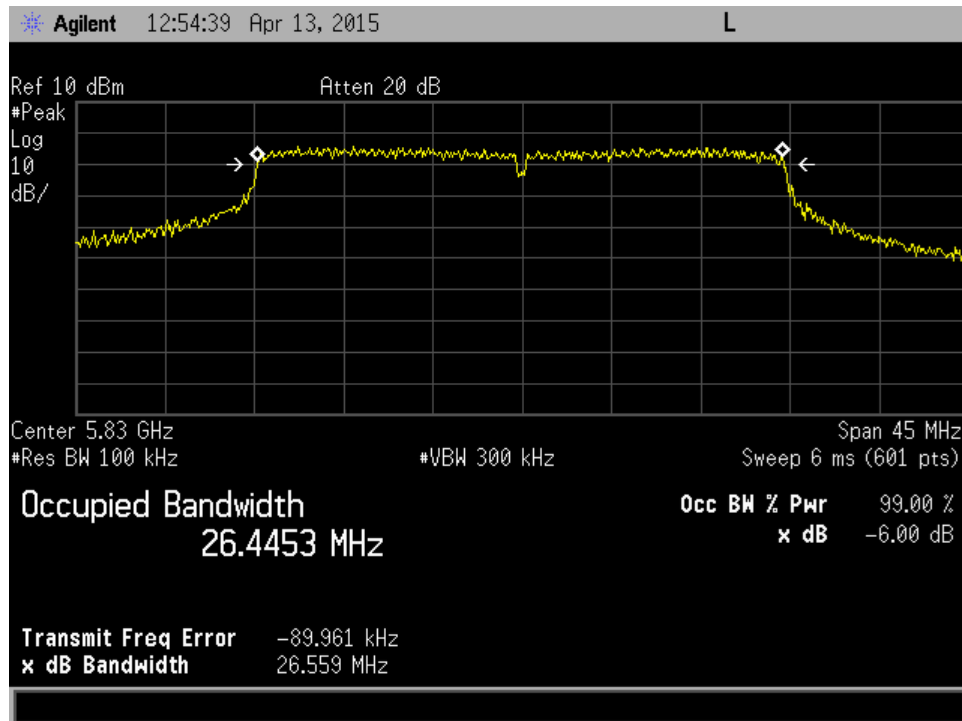
20MHz low ch_J4



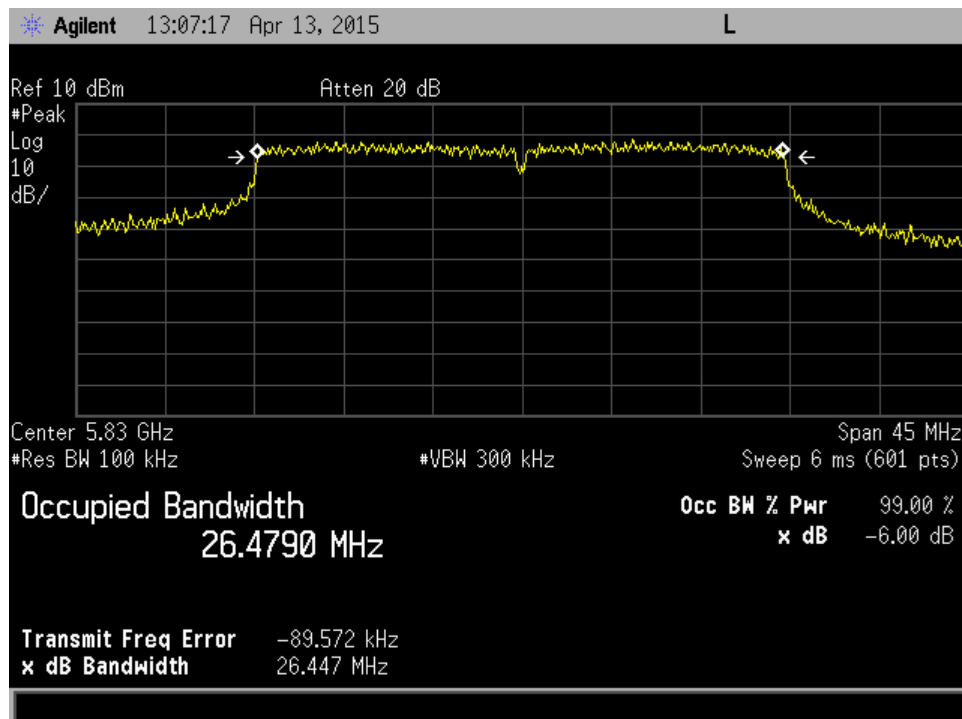
20MHz mid ch_J3



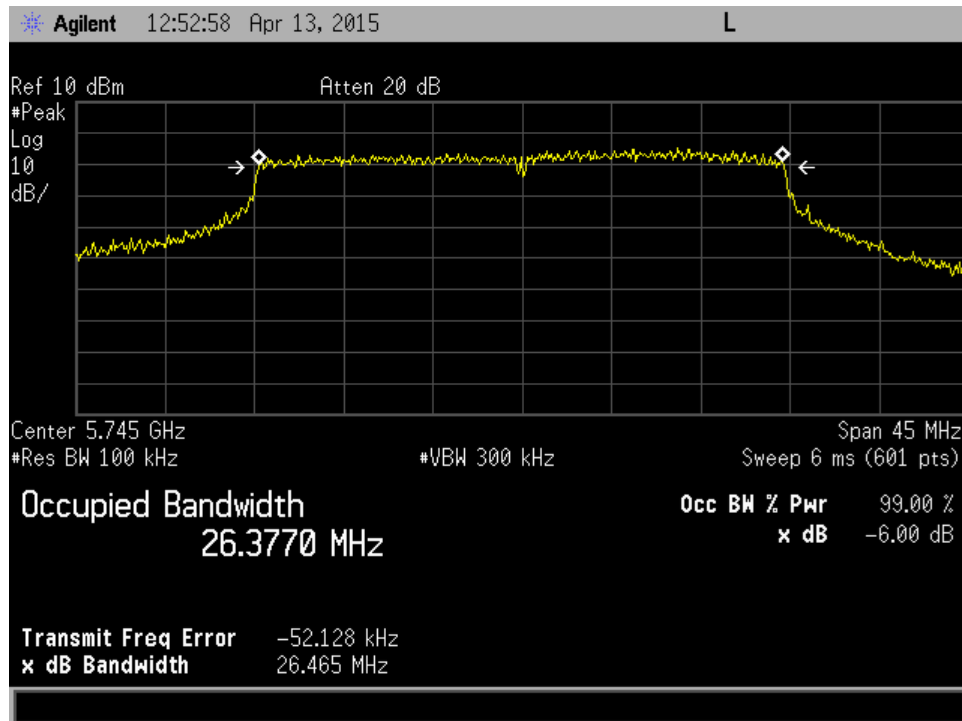
20MHz mid ch_J4



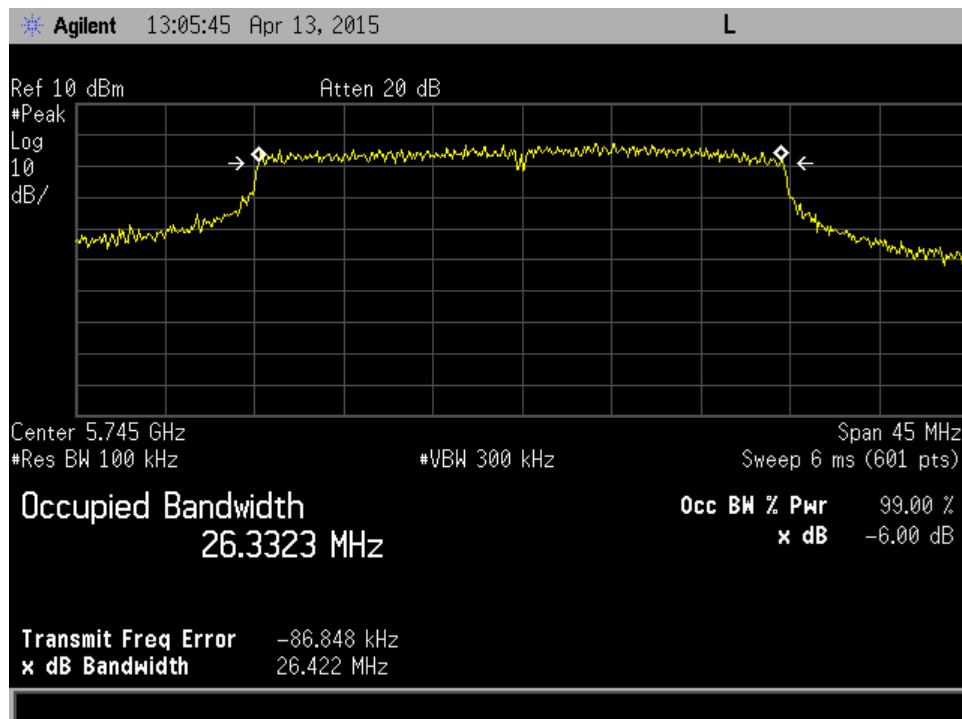
30MHz high ch_J3



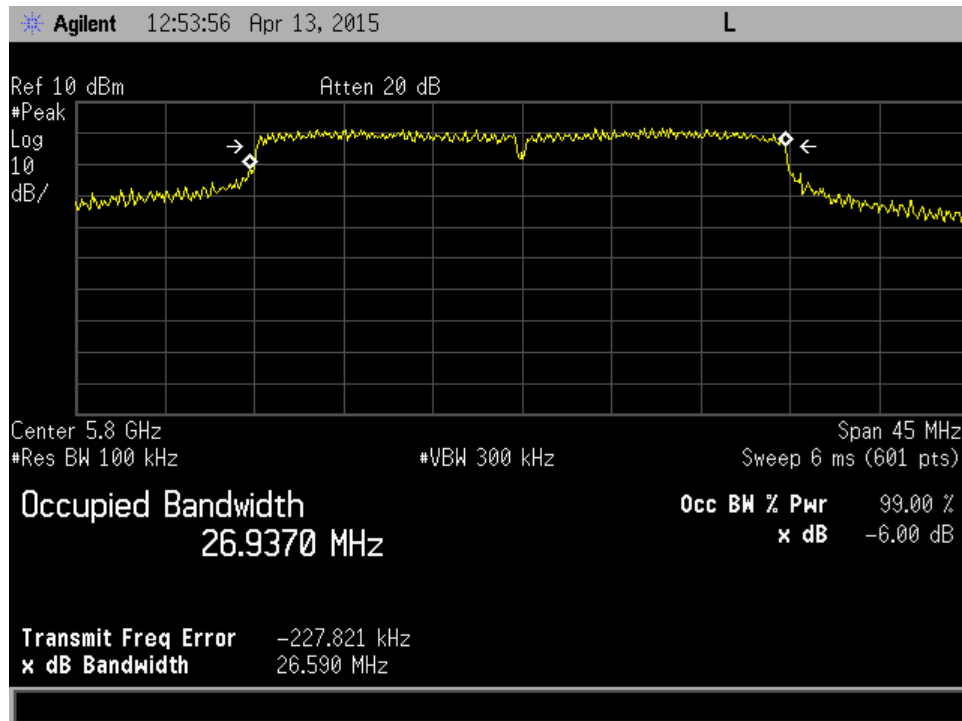
30MHz high ch_J4



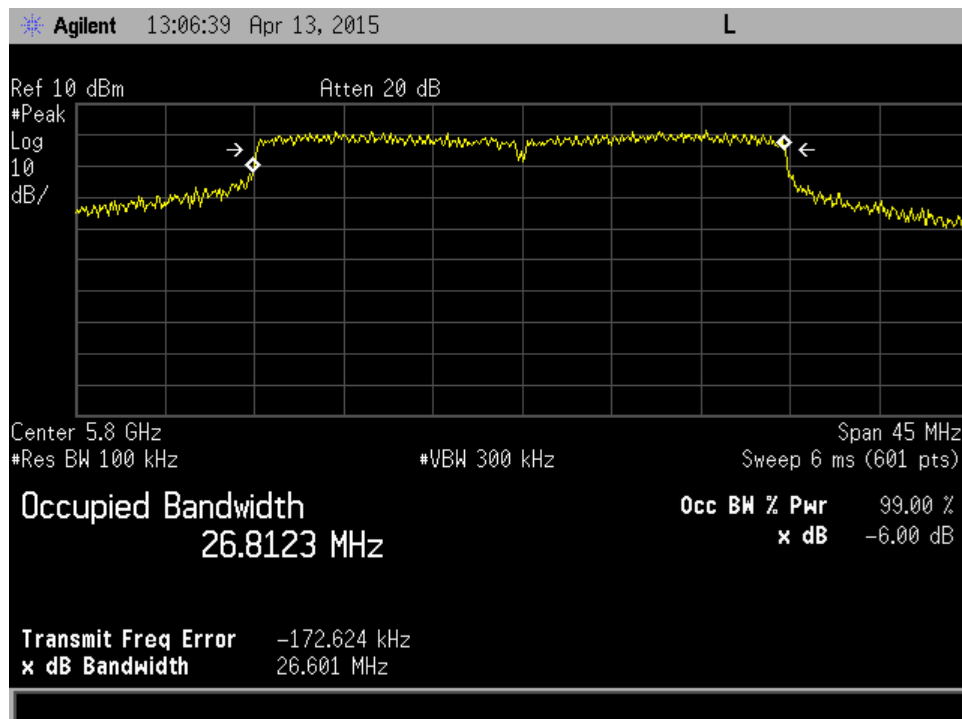
30MHz low ch_J3



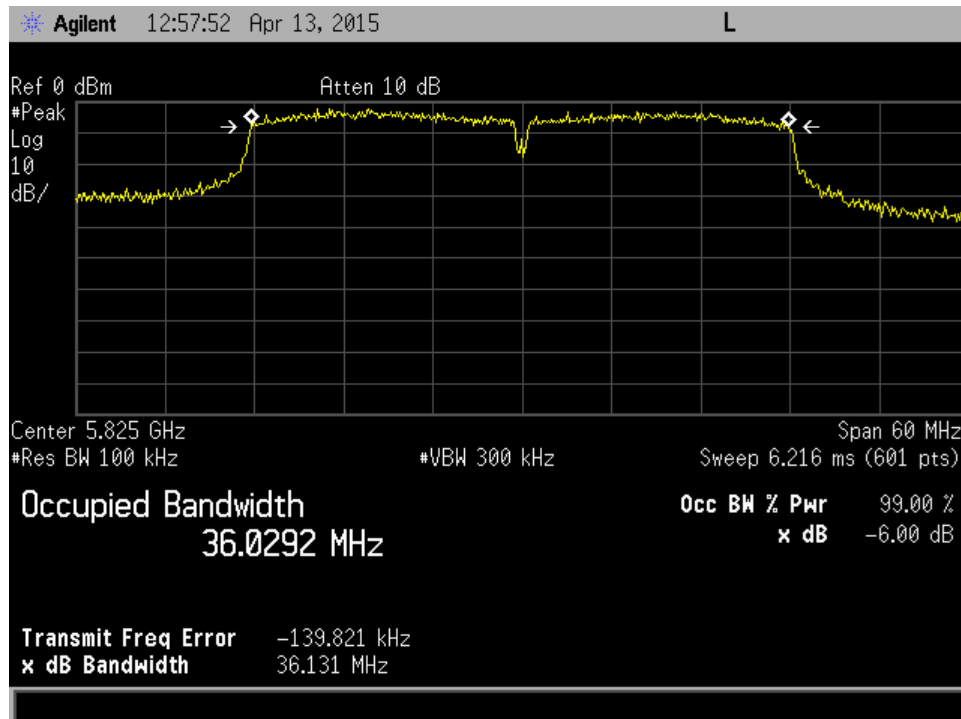
30MHz low ch_J4



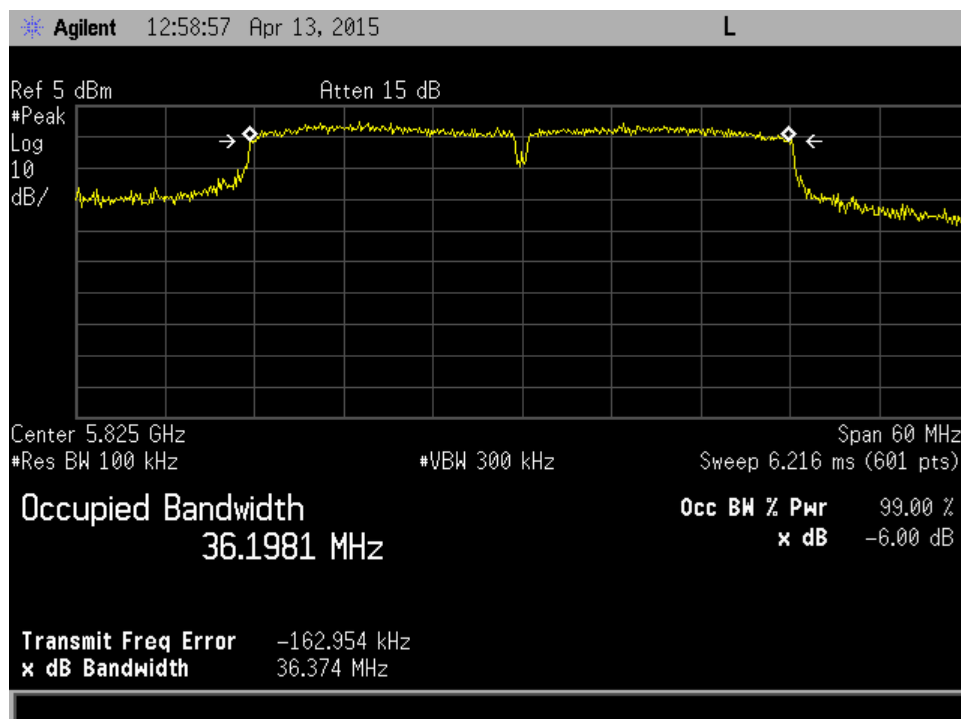
30MHz mid ch_J3



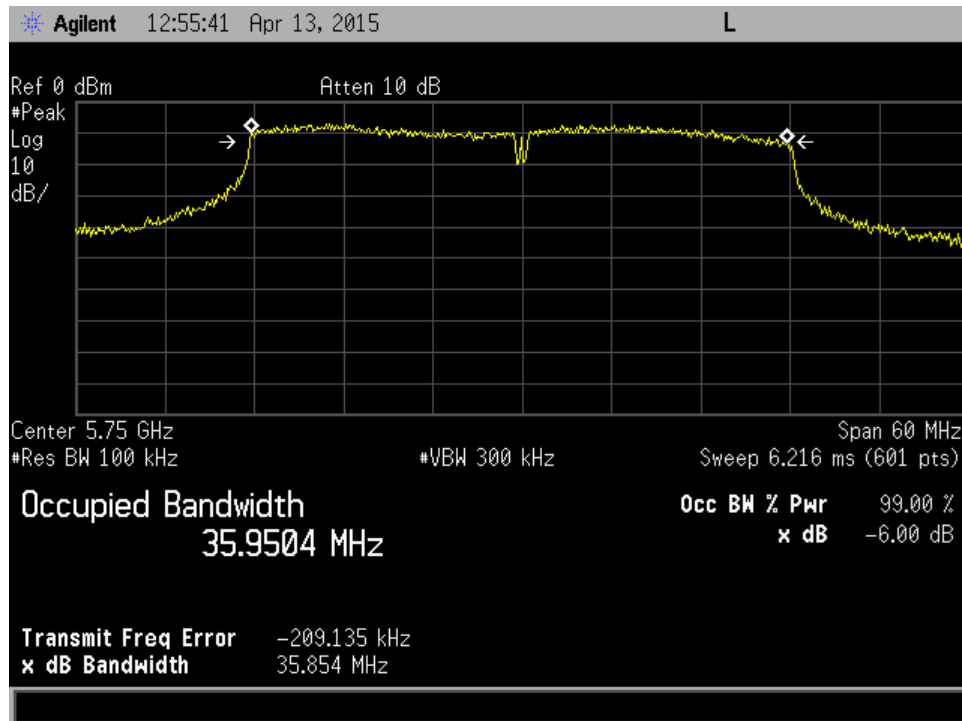
30MHz mid ch_J4



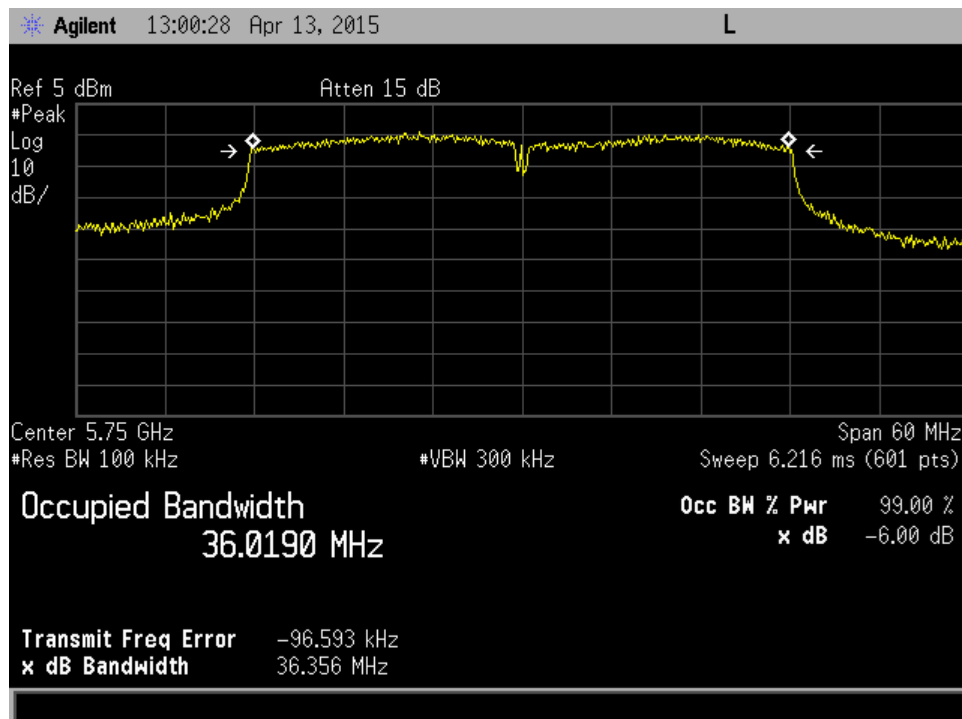
40MHz high ch_J3



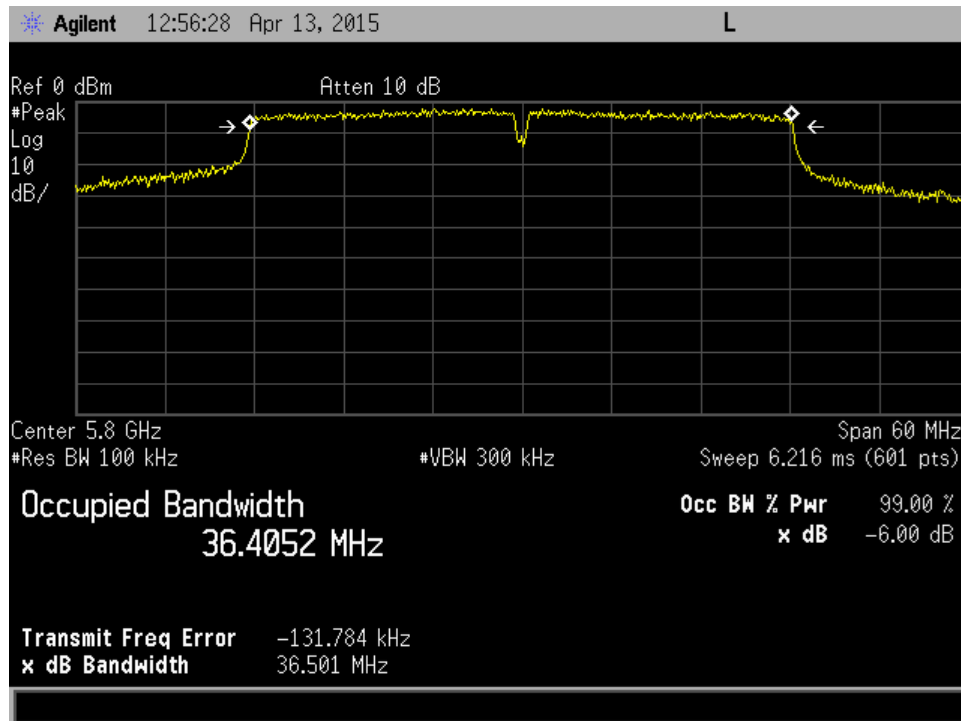
40MHz high ch_J4



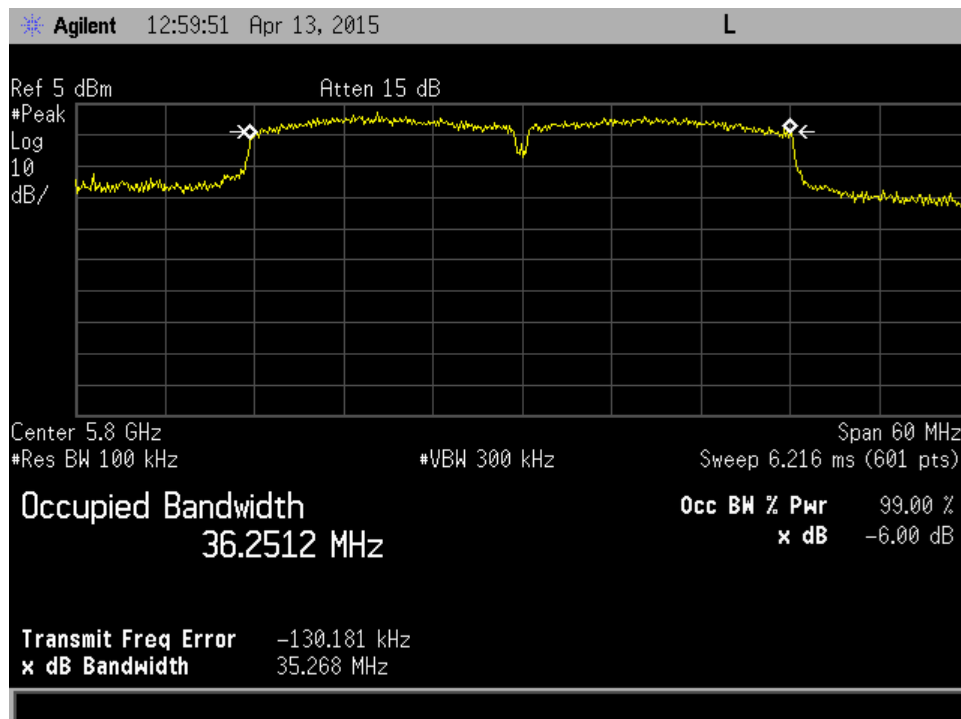
40MHz low ch_J3



40MHz low ch_J4



40MHz mid ch_J3



40MHz mid ch_J4