

PARAMETER			MIN	TYP	MAX	UNIT
Receiver	Noise figure	60 to 64 GHz		12		dB
	1-dB compression point (Out Of Band) ⁽¹⁾			−12		dBm
	Maximum gain			48		dB
	Gain range			18		dB
	Gain step size			2		dB
	IF bandwidth ⁽²⁾				10	MHz
	ADC sampling rate (real)				25	Msps
	ADC sampling rate (complex 1x)				12.5	Msps
	ADC resolution			12		Bits
	Idle Channel Spurs			−90		dBFS
Transmitter	Output power			12		dBm
	Power backoff range			26		dB
Clock subsystem	Frequency range		60		64	GHz
	Ramp rate				250	MHz/μs
	Phase noise at 1-MHz offset	60 to 64 GHz		−93		dBc/Hz

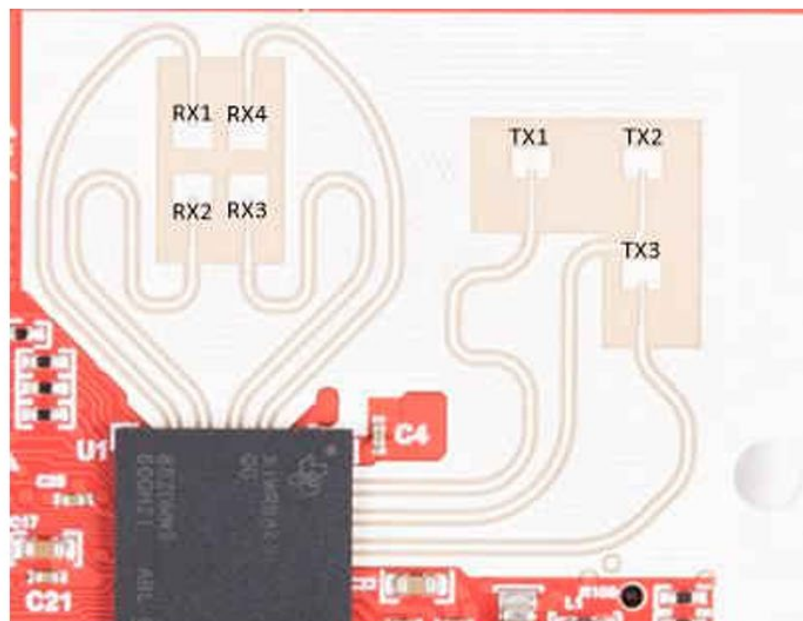
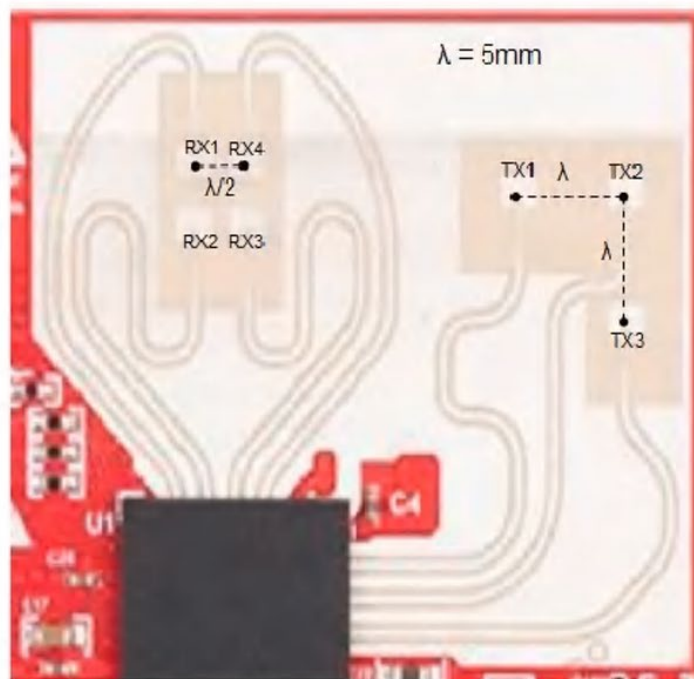
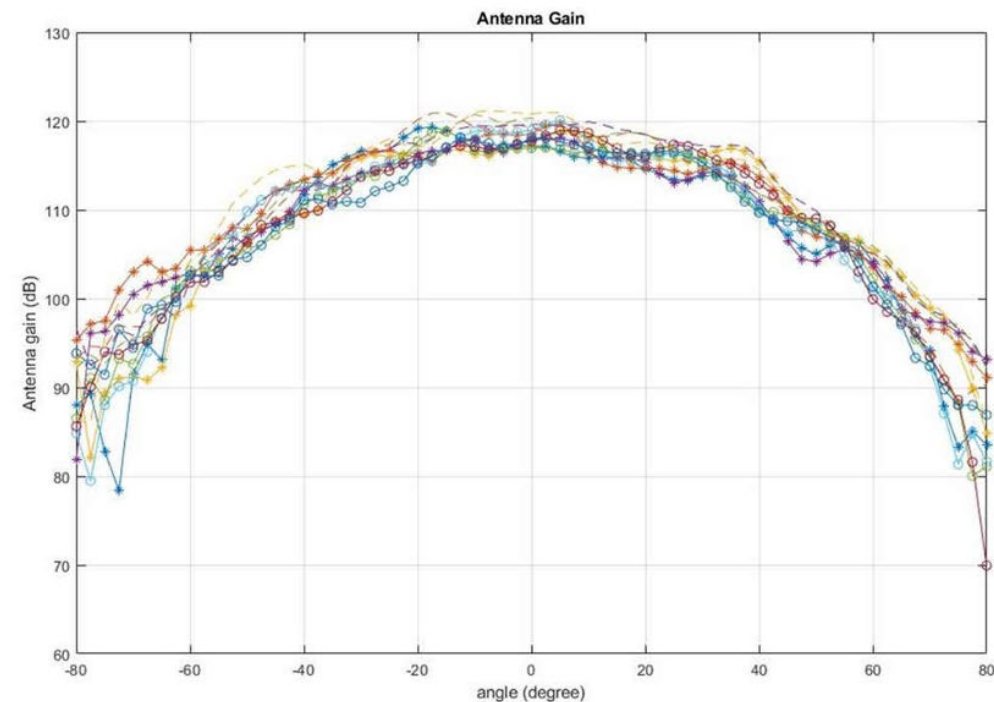
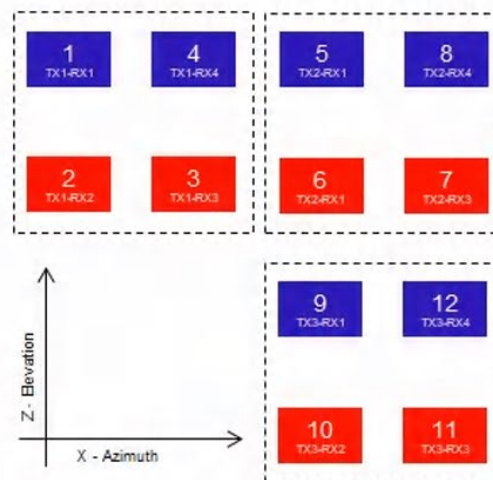


Figure 3-16. IWR6843ISK-ODS PCB Antenna

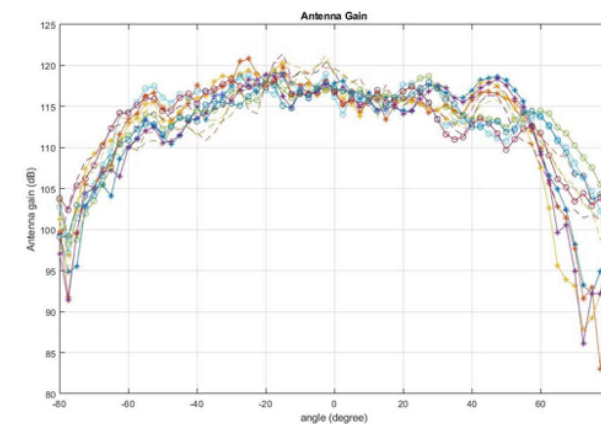


MIMO Virtual Antenna Array



RX2 and RX3 are 180° out of phase with respect to RX1 and RX4. Because of this, a 180° phase inversion needs to be applied in software processing for the corresponding virtual RX channels (highlighted in Red)

Measured Azimuthal Radiation Pattern for All Tx to Rx Pairs (All 12 Virtual Antenna Pairs Included)



Measured Elevation Radiation Pattern for All Tx to Rx Pairs (All 12 Virtual Antenna Pairs Included)